

mprj_io[6]_ser_tx mprj_io[3]_CSB mprj_io[2]_SD Serial Display Memory testing mprj_io[1]_SDO mprj_io[0]

Caravel Power Pin Headers

24 vccd1 25 vd. 25 vdda2

Interfaces

mprj_io[29] 12

mprj_io[27] 14

mprj_io[26] 15

mprj_io[24] 17

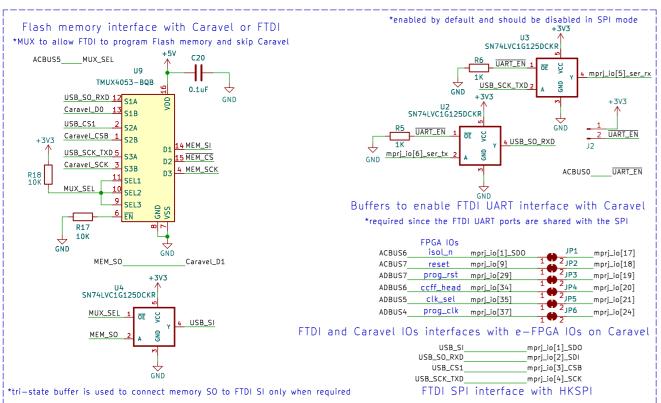
mprj_io[22] 19

mprj_io[18] 23 mprj_io[17] 24

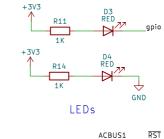
16 vdda1

18 19 xclk

20 gpio 21 vccd2 22 >+1/8



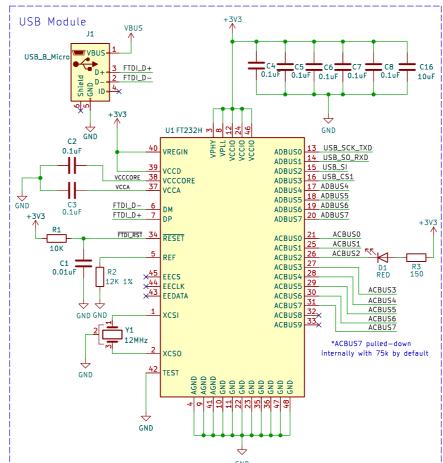
Clear REV 1B



SW_Push - SKQGADE010

Reset Switch

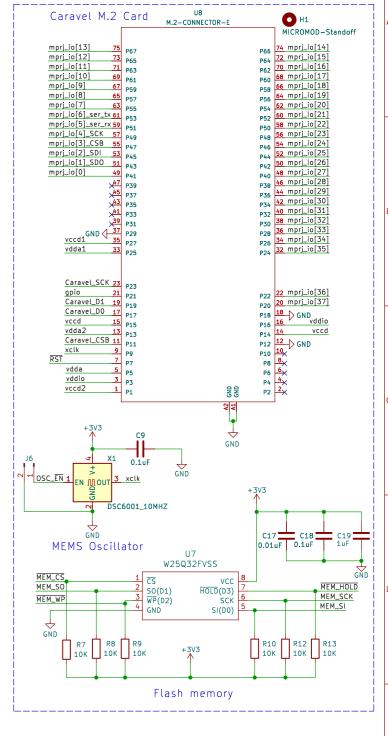
USB to Serial



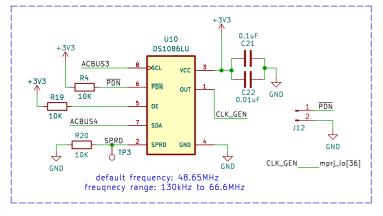
FTDI Module

*ACBUS8-9 are not configurable as GPIOs in MPSSE mode

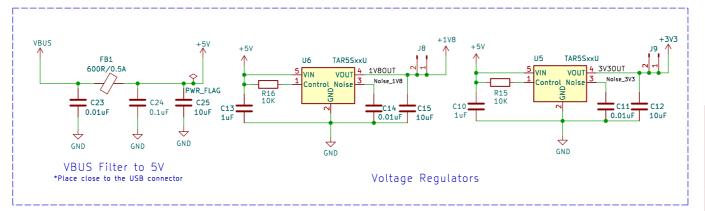
M.2 Connector + Peripherals



Clock Generator



Power



Efabless File: clear-dev-v1.kicad_sch Title: Clear Chip Development Board Size: A3 | Date: KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1 Rev: REV 1B