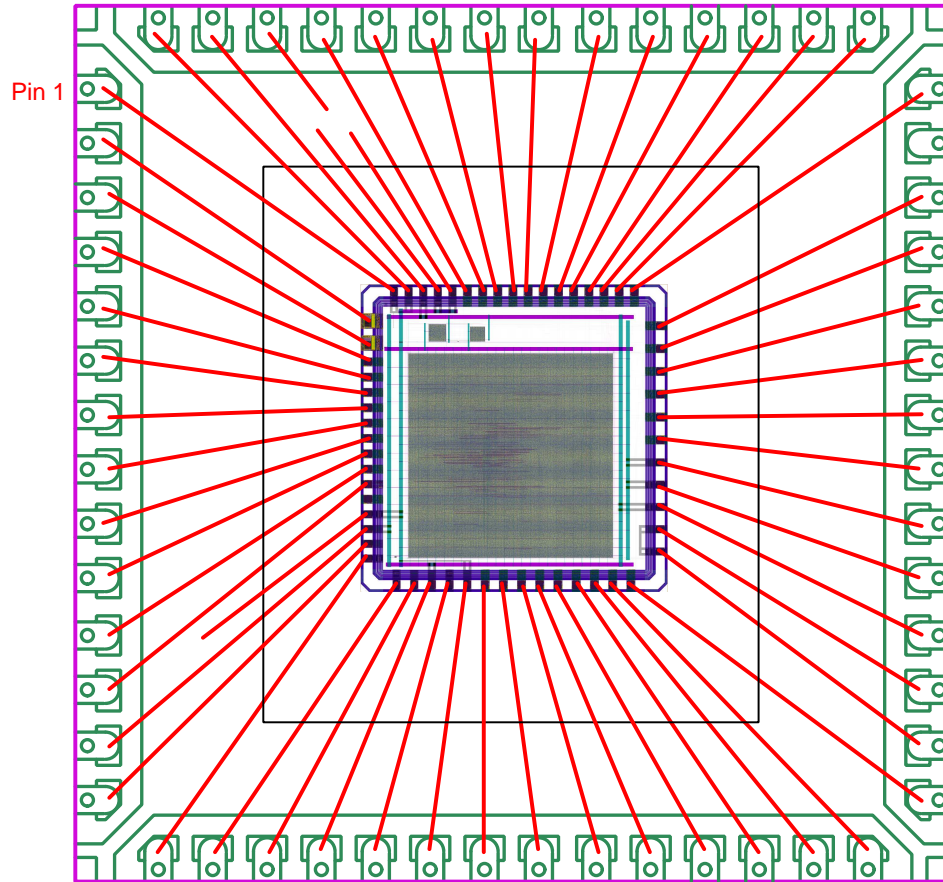striVe wirebond in 56-pin QFN

Pin 1

package size = 8mm × 8mm

paddle size = 6.63mm

pin pitch = 0.5mm

striVe padframe dimensions = 2.754 mm × 2.762 mm

striVe die dimensions = 4.430 mm × 4.945 mm

VDD3V3	1
ser_rx	2
ser_tx	3
flash_io3	4
flash_io2	5
flash_io1	6
flash_io0	7
flash_clk	8
xclk	9
SCK	10
CSB	11
RSTB	12
VDD1V8	13
VDD3V3	14

Top pins: 56 55 54 53 52 51 50 49 48 47 46 45 44 43
VDD3V3 VDD1V8 VSS gpio[8] gpio[7] gpio[6] gpio[5] gpio[15] gpio[14] gpio[13] gpio[12] irq SDO SDI

efabless
STRIVE
RISC-V 32-bit microprocessor

56 QFN
(view from top)

paddle = VSS

42	flash_csb
41	N/C
40	gpio[11]
39	gpio[0]
38	gpio[1]
37	gpio[2]
36	gpio[3]
35	gpio[4]
34	VSS
33	VSS
32	VDD1V8
31	VDD3V3
30	VDD3V3
29	gpio[9]

Bottom pins: 15 16 17 18 19 20 21 22 23 24 25 26 27 28
VDD3V3 VDD3V3 VDD3V3 VDD1V8 VSS VSS N/C N/C N/C N/C N/C xo xi gpio[10]