# 1. Description

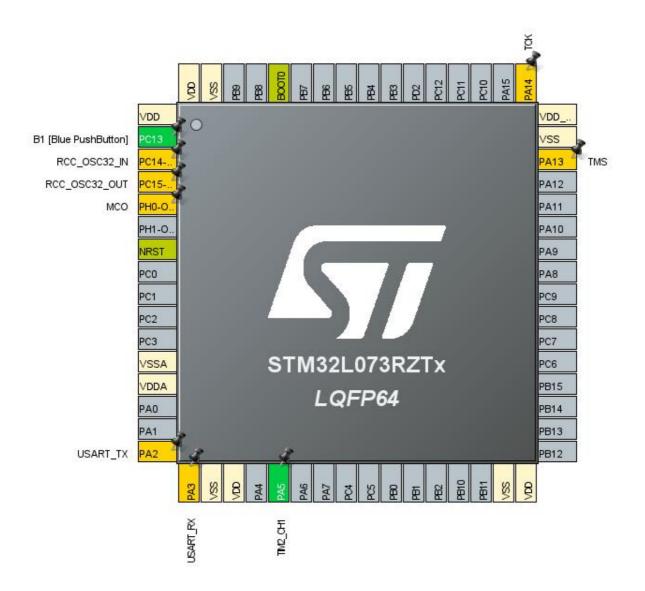
## 1.1. Project

Project Name	project15
Board Name	NUCLEO-L073RZ
Generated with:	STM32CubeMX 5.4.0
Date	01/03/2020

#### 1.2. MCU

MCU Series	STM32L0
MCU Line	STM32L0x3
MCU name	STM32L073RZTx
MCU Package	LQFP64
MCU Pin number	64

# 2. Pinout Configuration

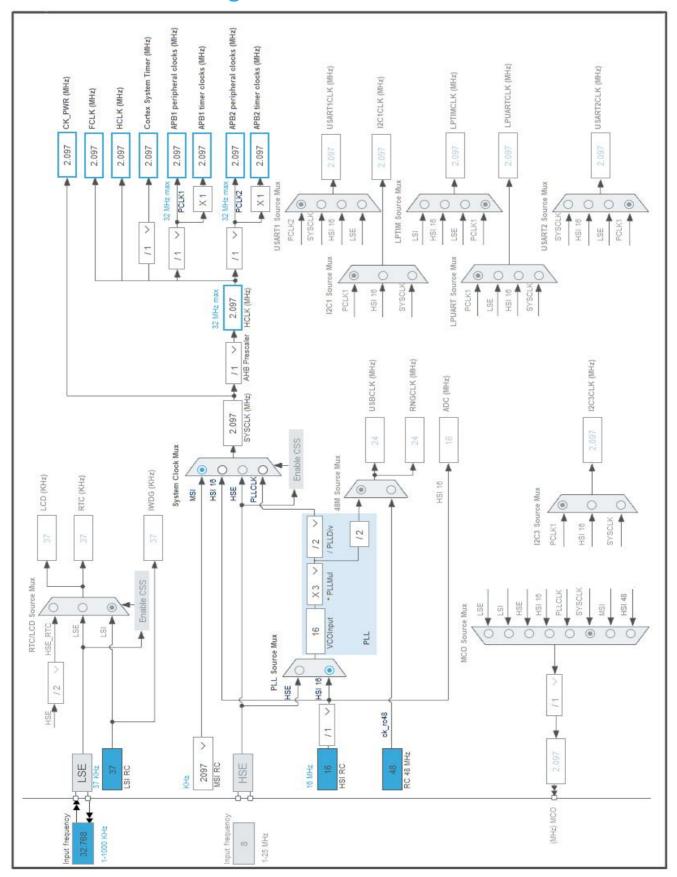


# 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PC13	I/O	GPIO_EXTI13	B1 [Blue PushButton]
3	PC14-OSC32_IN *	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT *	I/O	RCC_OSC32_OUT	
5	PH0-OSC_IN *	I/O	RCC_OSC_IN	MCO
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
16	PA2 *	I/O	USART2_TX	USART_TX
17	PA3 *	I/O	USART2_RX	USART_RX
18	VSS	Power		
19	VDD	Power		
21	PA5	I/O	TIM2_CH1	
31	VSS	Power		
32	VDD	Power		
46	PA13 *	I/O	SYS_SWDIO	TMS
47	VSS	Power		
48	VDD_USB	Power		
49	PA14 *	I/O	SYS_SWCLK	тск
60	воото	Boot		
63	VSS	Power		
64	VDD	Power		

<sup>\*</sup> The pin is affected with a peripheral function but no peripheral mode is activated

## 4. Clock Tree Configuration



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# 5. Software Project

## 5.1. Project Settings

Name	Value		
Project Name	project15		
Project Folder	F:\CubeIDE_workspace\project15		
Toolchain / IDE	STM32CubeIDE		
Firmware Package Name and Version	STM32Cube FW_L0 V1.11.2		

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	

# 6. Power Consumption Calculator report

#### 6.1. Microcontroller Selection

Series	STM32L0
Line	STM32L0x3
MCU	STM32L073RZTx
Datasheet	027096_Rev3

#### 6.2. Parameter Selection

Temperature	25
Vdd	3.0

# 7. IPs and Middleware Configuration 7.1. GPIO

#### 7.2. RCC

#### 7.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3

Buffer Cache Enabled

Prefetch Disabled

Preread Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

MSI Calibration Value 0

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 7.3. SYS

**Timebase Source: SysTick** 

#### 7.4. TIM2

**Channel1: PWM Generation CH1** 

#### 7.4.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 0
Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 0xFFFF \*
Internal Clock Division (CKD) No Division
auto-reload preload Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

#### **PWM Generation Channel 1:**

Mode PWM mode 1

Pulse (16 bits value) 0
Output compare preload Enable
Fast Mode Disable
CH Polarity High

<sup>\*</sup> User modified value

# 8. System Configuration

## 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
TIM2	PA5	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
Single Mapped	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
Signals	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	MCO
	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	USART_TX
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	USART_RX
	PA13	SYS_SWDIO	n/a	n/a	n/a	TMS
	PA14	SYS_SWCLK	n/a	n/a	n/a	тск
GPIO	PC13	GPIO_EXTI13	External Interrupt Mode with Falling edge trigger detection	No pull-up and no pull-down	n/a	B1 [Blue PushButton]

## 8.2. DMA configuration

nothing configured in DMA service

## 8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable Interrupt	true	0	0	
Hard fault interrupt	true	0	0	
System service call via SWI instruction	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	0	0	
PVD interrupt through EXTI line 16	unused			
Flash and EEPROM global interrupt	unused			
RCC and CRS global interrupt	unused			
EXTI line 4 to 15 interrupts	unused			
TIM2 global interrupt	unused			

<sup>\*</sup> User modified value

# 9. Software Pack Report