

Reconstruction of FIRs and IIRs from Faust signals

Yann Orlarey

EMERAUDE

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Introduction

Motivations

- Improve code generation for SYFALA by recreating loops
- Facilitate WCPG calculation for IIRs

Agenda

- Faust signals and their properties
- FIR and IIR extensions
- Rewriting rules
- Code Generation

PART 1: Faust Signals and their properties



Faust Signals Terms \mathbb{S}

The set \mathbb{S} is inductively generated by the following grammar:

Signal Terms

$$s \in \mathbb{S} ::= k \mid u \mid I_n \mid \star(s_1, s_2, \dots) \mid s_1 @ s_2 \mid X_i$$

Where:

$r1 : k : \text{constant number} \in \mathbb{R}$

$r2 : u : \text{user interface element (slider, button, etc.)}$

$r3 : I_n : \text{audio input channel } n$

$r4 : \star(s_1, \dots) : \text{numerical expressions } (\star \in \{+, *, \cos, \dots\})$

$r5 : s_1 @ s_2 : \text{signal } s_1 \text{ delayed by } s_2 \text{ samples}$

$r6 : X : \text{symbol representing a tuple of mutually recursive signals}$

$r7 : X_i : \text{the } i\text{-th projection of } X$

Moreover, $\text{def}[X] \in \mathbb{S}^n$ represent the definition associated with X , and by extension $\text{def}[X_i] = \text{def}[X][i]$.

Semantics of Faust Signals

A Faust signal $s \in \mathbb{S}$ denotes a function of time, notated $\llbracket s \rrbracket : \mathbb{Z} \rightarrow \mathbb{R}$. Time can be negative, but computation really starts at $t = 0$. By definition, we have:

$t \geq 0$

$p1 : \llbracket k \rrbracket(t) = \text{the constant signal of value } k$

$p2 : \llbracket u \rrbracket = \text{the signal produced by acting on widget } u$

$p3 : \llbracket I_n \rrbracket = \text{the signal on audio input channel } n$

$p4 : \llbracket \star(s_1, \dots) \rrbracket(t) = \star(\llbracket s_1 \rrbracket(t), \dots)$

$p5 : \llbracket s_1 @ s_2 \rrbracket(t) = \llbracket s_1 \rrbracket(t - \llbracket s_2 \rrbracket(t))$

$p6 : \llbracket X_i \rrbracket = \llbracket \text{def} \llbracket X_i \rrbracket \rrbracket$

$t < 0$

$n1 : \llbracket s \rrbracket(t) = 0$

Additional Definitions

Constant Signals

A signal s is said *constant* if and only if: $\forall t_1, t_2 \in \mathbb{N}, \llbracket s \rrbracket(t_1) = \llbracket s \rrbracket(t_2)$.

Positive Signals

A signal s is said *positive* if and only if: $\forall t \in \mathbb{N}, \llbracket s \rrbracket(t) \geq 0$.

Causal Delays

A delay $s_1 @ s_2$ is said *causal* if and only if s_2 is *positive*. Non-causal delays would imply looking at the future of a signal and are not allowed in Faust. Therefore, for every delay expression $s_1 @ s_2$, $\llbracket s_2 \rrbracket(t) \geq 0$.

Mutual Recursions

Mutual recursions are only allowed within the same recursive group X , but not between recursive groups. Therefore, dependencies between recursive groups form a DAG.

Equivalence Principle

Equivalence of Signals

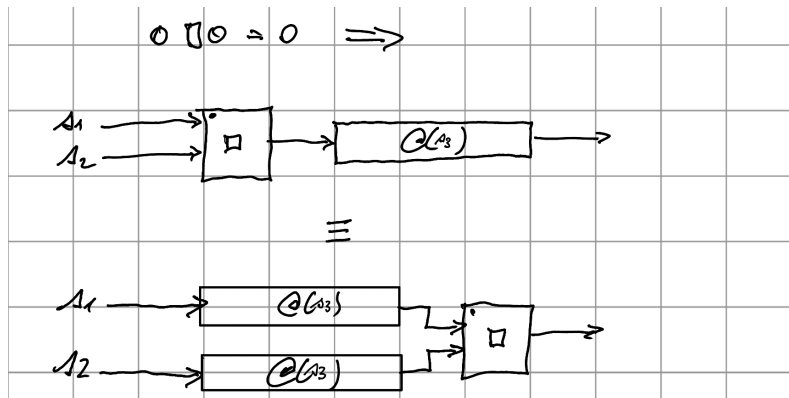
Two signals $s_1, s_2 \in \mathbb{S}$ are said *equivalent*, notated $s_1 \equiv s_2$, if and only if they have the same semantics

$$s_1 \equiv s_2 \Leftrightarrow \llbracket s_1 \rrbracket = \llbracket s_2 \rrbracket$$

Distributivity of the delay operation

The delay operation @ is distributive over *numerical operations that are null at origin* ($0 + 0 = 0$, $0 * 0 = 0$, etc.).

$$\star(s_1, s_2, \dots) @ s_3 \equiv \star(s_1 @ s_3, s_2 @ s_3, \dots)$$



Distributivity of the delay operation

We need to prove that $\llbracket \star(s_1, s_2, \dots) @ s_3 \rrbracket = \llbracket \star(s_1 @ s_3, s_2 @ s_3, \dots) \rrbracket$

Case 1: $t \geq \llbracket s_3 \rrbracket(t) \geq 0$

$$\begin{aligned}\llbracket \star(s_1, s_2, \dots) @ s_3 \rrbracket(t) &= \llbracket \star(s_1, s_2, \dots) \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= \star(\llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t)), \llbracket s_2 \rrbracket(t - \llbracket s_3 \rrbracket(t)), \dots) \\ &= \star(\llbracket s_1 @ s_3 \rrbracket(t), \llbracket s_2 @ s_3 \rrbracket(t), \dots) \\ &= \llbracket \star(s_1 @ s_3, s_2 @ s_3, \dots) \rrbracket(t)\end{aligned}$$

□

Distributivity of the delay operation

We need to prove that $\llbracket \star(s_1, s_2, \dots) @ s_3 \rrbracket = \llbracket \star(s_1 @ s_3, s_2 @ s_3, \dots) \rrbracket$

Case 2: $\llbracket s_3 \rrbracket(t) > t \geq 0$

$$\begin{aligned}\llbracket \star(s_1, s_2, \dots) @ s_3 \rrbracket(t) &= \llbracket \star(s_1, s_2, \dots) \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= 0\end{aligned}$$

$$\begin{aligned}\llbracket \star(s_1 @ s_3, s_2 @ s_3, \dots) \rrbracket(t) &= \star(\llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t)), \llbracket s_2 \rrbracket(t - \llbracket s_3 \rrbracket(t)), \dots) \\ &= \star(0, 0, \dots) \\ &= 0\end{aligned}$$



Other Equivalence Properties of Faust Signals

The following equivalence properties are useful for the reconstruction of FIRs and IIRs in Faust signals:

Equivalence 1

$$(s_1 @ s_2) @ s_3 \equiv s_1 @ (s_2 @ s_3 + s_3)$$

Equivalence 2

$$(s_1 @ k) @ s_3 \equiv s_1 @ (k + s_3)$$

Equivalence 3

$$(s_1 @ s_2) * k \equiv (s_1 * k) @ s_2$$

Proof of Equivalence 1 : $(s_1 @ s_2) @ s_3 \equiv s_1 @ (s_2 @ s_3 + s_3)$

We prove that $\forall t, \llbracket (s_1 @ s_2) @ s_3 \rrbracket(t) = \llbracket s_1 @ (s_2 @ s_3 + s_3) \rrbracket(t)$:

Proof

$$\begin{aligned}\llbracket (s_1 @ s_2) @ s_3 \rrbracket(t) &= \llbracket s_1 @ s_2 \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - \llbracket s_2 \rrbracket(t - \llbracket s_3 \rrbracket(t))) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - \llbracket s_2 @ s_3 \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 + s_2 @ s_3 \rrbracket(t)) \\ &= \llbracket s_1 @ (s_3 + s_2 @ s_3) \rrbracket(t) \\ &= \llbracket s_1 @ (s_2 @ s_3 + s_3) \rrbracket(t)\end{aligned}$$

□

Proof of Equivalence 2 : $(s_1 @ k) @ s_3 \equiv s_1 @ (k + s_3)$

We prove that $\forall t, \llbracket (s_1 @ k) @ s_3 \rrbracket(t) = \llbracket s_1 @ (k + s_3) \rrbracket(t)$ in two cases, when $t - \llbracket s_3 \rrbracket(t) \geq 0$ and when $t - \llbracket s_3 \rrbracket(t) < 0$

Case 1: $t - \llbracket s_3 \rrbracket(t) \geq 0$

$$\begin{aligned}\llbracket (s_1 @ k) @ s_3 \rrbracket(t) &= \llbracket s_1 @ k \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - \llbracket k \rrbracket(t - \llbracket s_3 \rrbracket(t))) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - k) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - \llbracket k \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 + k \rrbracket(t)) \\ &= \llbracket s_1 @ (s_3 + k) \rrbracket(t)\end{aligned}$$

□

Proof of Equivalence 2 : $(s_1 @ k) @ s_3 \equiv s_1 @ (k + s_3)$

We prove that, when $t - \llbracket s_3 \rrbracket(t) < 0$, $\llbracket (s_1 @ k) @ s_3 \rrbracket(t) = 0$ and $\llbracket s_1 @ (k + s_3) \rrbracket(t) = 0$

Case 2a: $t - \llbracket s_3 \rrbracket(t) < 0 \Rightarrow \llbracket (s_1 @ k) @ s_3 \rrbracket(t) = 0$

$$\begin{aligned}\llbracket (s_1 @ k) @ s_3 \rrbracket(t) &= \llbracket s_1 @ k \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - \llbracket k \rrbracket(t - \llbracket s_3 \rrbracket(t))) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t) - 0) \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_3 \rrbracket(t)) \\ &= 0\end{aligned}$$
□

Case 2b: $t - \llbracket s_3 \rrbracket(t) < 0 \Rightarrow \llbracket s_1 @ (k + s_3) \rrbracket(t) = 0$

$$\begin{aligned}\llbracket s_1 @ (k + s_3) \rrbracket(t) &= \llbracket s_1 \rrbracket(t - \llbracket k + s_3 \rrbracket(t)) \\ &= \llbracket s_1 \rrbracket(t - \llbracket k \rrbracket(t) - \llbracket s_3 \rrbracket(t)) \\ &= 0\end{aligned}$$
□

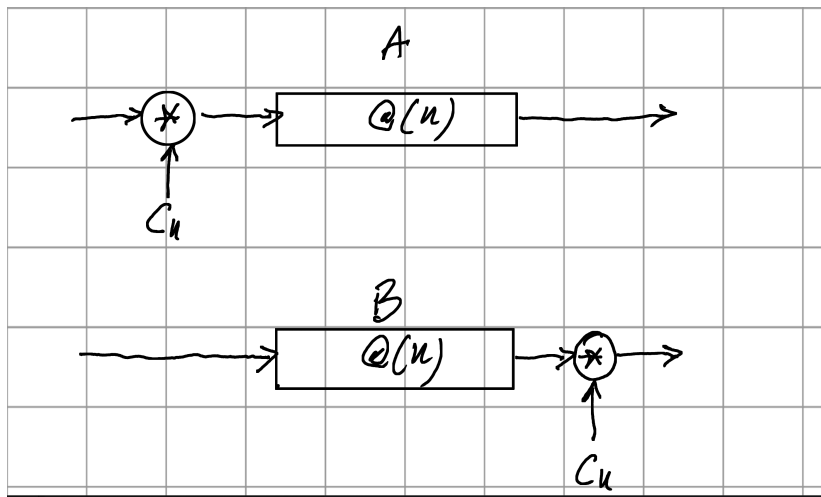
Proof of Equivalence 3 : $(s_1 @ s_2) * k \equiv (s_1 * k) @ s_2$

We prove that $\forall t, \llbracket (s_1 @ s_2) * k \rrbracket(t) = \llbracket (s_1 * k) @ s_2 \rrbracket(t)$

Proof

$$\begin{aligned}\llbracket (s_1 @ s_2) * k \rrbracket(t) &= \llbracket s_1 @ s_2 \rrbracket(t) * \llbracket k \rrbracket(t) \\ &= \llbracket s_1 @ s_2 \rrbracket(t) * k \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_2 \rrbracket(t)) * k \\ &= \llbracket s_1 \rrbracket(t - \llbracket s_2 \rrbracket(t)) * \llbracket k \rrbracket(t - \llbracket s_2 \rrbracket(t)) \\ &= \llbracket s_1 * k \rrbracket(t - \llbracket s_2 \rrbracket(t)) \\ &= \llbracket (s_1 * k) @ s_2 \rrbracket(t)\end{aligned}$$
□

Proof of Equivalence 3 : $(s_1 @ n) * c_n \equiv (s_1 * c_n) @ n$



Proof of Equivalence 3 : $(s_1 @ n) * c_n \equiv (s_1 * c_n) @ n$

Relaxed equivalence

$$c_n \text{ constant} \Rightarrow \llbracket A \rrbracket = \llbracket B \rrbracket$$

$$c_n \text{ variable} \Rightarrow \llbracket A \rrbracket \neq \llbracket B \rrbracket$$

$$c_n \text{ slow and } n \text{ small} \Rightarrow \llbracket A \rrbracket \approx \llbracket B \rrbracket$$

PART 2: FIR and IIR Extensions



Extension of Faust Signals with IIRs and FIRs

We extend the signal grammar with two new terms: FIR and IIR.

Extended Signals \mathbb{S}_e

$$\begin{aligned} s, c \in \mathbb{S}_e ::= & k \mid u \mid \mathbf{I}_n \mid \star(s_1, \dots) \mid s_1 @ s_2 \mid X_i \\ & \mid \text{FIR}(s, c_0, c_1, \dots, c_n) \\ & \mid \text{IIR}(X_i, s, c_0, c_1, \dots, c_n) \end{aligned}$$

Definition of FIR and IIR Signals

FIR and IIR are defined as follows:

$\text{FIR}(s, c_0, c_1, c_2 \dots)$

$$c_0 * s + c_1 * s@1 + c_2 * s@2 + \dots$$

$\text{IIR}(X_i, s, c_0, c_1, c_2, \dots)$

$$\text{def}[[X_i]] = s + c_1 * X_i@1 + c_2 * X_i@2 + \dots$$

(usually $c_0 = 0$)

PART 3: Reconstruction Rewriting Rules



FIR Rewriting Rules

Goal

$$(c_0 * s + c_1 * s@1 + c_2 * s@2 + \dots) \rightarrow \text{FIR}(s, c_0, c_1, c_2 \dots)$$

Rules

$$w1 : s@k \rightarrow \text{FIR}(s, 0, \dots^k, 1)$$

$$w2 : \text{FIR}(s, c_0, c_1, \dots)@k \rightarrow \text{FIR}(s, 0, \dots^k, c_0, c_1, \dots) \quad c_i \text{ constant}$$

$$w3 : c * \text{FIR}(s, c_0, c_1, \dots) \rightarrow \text{FIR}(s, c * c_0, c * c_1, \dots)$$

$$w4 : \text{FIR}(s, c_0, c_1, \dots) + \text{FIR}(s, c'_0, c'_1, \dots) \rightarrow \text{FIR}(s, c_0 + c'_0, c_1 + c'_1, \dots)$$

$$w5 : \text{FIR}(s_1, c_0, c_1, \dots) + \text{FIR}(s_2, c_0, c_1, \dots) \rightarrow \text{FIR}(s_1 + s_2, c_0, c_1, \dots)$$

$$w6 : \text{FIR}(s_1, c_0, c_1, \dots) + \text{FIR}(s_2, -c_0, -c_1, \dots) \rightarrow \text{FIR}(s_1 - s_2, c_0, c_1, \dots)$$

$$w7 : c * s + \text{FIR}(s, c_0, c_1, \dots) \rightarrow \text{FIR}(s, c + c_0, c_1, \dots)$$

Where $0, \dots^k$ means 0 repeated k times. For example: $0, \dots^3 = 0, 0, 0$

IIR Rewriting Rules

Goal

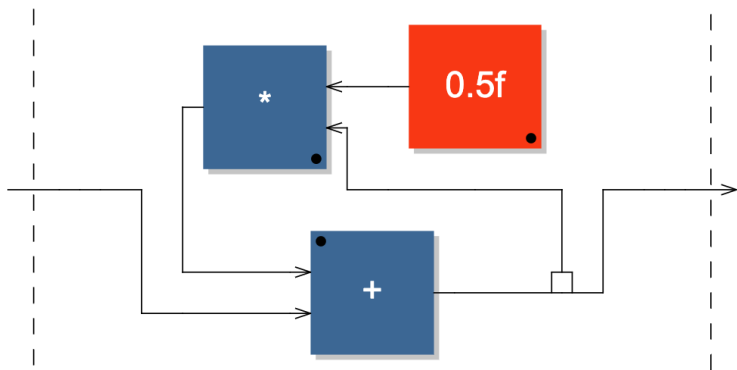
$$\text{def}[[X_i]] = s + c_1 * X_i@1 + c_2 * X_i@2 + \dots \rightarrow \text{IIR}(X_i, s, 0, c_1, c_2\dots)$$

Rules

$$w8 : s_1 + \text{FIR}(X_i, 0, c_1, \dots) \rightarrow \text{IIR}(X_i, s_1, 0, c_1, \dots) \quad (X_i \notin s_1)$$

$$w9 : s \rightarrow \emptyset$$

Example 1: `process = + ~ *(0.5);`

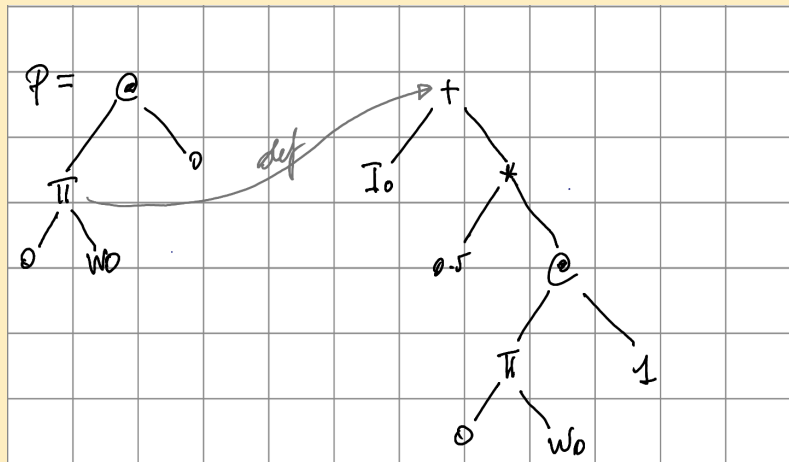


FIR and IIR Representation

`IIR[VOID, IN[0], 0.0f, 0.5f]`

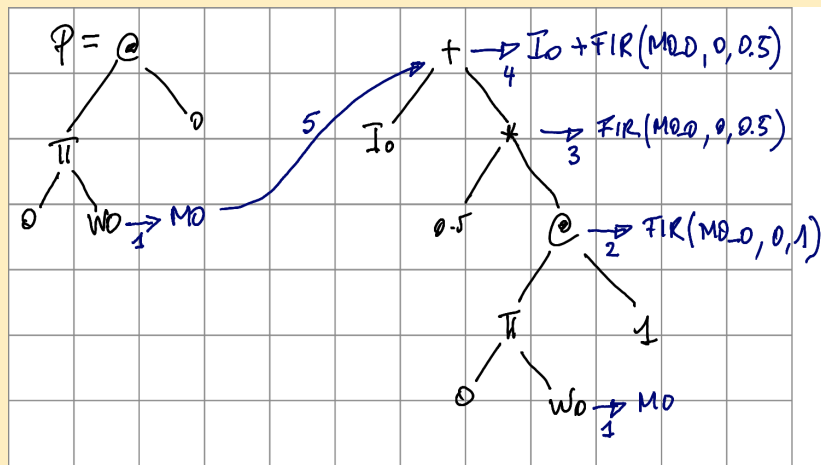
Example 1: process = + ~ *(0.5);

Signal Representation



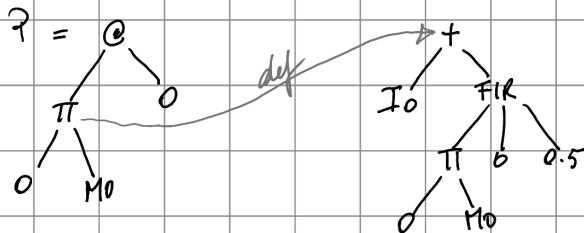
Example 1: $\text{process} = + \sim *(0.5);$

FIR Rewriting



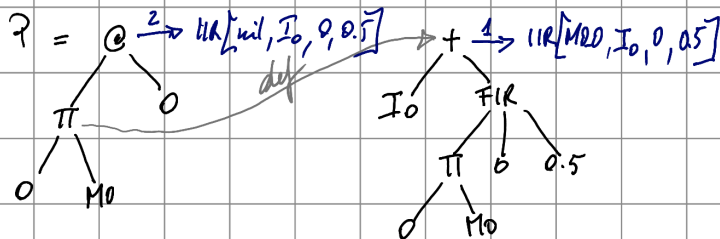
Example 1: `process = + ~ *(0.5);`

After FIR Rewriting



Example 1: $\text{process} = + \sim *(0.5);$

IIR Rewriting



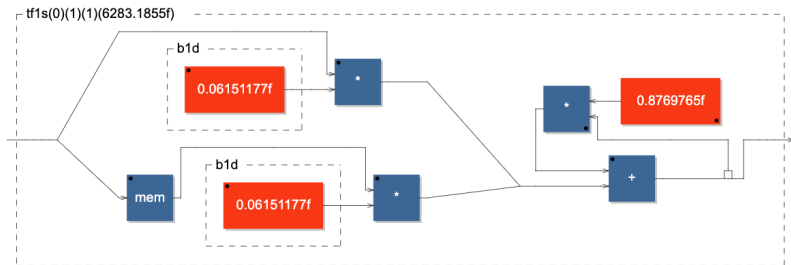
Example 2: `process = fi.lowpass(3, 1000);`

FIR and IIR Representation

```
s1 = FIR[IN[0], 0.06151177f, 0.06151177f];  
s2 = IIR[VOID, s1, 0.0f, 0.8769765f];  
s3 = IIR[VOID, s2, 0.0f, 1.8614085f, -0.8774705f]  
s4 = FIR[s3, 0.004015505f, 0.00803101f, 0.004015505f]
```

Example 2: `process = fi.lowpass(3, 1000);`

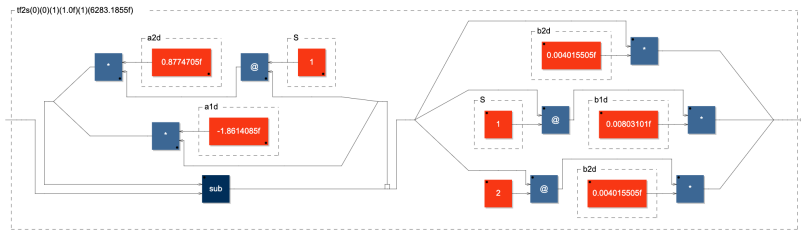
FIR and IIR Representation



```
s1 = FIR[IN[0], 0.06151177f, 0.06151177f];  
s2 = IIR[VOID, s1, 0.0f, 0.8769765f];
```

Example 2: `process = fi.lowpass(3, 1000);`

FIR and IIR Representation



```
s3 = IIR[VOID, s2, 0.0f, 1.8614085f, -0.8774705f]
s4 = FIR[s3, 0.004015505f, 0.00803101f, 0.004015505f]
```

PART 4: Code Generation



Comparing Current Compilation Strategies

Comparing the current compilation strategies for various IIRs and FIRs examples, in particular vector and scalar modes.

Test cases

- iir4
- fir16
- fir16:fir16
- iir4:fir16
- fir16:iir4

Compilation modes

- vector loop variant 0
- vector loop variant 1
- scalar
- experimental scalar mode

Source Code

```
import("stdfaust.lib");

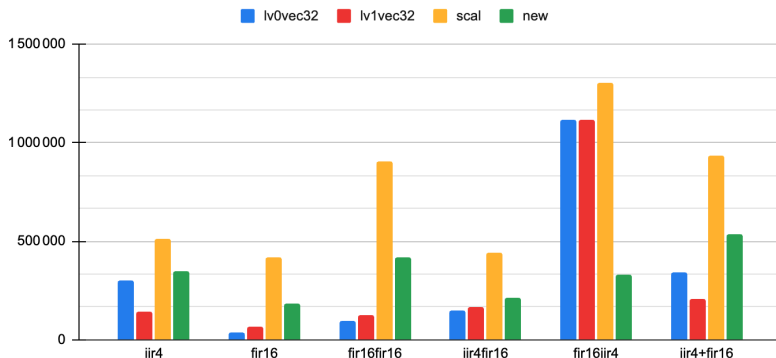
coeffs(N) = par(i, N, 1.0/float(i+2));
coeffsb(N) = par(i, N, 1.0/float(i+2.1));

iir4 = +~fi.fir(coeffs(4));
fir16 = fi.fir(coeffs(16));
fir16fir16 = fi.fir(coeffs(16)) : fi.fir(coeffsb(16));
iir4fir16 = +~fi.fir(coeffs(4)) : fi.fir(coeffs(16));
fir16iir4 = fi.fir(coeffs(16)) : +~fi.fir(coeffs(4));
```

Results

	iir4	fir16	fir16fir16	iir4fir16	fir16iir4	iir4+fir16
lv0vec32	300 507	39 023	97 848	147 819	1 115 630	339 530
lv1vec32	145 199	64 206	123 549	168 292	1 116 570	209 405
scal	514 765	417 509	908 155	441 582	1 302 560	932 274
new	350 846	183 467	419 829	212 594	330 260	534 313

CPU cycles, less is better



Vector Mode vs Handwritten Strategies for FIR16

Vector Mode (Loop Variant 0)

```
for (int i = 0; i < vsize; i = i + 1) {  
    output0[i] = FAUSTFLOAT(0.5f * float(input0[i])  
        + 0.33333334f * fYec0[i - 1]  
        + 0.25f * fYec0[i - 2] + 0.2f * fYec0[i - 3]  
        + 0.16666667f * fYec0[i - 4]  
        + 0.14285715f * fYec0[i - 5]  
        + 0.125f * fYec0[i - 6]  
        + 0.11111111f * fYec0[i - 7]  
        + 0.1f * fYec0[i - 8]  
        + 0.09090909f * fYec0[i - 9]  
        + 0.083333336f * fYec0[i - 10]  
        + 0.07692308f * fYec0[i - 11]  
        + 0.071428575f * fYec0[i - 12]  
        + 0.06666667f * fYec0[i - 13]  
        + 0.0625f * fYec0[i - 14]  
        + 0.05882353f * fYec0[i - 15]));  
}
```

Vector Mode vs Handwritten Strategies for FIR16

Coefficients Loop

```
float coef[] = {0.5f, 0.33333334f, 0.25f, 0.2f, 0.16666667f,  
               0.14285715f, 0.125f, 0.11111111f, 0.1f,  
               0.09090909f, 0.083333336f, 0.07692308f,  
               0.071428575f, 0.06666667f, 0.0625f,  
               0.05882353f};  
  
for (int i = 0; i < vsize; i = i + 1) {  
    float sum = 0.0f;  
    for (int c = 0; c < 16; c++) {  
        sum += coef[c] * fYec0[i - c];  
    }  
    output0[i] = FAUSTFLOAT(sum);  
}
```

Vector Mode vs Handwritten Strategies for FIR16

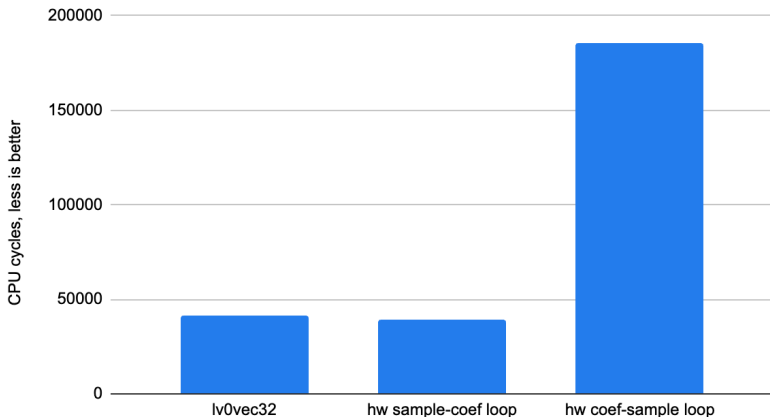
Switched Coefficients Samples Loop (à la SYFALA)

```
float coef[] = {0.5f, 0.33333334f, 0.25f, 0.2f, 0.16666667f,  
                0.14285715f, 0.125f, 0.11111111f, 0.1f,  
                0.09090909f, 0.083333336f, 0.07692308f,  
                0.071428575f, 0.06666667f, 0.0625f,  
                0.05882353f};  
  
for (int i = 0; i < vsize; i = i + 1) {  
    output0[i] = 0.0f;  
}  
for (int c = 0; c < 16; c++) {  
    float* src = &fYec0[-c];  
    for (int i = 0; i < vsize; i = i + 1) {  
        output0[i] += FAUSTFLOAT(coef[c] * src[i]);  
    }  
}
```

Results

	fir16			
lv0vec32	41648			
hw sample-coef	38920			
hw coef-sample	185299			

fir16, comparing vector mode and two hw modes



PART 5: What's remaining?



What's remaining?

- Code generation;
- WCPG calculation;
- Tests on FPGA;
- Grouping of parallel filters.