Embedded Systems Interfacing Assignment 03

Due: 19 September 2013

Problem 01: Timer Settings

Suppose you want to configure a timer to cause an interrupt every 832 μs.

1. How many bits are required for the timer contents assuming prescaler values of 8 and 256?

2. Assuming a prescaler value of 256, what ought the value of $PR1$ be to accomplish the $832 - \mu s$-interrupt task?

3. To what ought the special function register $T1CON$ be set for this application?