FLOP utilization rates of training runs of given sizes

- Setting: H100 SXM
- Setting: H100 SXM Zero Latency
- Setting: H100 SXM Global NVLink
- Setting: H100 SXM Global NVLink and ZL
- Setting: H100 SXM Infinite Network and ZL

Best achievable utilization rate vs. Training compute (16-bit FLOP)