

Broadcorn BCM63XX **FAILSAFE BOOT** 

**Application Note** 

**Broadcom Confidential** 

BCM63XX Application Note FAILSAFE BOOT

Broadcom, the pulse logo, Connecting everything, Avago Technologies, Avago, and the A logo are among the trademarks of Broadcom and/or its affiliates in the United States, certain other countries, and/or the EU.

Copyright © 2019 Broadcom. All Rights Reserved.

The term "Broadcom" refers to Broadcom Inc. and/or its subsidiaries. For more information, please visit www.broadcom.com.

Broadcom reserves the right to make changes without further notice to any products or data herein to improve reliability, rate an.
on, nor the
intrights nor the function, or design. Information furnished by Broadcom is believed to be accurate and reliable. However, Broadcom does not assume any liability arising out of the application or use of this information, nor the application or use of any product or circuit described herein, neither does it convey any license under its patent rights nor the rights of others.

Broadcom Confidential BCM63xx FAILSAFE BOOT NOTE-R

**BCM63XX** Application Note **FAILSAFE BOOT** 

# 1 Description

Beginning with release 5.02L.07, FAILSAFE BOOT is supported on devices that use GEN3 Secure boot ROM boot. When enabled, the standard Linux watchdog must also be configured.

The FAILSAFE boot mechanism allows a watchdog to reboot to a previously programmed image in the event that the system becomes unresponsive during the boot process. This mode only works on types of failure which are recoverable via SoftReset.

The implementation enables CFEROM, CFERAM, and Linux Userspace to maintain a run-time (RT) state across the software stack via storing the state in SoftReset-safe register. The WATCHDOG (WD) is enabled in specific places within the bootloaders where significant boot stages can be identified.

An RT state is a combination of the following values (state | error):

- state is one of CFE BOOT INFO ROM, CFE BOOT INFO PRIMARY, CFE BOOT INFO SECONDARY, CFE\_BOOT\_INFO\_LINUX.
- error is one of CFE\_BOOT\_ERR\_OK, CFE\_BOOT\_ERR\_ABORTED, CFE\_BOOT\_ERR\_CRIT.

These stages/points are:

#### **CFEROM**

On init, before DDR initialization, process state to make decision, as in the following:

If booted from PoR or if error is CFE BOOT ERR OK:

 Set state CFE\_BOOT\_INFO\_ROM and set error CFE\_BOOT\_ ERR ABORTED.

WD armed unconditionally.

If booted from SoftReset:

If state = CFE BOOT INFO ROM and err = CFE BOOT ERR ABORTED:

· Select DDR safe mode

Else, if state = CFE\_BOOT\_INFO\_PRIMARY and err = CFE\_BOOT\_ERR\_ABORTED:

- Set state to CFE BOOT INFO SECONDARY and err to CFE BOOT ERR CRIT. If state also had CFE BOOT INFO LINUX value, then it is added to the state accordingly, for example, state set to CFE\_BOOT\_INFO\_SECONDARY|CFE\_BOOT\_INFO\_LINUX.
- Select to boot from other (inactive) image on flash.

Else, if state = CFE\_BOOT\_INFO\_SECONDARY and err = CFE\_BOOT\_ERR\_CRIT:

· CFEROM halts without return.

Arm watchdog.

2. Before loading/starting CFERAM from flash image from the storage such as SPINAND/NAND/EMMC:

If state = CFE BOOT INFO ROM:

Set state to CFE\_BOOT\_INFO\_PRIMARY.

Arm watchdog.

Boot to the selected image.

**Broadcom Confidential** BCM63xx FAILSAFE BOOT NOTE-R BCM63XX Application Note FAILSAFE BOOT

### **CFERAM**

- 1. Init stage:
  - Arm watchdog (to avoid expiration of timer).
- 2. Before Linux loading:

If state = CFE\_BOOT\_INFO\_PRIMARY of CFE\_BOOT\_INFO\_SECONDARY:

- Add to state CFE BOOT INFO LINUX.
- · Arm watchdog.

If user pressed key to drop to console, stop state tracking; disable WD.

## **Linux User Space**

- On init script S1, re-arm watchdog.
- On init script S89, stop watchdog, set RT error to CFE\_BOOT\_ERR\_OK. (This value should still be present whenever Linux does an intentional reboot.)

Every time WD is armed the default counter is set to expire in 30s. If not reset or disabled, WD will soft-reset SoC. On every stage as described above, WD is disarmed as required to update the RT state then re-armed to continue to the next tracked stage.

The RT state is accessible via proc kernel entry.

/proc/boot/failsafe -

Reading this entry would return the following values:

- ACTIVE: indicating normal boot.
- PREVIOUS: indicating that booted image was an attempt to recover from crash or stall during the boot of the ACTIVE image.

Writing 0 to it resets an error status to success.

**NOTE:** There is no built-in functionality to make policy decisions to either continue attempting to use the failed image, invalidating, or performing an upgrade. Customer-specific code can base policy decisions on /proc/boot/failsafe or use other means to reset the watchdog.

Broadcom Confidential BCM63xx FAILSAFE BOOT NOTE-R

BCM63XX Application Note FAILSAFE BOOT

# **Revision History**

## BCM63xx FAILSAFE BOOT NOTE-R, July 10, 2019

Initial release



Broadcom Confidential BCM63xx FAILSAFE BOOT NOTE-R

Broadcom Confidential

