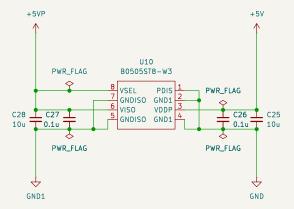


Translated from datasheet

电源关断管脚, 接GND1芯片正常工作, 接逻辑高电平, 芯片停止工作。

The power shutdown pin - connect it to GND1 for normal operation; connect it to a logic high level to stop the chip from operating.

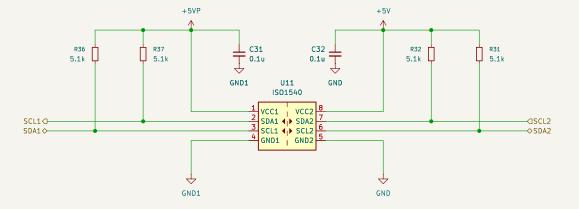
输出电压选择。将VSEL连接到VISO以获得5 V输出或将VSEL连接到GNDISO以获得3.3 V输出。这个引脚有一个较弱的内部上拉:因此,不要让该引脚悬空。 Output voltage selection — connect VSEL to VISO for a 5 V output, or connect VSEL to GNDISO for a 3.3 V output. This pin has a weak internal pull—up, so it should not be left floating.



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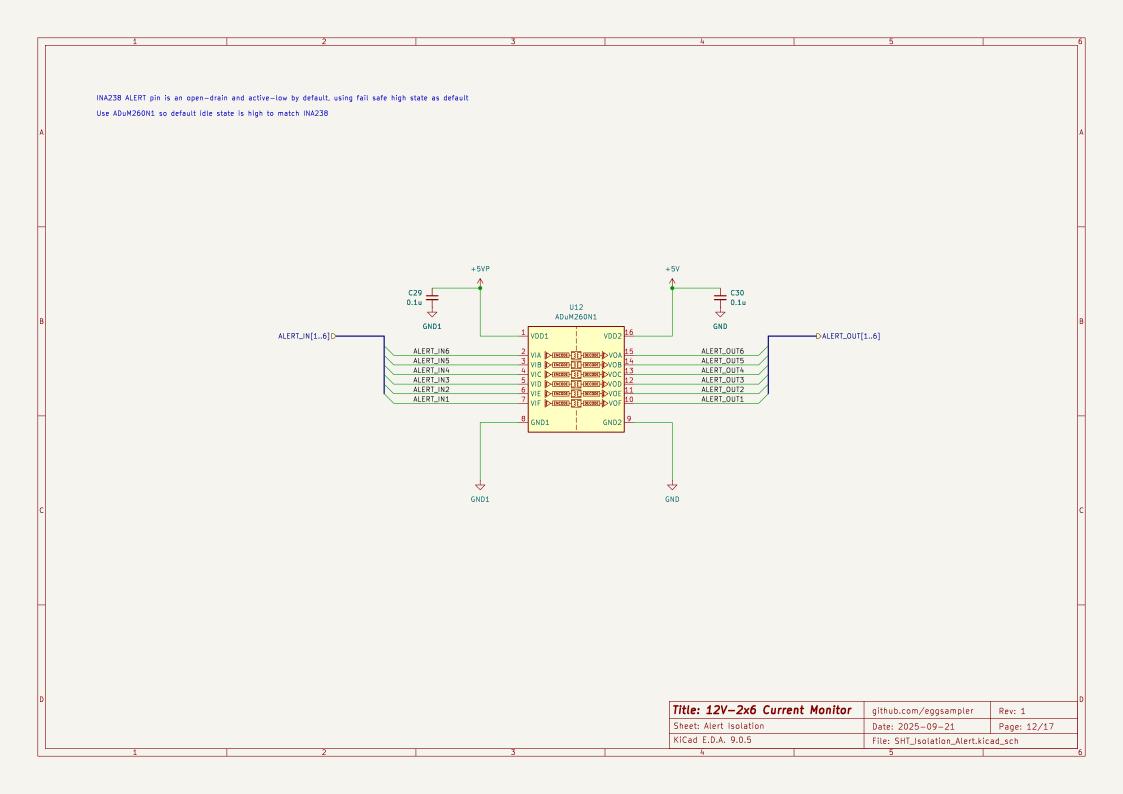
SDA and SCL are interchangeable as the internal circuitry for each is identical These connections are swapped for PCB placement reasons

ISO1640 is a drop-in replacement



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Using linear regulator not switching for less noise MIC5219 typically 10mV-500mV dropout, heaps of headroom from 5->3.3 500mA should be more than enough for the STM32 +5٧ +3.3٧ U13 MIC5219-3.3YM5 TIN OUT 4 C35 C33 ___ C34 470p 1u 🕇 GND Title: 12V-2x6 Current Monitor github.com/eggsampler Rev: 1 Sheet: Microcontroller Power Date: 2025-09-21 Page: 13/17 KiCad E.D.A. 9.0.5 File: SHT_MCU_Power.kicad_sch

