

Based on Schematic QCAA QFN32 with internal LDO regulator setup
https://infocenter.nordicsemi.com/index.jsp?topic=%2Fsdk_nrf5_v17.0.0%2Fnrf52810_user_guide.html
<https://devzone.nordicsemi.com/guides/hardware-design-test-and-measuring/b/nrf5x/posts/general-pcb-design-guidelines-for-nrf52>

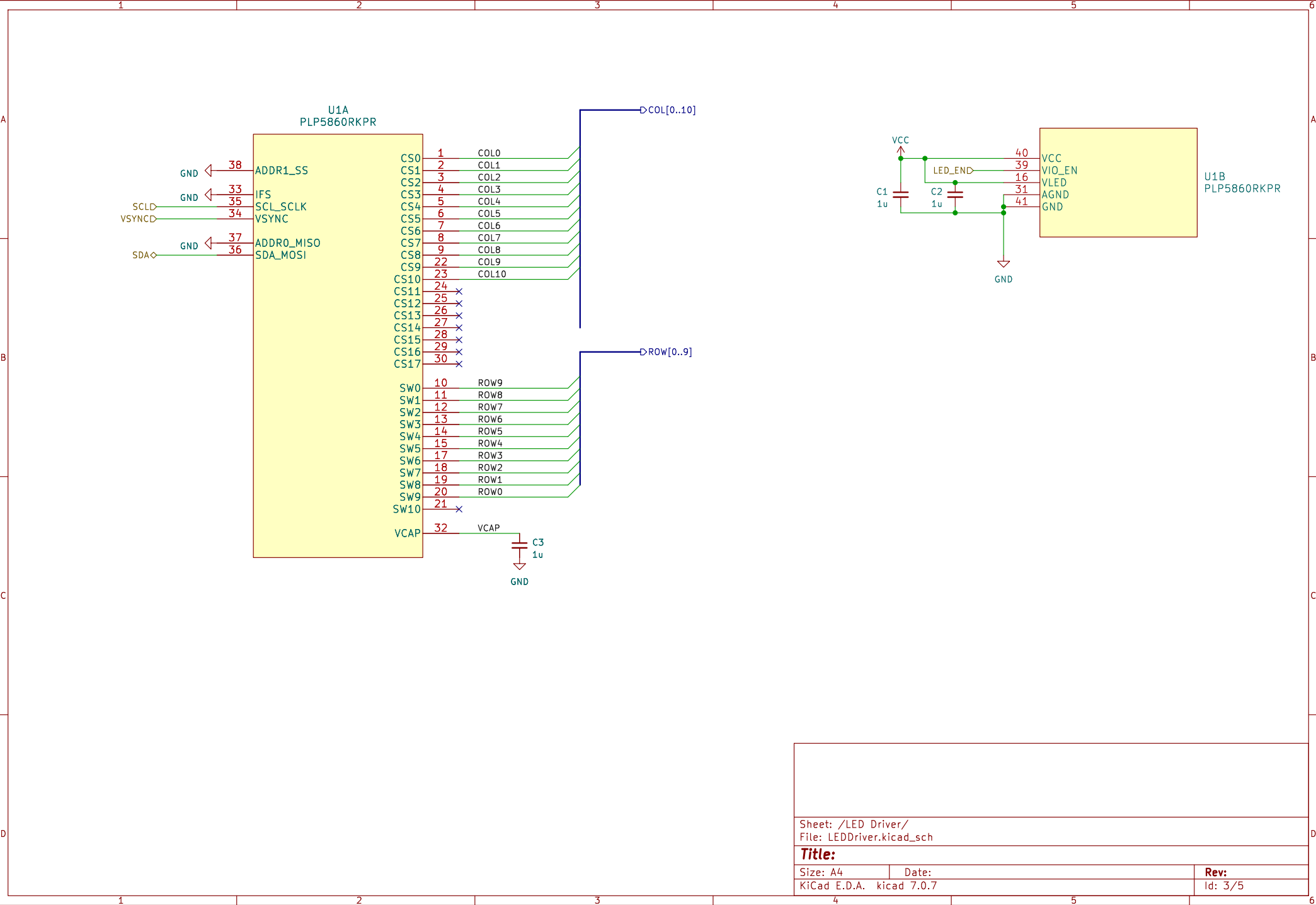
Sheet: /Microcontroller/
 File: Microcontroller.kicad_sch

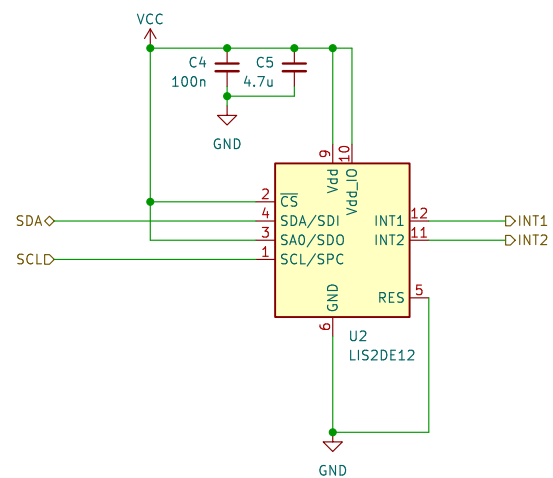
Title:

Size: A4
 KiCad E.D.A. kicad 7.0.7

Date:

Rev:
 Id: 2/5





Sheet: /Accelerometer/
File: Accelerometer.kicad_sch

Title:

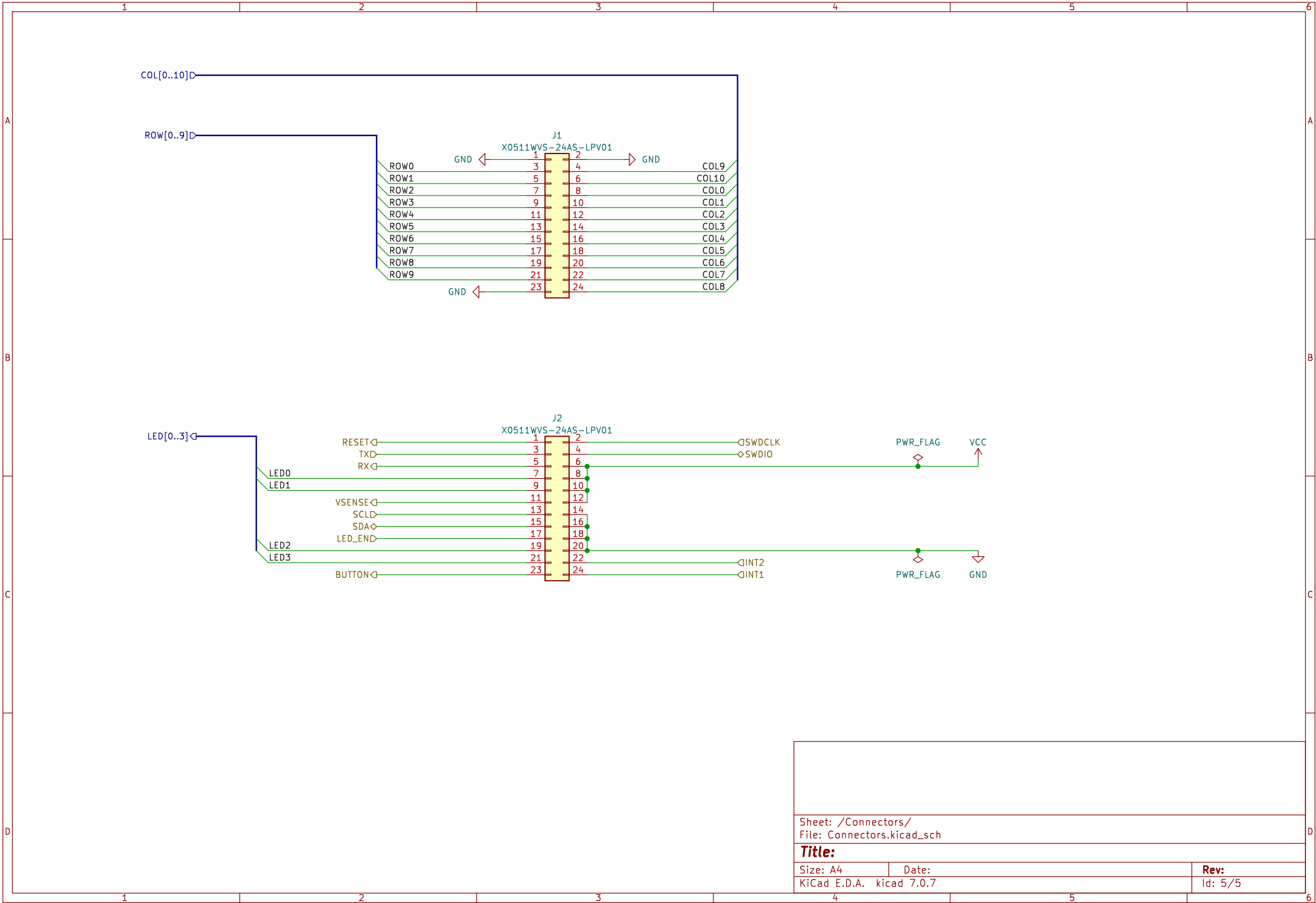
Size: A4

Date:

KiCad E.D.A. kicad 7.0.7

Rev:

Id: 4/5



Sheet: /Connectors/
File: Connectors.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad 7.0.7

Date:

Rev:
Id: 5/5