

Based on Schematic QCAA QFN32 with internal LDO regulator setup
https://infocenter.nordicsemi.com/index.jsp?topic=%2Fsdk_nrf5_v17.0.0%2Fnrf52810_user_guide.html
<https://devzone.nordicsemi.com/guides/hardware-design-test-and-measuring/b/nrf5x/posts/general-pcb-design-guidelines-for-nrf52>

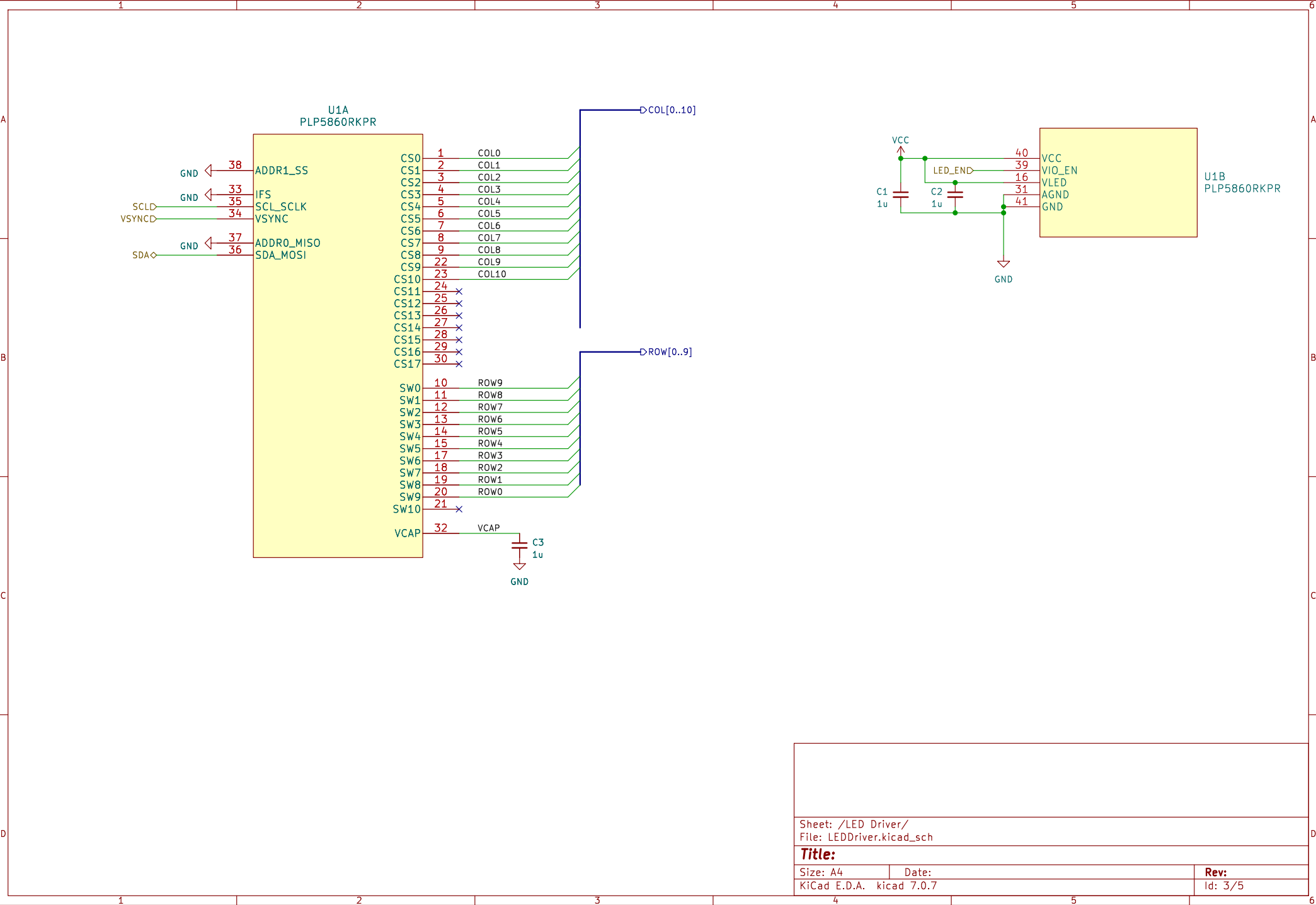
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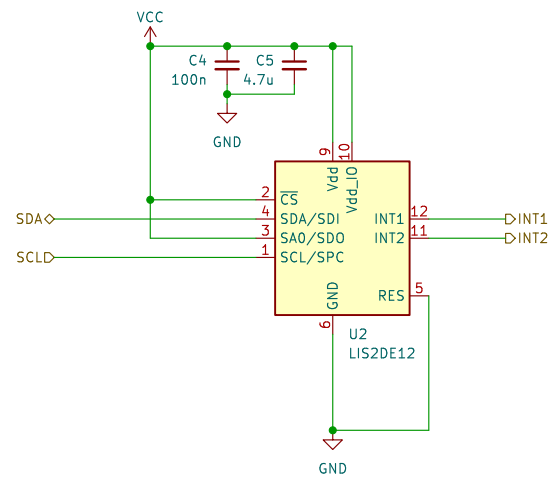
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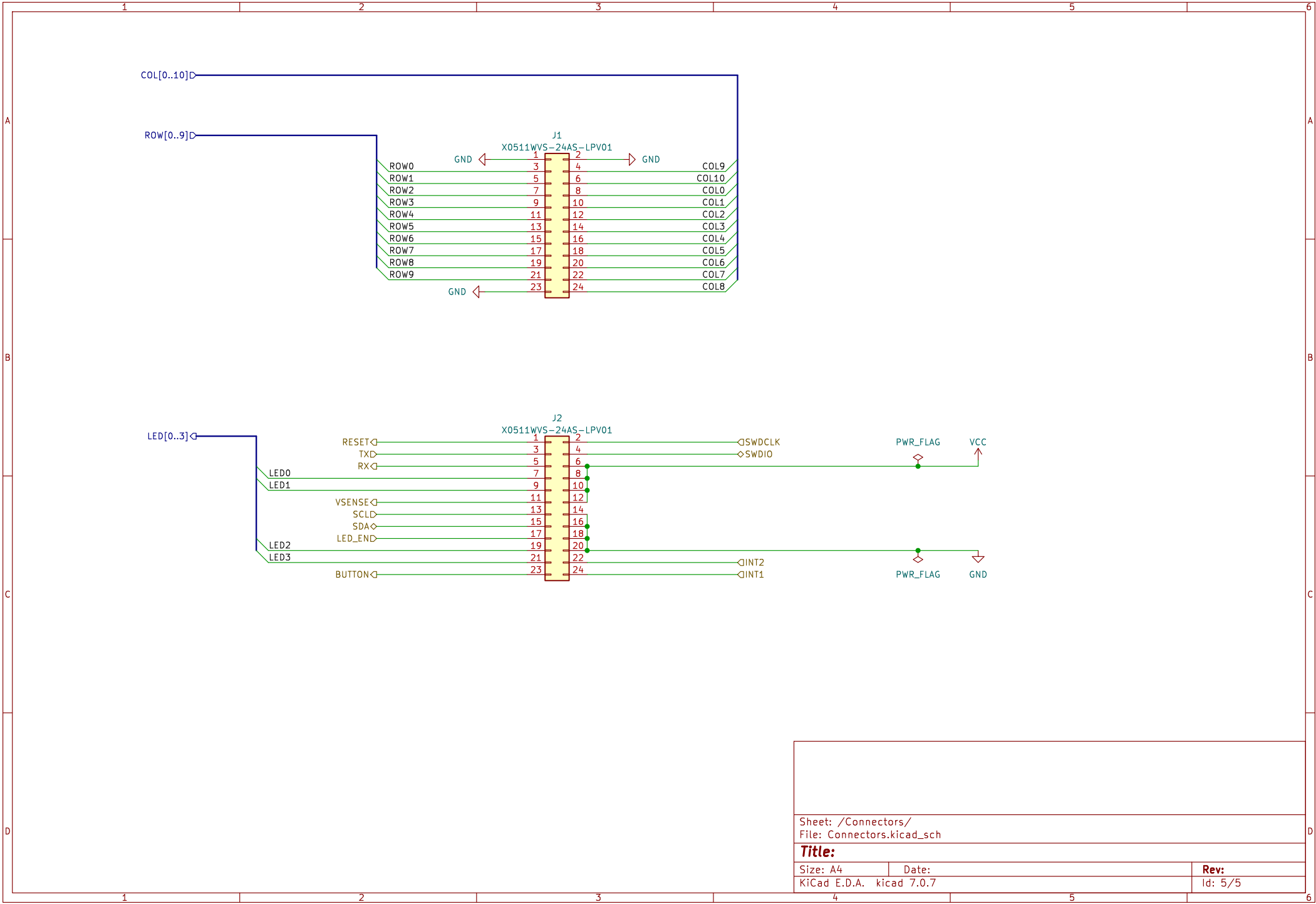
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