

Based on Schematic QFAA QFN48 with internal LDO regulator setup  
<https://infocenter.nordicsemi.com/index.jsp?topic=/%2Fcom.nordic.infocenter.nrf52832.ps.v1.1%2Fpin.html>  
<https://devzone.nordicsemi.com/guides/hardware-design-test-and-measuring/b/nrf5x/posts/general-pcb-design-guidelines-for-nrf52>

Sheet: /Microcontroller/  
File: Microcontroller.kicad\_sch

**Title:**

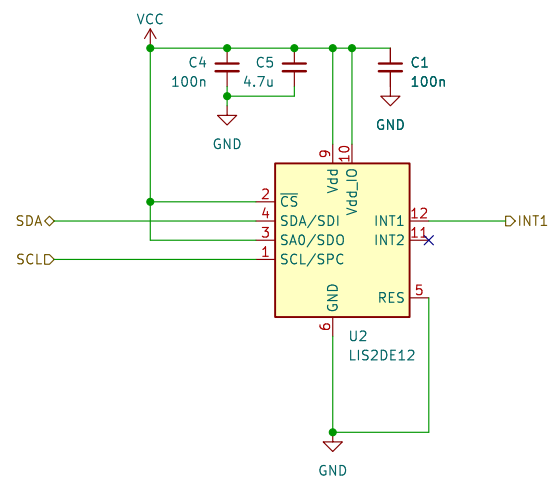
Size: A4

Date:

KiCad E.D.A. kicad 7.0.8

**Rev:**

Id: 2/4



Sheet: /Accelerometer/  
File: Accelerometer.kicad\_sch

**Title:**

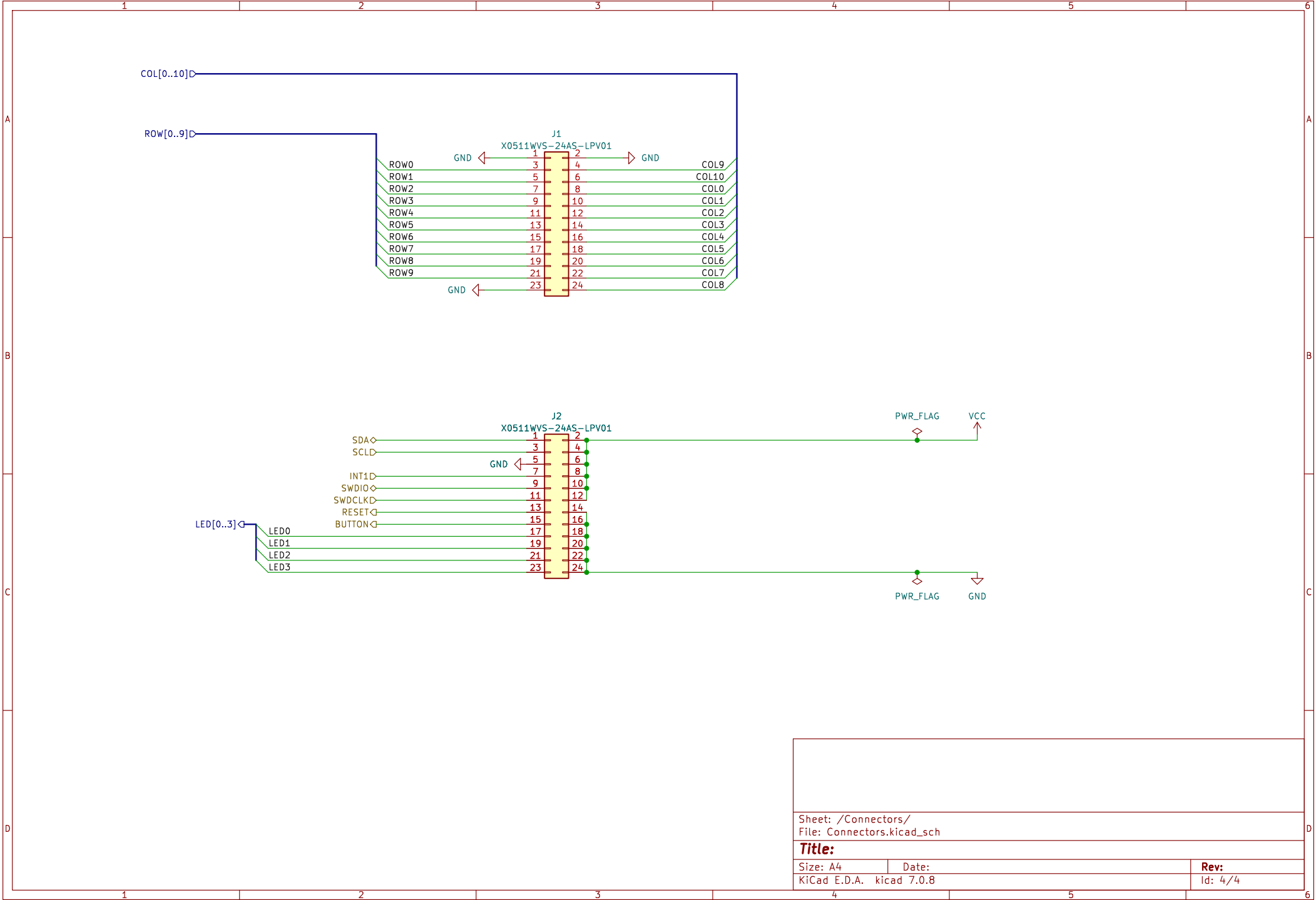
Size: A4

Date:

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**Rev:**

Id: 3/4



Sheet: /Connectors/  
File: Connectors.kicad\_sch

**Title:**

Size: A4  
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Date:

Rev:

Id: 4/4