M1 Info – ARC - LAB2

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1 Dessin de l'automate

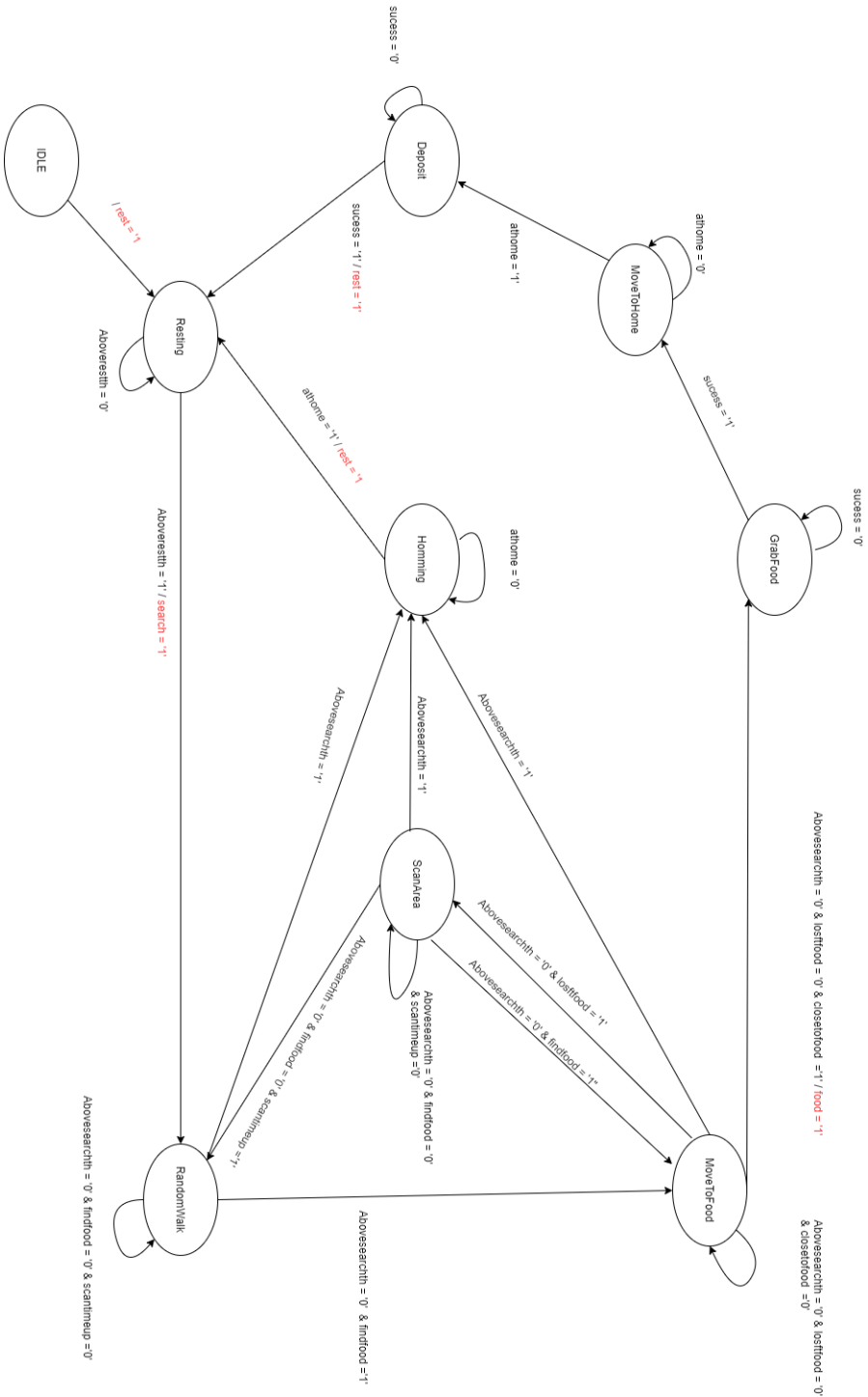

Figure 1: Automate du robot

2 Description VHDL comportementale

Le code source est donné en annexe du document (robot.vhd et testRobot.vhd)

3 TestRobot

3.1 Réalisation du test

Pour tester mon implémentation, j'ai tout d'abord rédigé un tableau des différentes entrées et sorties et des comportements que ceux-ci devais avoir pour toutes les transitions ainsi que les différentes pour y accéder depuis un reset de l'automate.

Dans ce tableau se trouve différentes informations tel que : En rouge : les inputs que je doit mettre à 1, En bleu : les données que doivent prendre les outputs.

Ce tableau est fournis en annexe.

3.2 Interpretation du test

Après simulation, on obtiens les différents graphes fournis en annexe. La figure 2 représente la totalité de la simulation. Les figures 3, 4 et 5 sont cette même simulation partitionné.

Ce que l'on peux observer avec ces différents graphes c'est que notre simulation implémente bien le problème. Les sorties et la suite d'état correspondent bien à ce qui a était prédit dans notre tableau.

4 Counter

Le code source est donné en annexe du document (count.vhd et testCount.vhd)

Après simulation, sur la figure 6 on observe bien que le compteur ne se met en marche uniquement lorsque l'état start est à 1 et le reset fonctionne bien. Le nombre de front montant compté est le bon.

5 System

Le programme compile et la simulation se fait, je n'ai pas eu le temps de faire le testbench, je le rendrais plus tard...

En annexe le code.

STATE	NEXTSTATE	INPUT								OUTPUT						
		athome	findfood	lostfood	closetofood	success	aboveresstth	abovesearcht	scantimeup	rest	food	search	CYCLE	TEMPS MIS A '1'	TEMPS MIS A '0'	MARQUE
FROM IDLE TO IDLE																
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	1	25	35	1
FROM IDLE TO RESTING													RESET	44	45	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	2	45	55	2
RESTING	RESTING	0	0	0	0	0	0	0	0	0	0	0	3	65	75	
FROM IDLE TO RANDOWALK													RESET	84	85	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	4	85	95	3
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	5	105	115	
RANDOMWAL	RANDOMWAL	0	0	0	0	0	0	0	0	0	0	0	6	125	135	
FROM IDLE TO MOVETOFOOD													RESET	144	145	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	7	145	155	4
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	8	165	175	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	9	185	195	
MOVETOFOOD	MOVETOFOOD	0	0	0	0	0	0	0	0	0	0	0	10	205	215	
FROM IDLE TO GRABFOOD													RESET	224	225	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	11	225	235	5
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	12	245	255	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	13	265	275	
MOVETOFOOD	GRABFOOD	0	0	0	1	0	0	0	0	0	1	0	14	285	295	
GRABFOOD	GRABFOOD	0	0	0	0	0	0	0	0	0	0	0	15	305	315	
FROM IDLE T MOVETOHOME													RESET	324	325	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	16	325	335	6
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	17	345	355	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	18	365	375	
MOVETOFOOD	GRABFOOD	0	0	0	1	0	0	0	0	0	1	0	19	385	395	
GRABFOOD	MOVETOHOM	0	0	0	0	1	0	0	0	0	0	0	20	405	415	
MOVETOHOM	MOVETOHOM	0	0	0	0	0	0	0	0	0	0	0	21	425	435	
FROM IDLE TO DEPOSIT													RESET	444	445	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	22	445	455	7
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	23	465	475	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	24	485	495	
MOVETOFOOD	GRABFOOD	0	0	0	1	0	0	0	0	0	1	0	25	505	515	
GRABFOOD	MOVETOHOM	0	0	0	0	1	0	0	0	0	0	0	26	525	535	
MOVETOHOM	DEPOSIT	1	0	0	0	0	0	0	0	0	0	0	27	545	555	
DEPOSIT	DEPOSIT	0	0	0	0	0	0	0	0	0	0	0	28	565	575	
FROM IDLE TO SCAN AREA													RESET	584	585	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	29	585	595	8
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	30	605	615	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	31	625	635	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0	0	0	0	0	32	645	655	
SCANAREA	SCANAREA	0	0	0	0	0	0	0	0	0	0	0	33	665	675	
FROM IDLE TO HOMING VIA SCANAREA													RESET	684	685	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	34	685	695	9
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	35	705	715	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	36	725	735	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0	0	0	0	0	37	745	755	
SCANAREA	HOMING	0	0	0	0	0	0	1	0	0	0	0	38	765	775	
HOMING	HOMING	0	0	0	0	0	0	0	0	0	0	0	39	785	795	
FROM IDLE TO HOMING VIA MOVETOFOOD													RESET	804	805	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	40	805	815	10
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	41	825	835	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	42	845	855	
MOVETOFOOD	HOMING	0	0	0	0	0	0	1	0	0	0	0	43	865	875	
HOMING	HOMING	0	0	0	0	0	0	0	0	0	0	0	44	885	895	
FROM IDLE TO HOMING VIA RANDOMWALK													RESET	904	905	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	45	905	915	11
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	46	925	935	
RANDOMWAL	HOMING	0	0	0	0	0	0	1	0	0	0	0	47	945	955	
HOMING	HOMING	0	0	0	0	0	0	0	0	0	0	0	48	965	975	
FROM IDLE TO RESTING VIA HOMMING													RESET	984	985	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	49	985	995	12
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	50	1005	1015	
RANDOMWAL	HOMING	0	0	0	0	0	0	1	0	0	0	0	51	1025	1035	
HOMING	RESTING	1	0	0	0	0	0	0	0	1	0	0	52	1045	1055	
FROM IDLE TO RESTING VIA DEPOSIT													RESET	1064	1065	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	53	1065	1075	13
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	54	1085	1095	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	55	1105	1115	
MOVETOFOOD	GRABFOOD	0	0	0	1	0	0	0	0	0	1	0	56	1125	1135	
GRABFOOD	MOVETOHOM	0	0	0	0	1	0	0	0	0	0	0	57	1145	1155	
MOVETOHOM	DEPOSIT	1	0	0	0	0	0	0	0	0	0	0	58	1165	1175	
DEPOSIT	RESTING	0	0	0	0	1	0	0	0	1	0	0	59	1185	1195	
FROM IDLE TO MOVETOFOOD VIA SCANAREA													RESET	1204	1205	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	60	1205	1215	14
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	61	1225	1235	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	62	1245	1255	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0	0	0	0	0	64	1285	1295	
SCANAREA	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	65	1305	1315	
FROM IDLE TO RANDOMWALK VIA SCANAREA													RESET	1324	1325	
IDLE	RESTING	0	0	0	0	0	0	0	0	1	0	0	66	1325	1335	15
RESTING	RANDOMWAL	0	0	0	0	0	1	0	0	0	0	1	67	1345	1355	
RANDOMWAL	MOVETOFOOD	0	1	0	0	0	0	0	0	0	0	0	68	1365	1375	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0	0	0	0	0	69	1385	1395	
SCANAREA	RANDOMWAL	0	0	0	0	0	0	0	1	0	0	0	70	1405	1415	

```vhdl
-- ROBOT.VHD --

library ieee;
use ieee.std_logic_1164.all;

entity Robot is
    port(reset, clk, athome, findfood, lostfood, closetofood,
    success, aboverestth, abovesearchth, scantimeup: in std_logic;
    rest, search, food: out std_logic);
end Robot;

architecture automate_robot of Robot is

    type States is (IDLE, RESTING, RANDOMWALK, SCANAREA, HOMING, MOVETOFOOD,
    MOVETOHOME, DEPOSIT, GRABFOOD) ;
    Signal state, nextstate : States := IDLE;

begin
    -- Calcul de l'état suivant
    -- Comme on est en std_logic,"elsif ='0'" et non "else", car le signal peux
    avoir d'autre valeur
    process (state, athome, findfood, lostfood, closetofood, success, aboverestth,
    abovesearchth, scantimeup)
    begin
        case state is
            when IDLE => nextstate <= RESTING;
            when RESTING =>
                if aboverestth = '1' then nextstate <= RANDOMWALK;
                elsif aboverestth = '0' then nextstate <= RESTING;
                end if;

            when RANDOMWALK =>
                if abovesearchth = '1' then nextstate <= HOMING;
                elsif  abovesearchth = '0' then
                    if findfood = '1' then nextstate <= MOVETOFOOD;
                    elsif findfood = '0' then
                        nextstate <= RANDOMWALK;
                    end if;
                end if;

            when SCANAREA =>
                if abovesearchth = '1' then nextstate <= HOMING;
                elsif  abovesearchth = '0' then
                    if findfood = '1' then nextstate <= MOVETOFOOD;
                    elsif findfood = '0' then
                        if scantimeup = '1' then nextstate <= RANDOMWALK;
                        elsif scantimeup = '0' then nextstate <= SCANAREA;
                        end if;
                    end if;
                end if;
            when HOMING => nextstate <= RESTING;
            when MOVETOFOOD =>
                if abovesearchth = '1' then nextstate <= HOMING;
                elsif  abovesearchth = '0' then
                    if lostfood = '1' then nextstate <= SCANAREA;
                    elsif lostfood = '0' then
                        if closetofood = '1' then nextstate <= GRABFOOD;
                        elsif closetofood = '0' then
                            nextstate <= MOVETOFOOD;
                        end if;
                    end if;
                end if;
            when GRABFOOD =>
                if success = '1' then nextstate <= MOVETOHOME;
                elsif success = '0' then nextstate <= GRABFOOD;
                end if;
            when MOVETOHOME =>
                if athome = '1' then nextstate <= DEPOSIT;
                elsif athome = '0' then nextstate <= MOVETOFOOD;
                end if;
            when DEPOSIT =>
                if success = '1' then nextstate <= RESTING;
                elsif success = '0' then nextstate <= DEPOSIT;
```

```
71                  end if;
72          end case;
73      end process;
74
75      -- MISE A JOUR DU REGISTRE D'ETAT
76
77      process(reset, clk)
78      begin
79          -- RESET : asynchrone haut
80          if reset = '1' then state <= IDLE;
81          -- HORLOGE : front montant
82          elsif (clk'event and clk = '1') then
83              state <= nextstate;
84          end if;
85      end process;
86
87
88      -- MISE A JOUR DES OUTPUTS
89      rest <= '1' when (( state = DEPOSIT and success = '1' ) OR (state = IDLE) OR
        (state = HOMING and athome = '1') ) else '0';
90      search <= '1' when (state = RESTING and aboverestth = '1' ) else '0';
91      food <= '1' when  (state = MOVETOFOOD and abovesearchth = '0' and lostfood = '0'
        and closetofood ='1') else '0';
92
93
94
95  end automate_robot;
96
97
```

```
1   -- TESTROBOT.VHD --
2
3   library ieee;
4   use ieee.std_logic_1164.all;
5
6   entity testRobot is
7   end testRobot;
8
9   architecture test1 of testRobot is
10    component Robot is
11       port(reset, clk, athome, findfood, lostfood, closetofood,
12      success, aboverestth, abovesearchth, scantimeup: in std_logic;
13      rest, search, food: out std_logic);
14    end component;
15    signal r, clk, ah, f, l, c, s, arest, asearch,scan, re, se, fo : std_logic := '0';
16  begin
17       A: Robot port map(r, clk, ah, f, l, c, s, arest, asearch,scan, re, se, fo);
18      -- manage reset
19      r <= '0', '1' after 44 ns, '0' after 45 ns, '1' after 84 ns, '0' after 85 ns,
        '1' after  144 ns, '0' after 145  ns,
20      '1' after 224 ns, '0' after  225 ns, '1' after  324 ns, '0' after 325  ns, '1'
        after  444 ns, '0' after 445  ns, '1' after  584 ns, '0' after  585 ns,
21      '1' after  684 ns, '0' after 685  ns, '1' after  804 ns, '0' after 805  ns, '1'
        after 904  ns, '0' after 905  ns, '1' after  984 ns, '0' after  985 ns,
22      '1' after 1064  ns, '0' after 1065  ns, '1' after 1204  ns, '0' after 1205  ns,
        '1' after 1324  ns, '0' after 1325  ns;
23      -- manage clock
24      process
25      begin
26          clk <= '0';
27          wait for 10 ns;
28          clk <= '1';
29          wait for 10 ns;
30      end process;
31      -- manage athome
32      ah <= '0', '1' after 545 ns, '0' after 555 ns, '1' after 1045 ns, '0' after 1055
        ns, '1' after 1165 ns, '0' after 1175 ns;
33      -- manage findfood
34      f <= '0', '1' after 185 ns, '0' after 195 ns, '1' after 265 ns, '0' after 275
        ns, '1' after 365 ns, '0' after 375 ns,
35      '1' after 485 ns, '0' after 495 ns, '1' after 625 ns, '0' after 635 ns, '1'
        after 725 ns, '0' after 735 ns,
36      '1' after 845 ns, '0' after 855 ns, '1' after 1105 ns, '0' after 1115 ns, '1'
        after 1245 ns, '0' after 1255 ns,
37      '1' after 1265 ns, '0' after 1275 ns;
38      -- manage lostfood
39      l <= '0', '1' after 645 ns, '0' after 655 ns, '1' after 745 ns, '0' after 755
        ns, '1' after 1265 ns, '0' after 1275 ns;
40      --manage closetofood
41      c <= '0' , '1' after 285 ns, '0' after 295 ns, '1' after 385 ns, '0' after 395
        ns, '1' after 505 ns, '0' after 515 ns,
42      '1' after 1125 ns, '0' after 1135 ns;
43      --manage sucess
44      s <= '0', '1' after 405 ns, '0' after 415 ns, '1' after 525 ns, '0' after 535
        ns, '1' after 1145 ns, '0' after 1155 ns,
45      '1' after 1185 ns, '0' after 1195 ns;
46
47      -- manage aboverestth
48
49      arest <= '0', '1' after 105 ns, '0' after 115 ns, '1' after 165 ns, '0' after
        175 ns,
50      '1' after 245 ns, '0' after 255 ns , '1' after 345 ns, '0' after 355 ns, '1'
        after 465 ns, '0' after 475 ns,
51      '1' after 605 ns, '0' after 615 ns, '1' after 705  ns, '0' after 715 ns, '1'
        after 825 ns, '0' after 835 ns,
52      '1' after 925 ns, '0' after 935 ns, '1' after 1005 ns, '0' after 1015 ns, '1'
        after 1085 ns, '0' after 1095 ns,
53      '1' after 1225 ns, '0' after 1235 ns;
54
55      -- manage abovesearchth
56      asearch <= '0', '1' after 765 ns, '0' after 775 ns, '1' after 865 ns, '0' after
        875 ns, '1' after 945 ns, '0' after 955 ns,
57      '1' after 1025 ns, '0' after 1035 ns;
```

```
58
59
60          -- manage scantimeup
61          scan <= '0', '1' after 1405 ns, '0' after 1415 ns;
62
63      end test1;
64
65      library work;
66      configuration config1 of work.testRobot is
67          for test1
68             for A:Robot use entity work.Robot(automate_robot);
69             end for;
70          end for;
71      end config1;
72
73
74
```

```vhdl
-- COUNT.VHD --
library ieee;
use ieee.std_logic_1164.all;


entity Count is generic(threshold: natural := 10);
 port(reset, clk, start: in std_logic; aboveth: out std_logic);
end Count;

architecture Behav of Count is
    type States is (IDLE, COUNTING);
    Signal state, nextstate : States := IDLE;
    Signal c : natural := 0;
begin
    -- Calcul de l'état suivant
    -- Comme on est en std_logic,"elsif ='0'" et non "else", car le signal peux avoir d'autre valeur
    process (state, reset, clk, start)
    begin
        case state is
        when IDLE =>
            if start = '1' then
                nextstate <= COUNTING;
            elsif start = '0' then
                nextstate <= IDLE;
            end if;
        when COUNTING =>
            if c < threshold then
                nextstate <= COUNTING;
            else
                nextstate <= IDLE;
            end if;
        end case;
    end process;

    -- MISE A JOUR DU REGISTRE D'ETAT
    process(reset, clk)
    begin
        -- RESET : asynchrone haut
        if reset = '1' then
            state <= IDLE;
        -- HORLOGE : front montant
        elsif (clk'event and clk = '1') then
            state <= nextstate;
        end if;
    end process;

    -- MISE A JOUR A CHAQUE FRONT MONTANT DE LA CLOCK POUR C ou sur un reset
    process(start, clk, c, reset)
    begin
        if(reset = '1') then
            c <= 0;

        else
            if (clk'event and clk = '1') then
                if (state = IDLE and start = '0') then

                elsif ( state = IDLE and start = '1') then
                    c <= c + 1;
                elsif (state = COUNTING and c < threshold) then
                    c <= c + 1;
                elsif(state = COUNTING and c >= threshold) then
                    c <= 0;
                end if;
            end if;
        end if;
    end process;

    -- Mise a jour de aboveth
    aboveth <= '0' when c < threshold else '1';

end Behav;

```

```vhdl
-- TESTCOUNT.VHD --

library ieee;
use ieee.std_logic_1164.all;

entity testCount is
end testCount;

architecture test2 of testCount is
  component Count is
    generic (threshold : natural);
     port(reset, clk, start: in std_logic; aboveth: out std_logic);
  end component;
  signal r, c, s,a : std_logic := '0';
begin
     B: Count
     generic map(3)
     port map(r,c,s,a);

    process
    begin
        c <= '0';
        wait for 10 ns;
        c <= '1';
        wait for 10 ns;
    end process;

    s <= '0', '1' after 20 ns , '0' after 40 ns, '1' after 170 ns, '0' after 190 ns,
    '1' after 210 ns;
    r <= '0', '1' after 200 ns, '0' after 201 ns, '1' after 290 ns, '0' after 300 ns;
end test2;

library work;
configuration config2 of work.testCount is
    for test2
       for B:Count use entity work.testCount(Behav);
       end for;
    end for;
end config2;
```

```vhdl
-- SYSTEM.VHD --
library ieee;
use ieee.std_logic_1164.all;

entity System is
     port(reset, clk, athome, findfood, lostfood, closetofood, success,
     scantimeup: in std_logic;
     food: out std_logic);
end System;

architecture Struct of System is
    component Count is
        generic (threshold : natural);
        port(reset, clk, start: in std_logic; aboveth: out std_logic);
    end component;

    component Robot is
         port(reset, clk, athome, findfood, lostfood, closetofood,
        success, aboverestth, abovesearchth, scantimeup: in std_logic;
        rest, search, food: out std_logic);
    end component;
    Signal foodOut, link_ab_reset, link_ab_search, link_rest, link_search :
    std_logic := '0';
begin
-- Count1
     C1: Count
     generic map(3) -- Dois-je compter 4 front à la fin du start ou depuis le début
     du start
     port map(reset,clk,link_rest,link_ab_reset);
-- Count2
     C2: Count
     generic map(9) -- Dois-je compter 10 front à la fin du start ou depuis le début
     du start
     port map(reset,clk,link_search,link_ab_search);
-- Robot
     R: Robot port map(reset, clk, athome, findfood, lostfood, closetofood, success,
     link_ab_reset, link_ab_search, scantimeup, link_rest, link_search, foodOut);

     food <= foodOut;

end Struct;

```

```
1   -- TESTSYSTEM.VHD --
2
3   library ieee;
4   use ieee.std_logic_1164.all;
5
6   entity testSystem is
7   end testSystem;
8
9   architecture test3 of testSystem is
10      component System is
11      port(reset, clk, athome, findfood, lostfood, closetofood, success,
12           scantimeup: in std_logic;
13           food: out std_logic);
14      end component;
15      Signal reset, clk, athome, findfood, lostfood, closetofood, success, scantimeup,
        food : std_logic := '0';
16  begin
17      S:System port map(reset, clk, athome, findfood, lostfood, closetofood, success,
        scantimeup, food);
18
19  end test3;
20
21
22
23
```

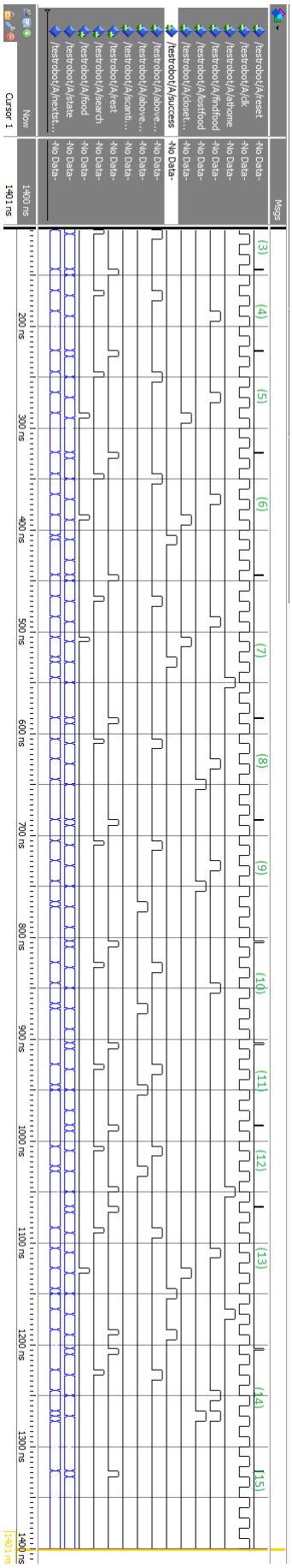

Figure 2: Simulation du robot

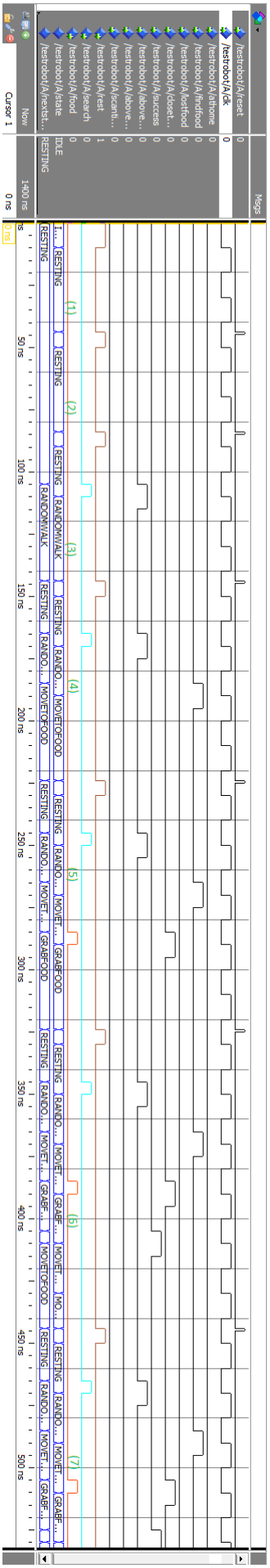

Figure 3: Simulation du robot : 0 ns à 500 ns

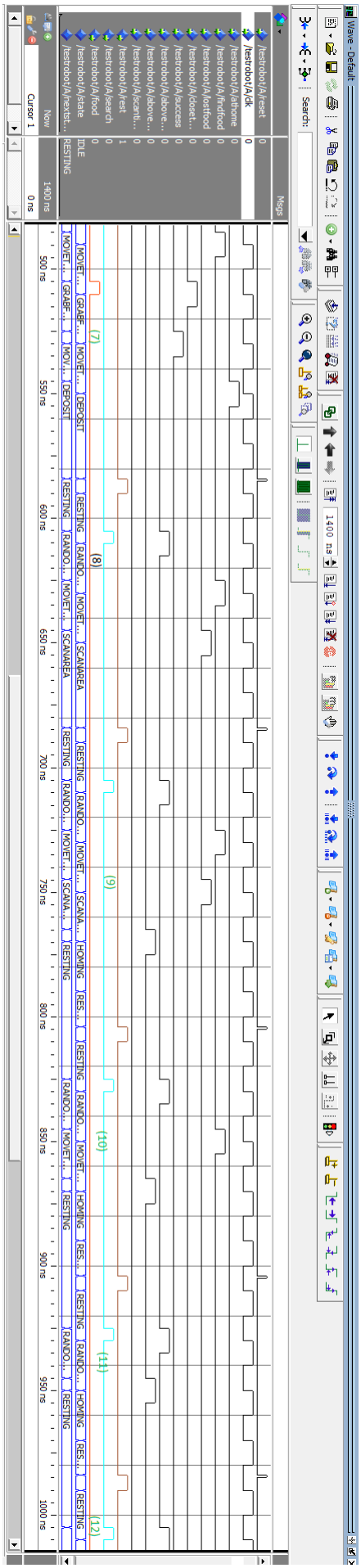

Figure 4: Simulation du robot : 500 ns à 1000 ns

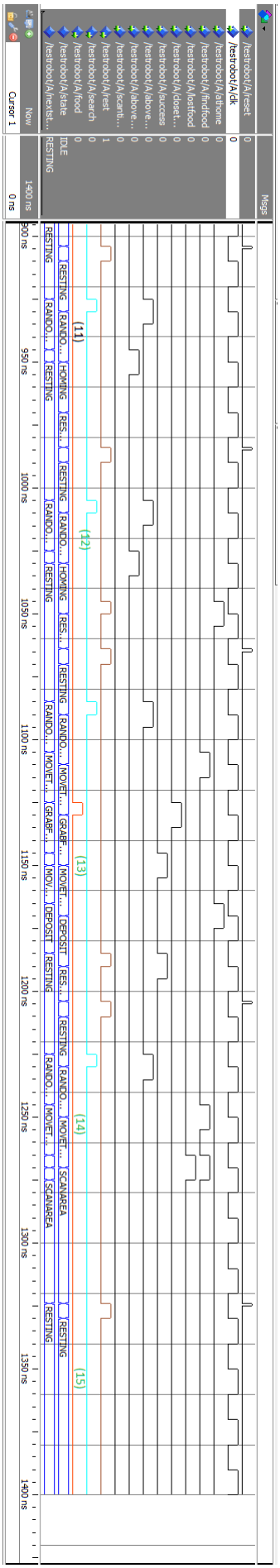

Figure 5: Simulation du robot : 1000 ns à 1500 ns

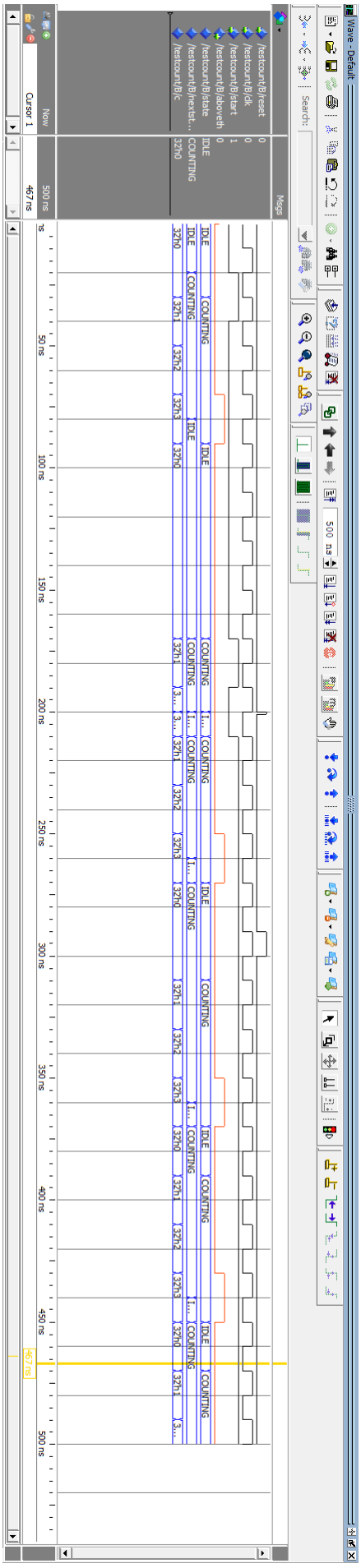

Figure 6: Simulation du Count

6 Travail effectué après le rendu

6.1 Modification du code

Après le rendu, j'ai continué le travail afin de pouvoir simuler le système complet.
Voici quelques erreurs que j'ai pu trouver :

- J'ai oublié d'implémenter le fait d'attendre l'entrée 'athome' pour effectuer la transition entre l'état HOMING et l'état RESTING
- Ligne 66 du fichier robot.vhd : J'ai écris MOVETOFOOD au lieu de MOVETOHOME
- Ligne 17 de count.vhd : mauvaise liste de sensibilité. J'ai donc remplacé

```
process(state, reset, clk, start)
par
process (state, start, c)
```

- Ligne 37 de count.vhd: J'ai oublié de prendre en compte le signal start pour la mise à jour instantanée des registres. J'ai alors rajouté start a la liste de sensibilité et ajouté le code suivant pour pouvoir détecter un spike :

```
if ( (start = '1') and (state = IDLE) )then
state <= COUNTING;
end if;
```

Je fournis aussi ci dessous le fichier finale de testSystem.vhd

Les différentes modifications et le code suplémentaire sont disponnible sur mon github : `https://github.com/egobiah/ARC`

6.2 Testbench

Comme dans le premier testbench, je me suis fixé l'objectif de passer par toutes les transitions et de vérifier que l'enchainement des états est correct. Je réutilise alors le même principe de tableau que précédemment

pour calculer mes enchainements cependant désormais d'autres colonnes viennent en complément.

Déclenchement c1 Cette colonne me permet de calculer combien de repérer quand-est ce que le signal rest est déclenché et combien de temps dois-je attendre avant de faire une autre action.

Attente counter Le nombre de cycle à attendre.
Ci dessous le tableau et la figure 7 représentant le chrono-graphe complet, les figures 8,9,10 et 11 des partitions du chrono-graphe

```vhdl
-- TESTSYSTEM.VHD --

library ieee;
use ieee.std_logic_1164.all;

entity testSystem is
end testSystem;

architecture test3 of testSystem is
    component System is
    port(reset, clk, athome, findfood, lostfood, closetofood, success,
         scantimeup: in std_logic;
         food: out std_logic);
    end component;
    Signal reset, clk, athome, findfood, lostfood, closetofood, success, scantimeup,
    food : std_logic := '0';
begin
    S:System port map(reset, clk, athome, findfood, lostfood, closetofood, success,
    scantimeup, food);
    reset <= '1', '0' after 5 ns, '1' after 1830 ns, '0' after 1850 ns;
    process
    begin
    clk <= '0';
    wait for 10 ns;
    clk <= '1';
    wait for 10 ns;
    end process;

    athome <= '0', '1' after 400 ns, '0' after 440 ns, '1' after 1030 ns, '0' after
    1050 ns
    ,'1' after 1530 ns, '0' after 1550 ns;

    findfood <= '0', '1' after 590 ns, '0' after 610 ns,'1' after 1330 ns, '0' after
    1350 ns,
    '1' after 1430 ns, '0' after  1450 ns, '1' after 1650 ns, '0' after  1670 ns,
    '1' after 1690 ns, '0' after  1710 ns;

    closetofood <= '0', '1' after 630 ns, '0' after 650 ns;

    success <= '0', '1' after 850 ns, '0' after 870 ns,'1' after 1230 ns, '0' after
    1250 ns;

    lostfood <= '0', '1' after 1370 ns, '0' after 1390 ns, '1' after 1670 ns, '0'
    after  1690 ns,
    '1' after 1710 ns, '0' after  1730 ns;

    scantimeup <= '0', '1' after 1410 ns, '0' after 1430 ns;


end test3;
```

STATE	NEXTSTATE	INPUT						OUTPUT	Déclenchement C1	Attente COUNTER (en cycles)	CYCLE	TEMPS MIS A '1'	TEMPS MIS A '0'	MARQUE
		athome	findfood	lostfood	closetofood	success	scantimeup	food						
DO NOTHING														1
IDLE	RESTING	0	0	0	0	0	0	0						
RESTING	RANDOMWALK	0	0	0	0	0	0	0	1	4	1	10	30	
RANDOMWALK	HOMING	0	0	0	0	0	0	0		10	5	90	110	
TIME TO GO HOME														2
HOMING	RESTING	1	0	0	0	0	0	0	1	4	21	410	430	
RESTING	RANDOMWALK	0	0	0	0	0	0	0		0	25	490	510	
FIND SOME FOOD														3
RANDOMWALK	RANDOMWALK	0	0	0	0	0	0	0			25	490	510	
RANDOMWALK	MOVETOFOOD	0	1	0	0	0	0	0			30	590	610	
MOVETOFOOD	GRABFOOD	0	0	0	1	0	0	1			32	630	650	
WAIT LONG TIME TO PROOVE THAT NEED SUCCESS														4
GRABFOOD	MOVETOHOME	0	0	0	0	1	0	0			42	830	850	
WAIT LONG TIME TO PROOVE THAT NEED ATHOME														5
MOVETOHOME	DEPOSIT	1	0	0	0	0	0	0			52	1030	1050	
WAIT LONG TIME TO PROOVE THAT NEED SUCESS														6
DEPOSIT	RESTING	0	0	0	0	1	0	0			62	1230	1250	
BACK TO NORMAL														7
RESTING	RANDOMWALK	0	0	0	0	0	0	0	1	4	62	1230	1250	
GO TO SCAN AREA														8
RANDOMWALK	RANDOMWALK	0	0	0	0	0	0	0			66	1310	1330	
RANDOMWALK	MOVETOFOOD	0	1	0	0	0	0	0			67	1330	1350	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0			69	1370	1390	
GO TO RANDOMWALK														9
SCANAREA	RANDOMWALK	0	0	0	0	0	1	0			71	1410	1430	
GO TO MOVETOFOOD AND WAIT FOR COUNTER														10
RANDOMWALK	MOVETOFOOD	0	1	0	0	0	0	0			72	1430	1450	
MOVETOFOOD	HOMING	0	0	0	0	0	0	0			76	1510	1530	
GO TO SCAN AREA AND PING PONG WITH MOVE TO FOOD														11
HOMING	HOMING	0	0	0	0	0	0	0			76	1510	1530	
HOMING	RESTING	1	0	0	0	0	0	0	1	4	77	1530	1550	
RESTING	RANDOMWALK	0	0	0	0	0	0	0			81	1610	1630	
RANDOMWALK	MOVETOFOOD	0	1	0	0	0	0	0			83	1650	1670	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0			84	1670	1690	
SCANAREA	MOVETOFOOD	0	1	0	0	0	0	0			85	1690	1710	
MOVETOFOOD	SCANAREA	0	0	1	0	0	0	0			86	1710	1730	
WAIT FOR HOMING														12
SCANAREA	HOMING	0	0	0	0	0	0	0			0	1810	1830	
LAST THING TO TEST IS RESET														13
HOMING	IDLE	RESET									0	1830	1850	

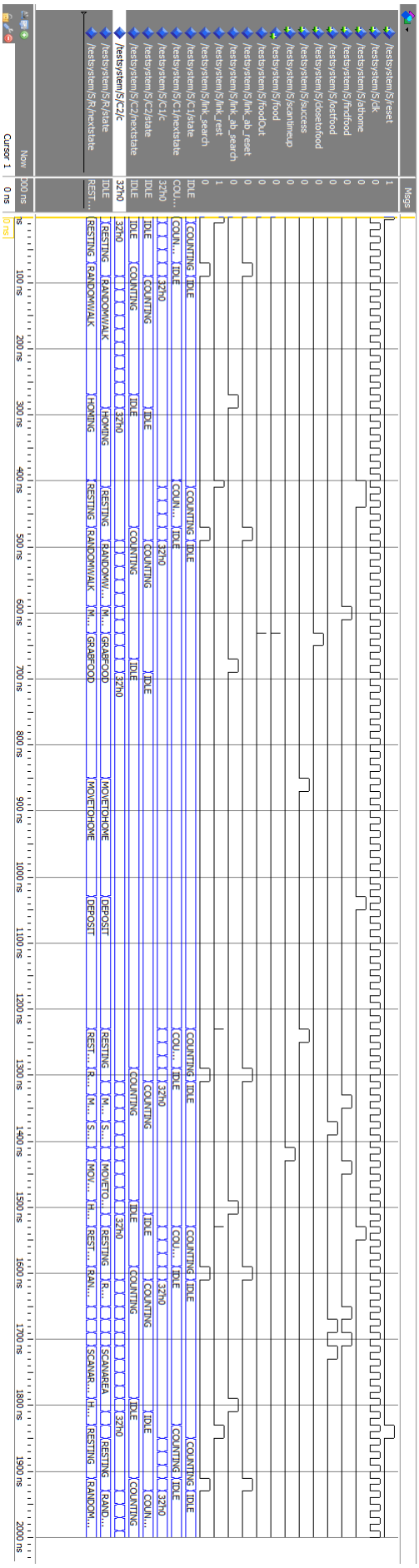

Figure 7: Simulation du Système

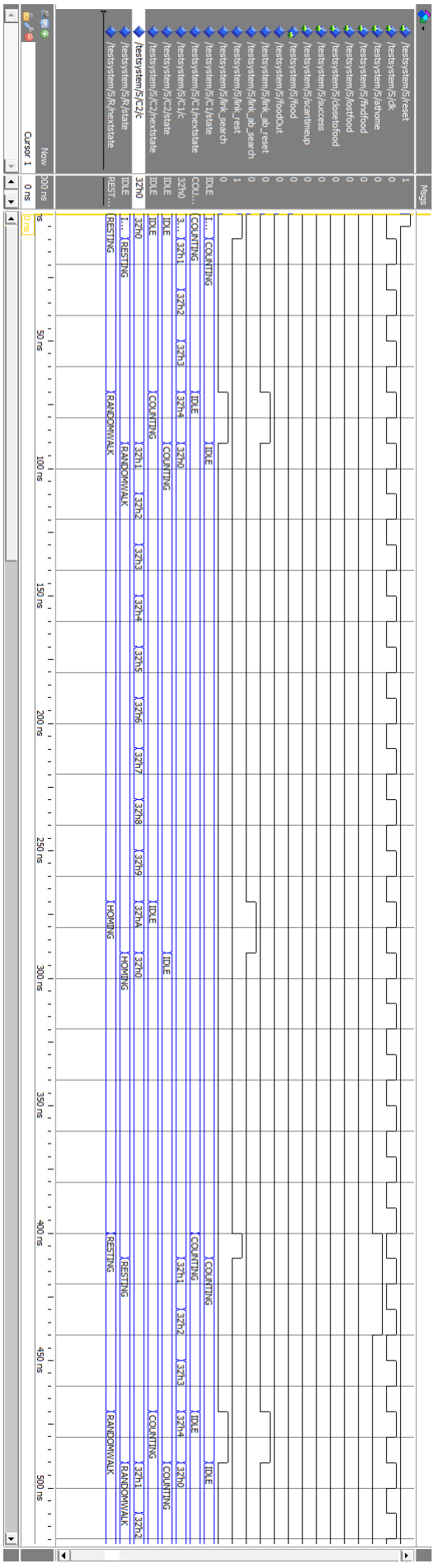

Figure 8: Simulation du robot : 0 ns à 500 ns

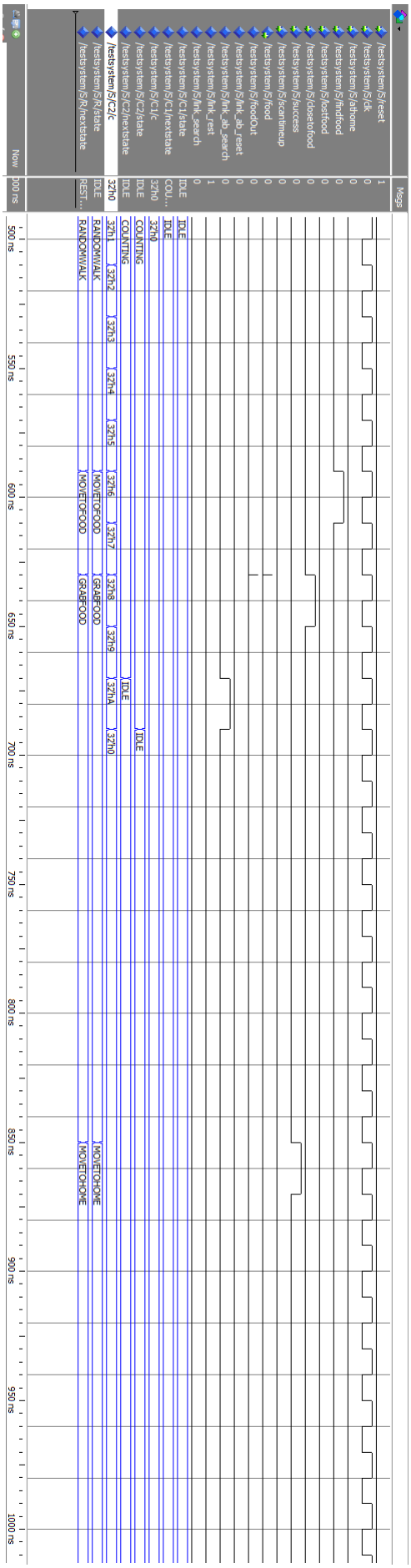

Figure 9: Simulation du robot : 500 ns à 1000 ns

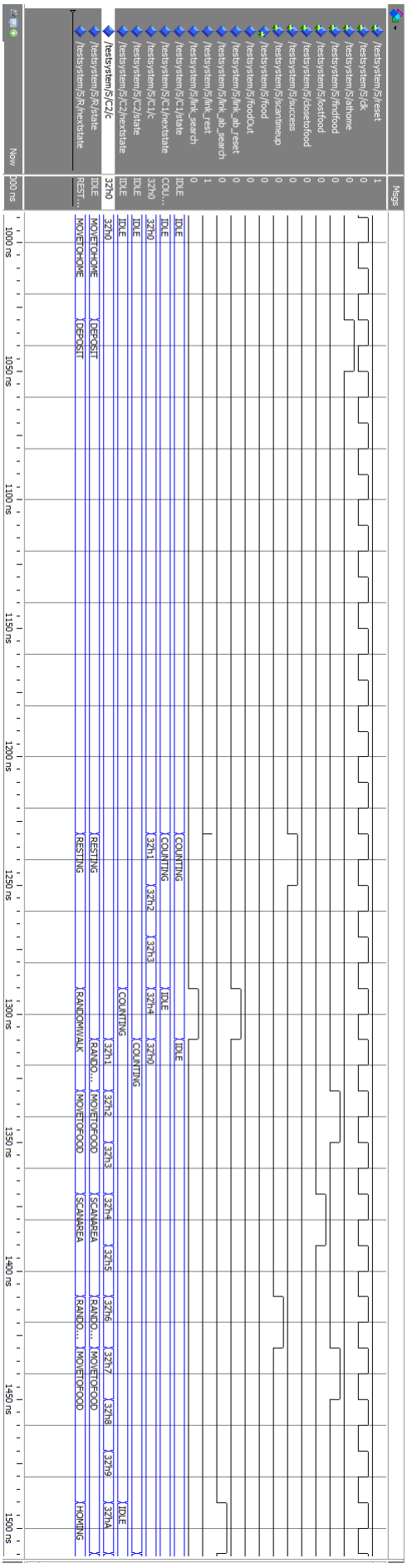

Figure 10: Simulation du robot : 1000 ns à 1500 ns

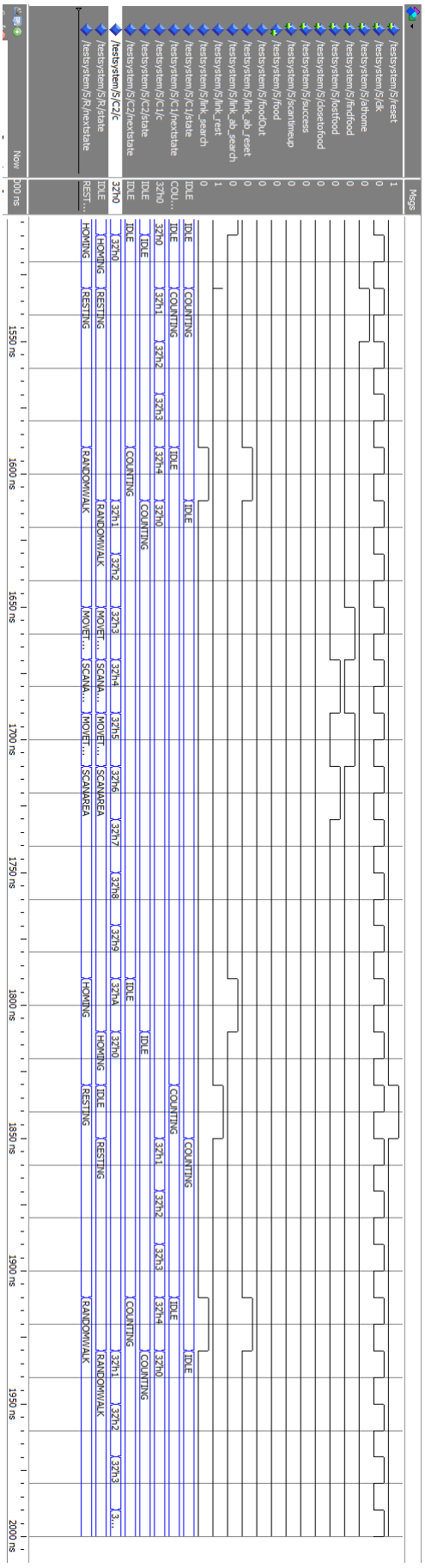

Figure 11: Simulation du robot : 1500 ns à 2000 ns