

# Egor Kokonin

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## Education

2023–2025 **Department of Radio Engineering and Computer Technology, Moscow Institute of Dolgoprudny, Physics and Technology (MIPT)**  
Russia

**GPA:** 8.4 / 10.0

### Core Courses:

- Mathematical Analysis, Linear Algebra, Computer Technology, Discrete Mathematics, Physics
- School of Digital Circuit Synthesis, Yadro
- Introduction to the Architecture of Computing Systems, Sber

## Skills

Programming languages System Verilog/Verilog, C/C++

Development Tools Git, Linux, FPGA,  $\LaTeX$ , cmake/make, bash, shell, Microsoft Office

Soft Skills Time Management, Teamwork, Communication, Self-development, Presentation skills, Report writing

## Projects

2023–2024 **Game** - creating your own game using the functionality of the PLIZ Nexus 7 Artix-7 on System Verilog at the hackathon from Sber

**Processor** - program is designed to execute bytecode instructions stored in a file in C

**Differentiator** - project is designed to perform symbolic differentiation on mathematical expressions represented as trees in C

**Cache** - implementation of one type of caching (LRU, LFU and "perfect" caches) in C++

**Stack, Two-Linked List, Binary tree** - basic data structures in C

## Languages

Russian Native Proficiency

English B1

## About me

- I am interested in circuit engineering and hardware development, and I study educational and technical literature.
- In my free time, I enjoy playing sports such as football, hockey, swimming, and gym classes, as well as going out with friends for walks and watching cartoons/TV series.