3827 OH

Eumin Hong (eh2890)

Columbia University

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Announcements

Announcements: Upcoming Assessments

- HW2 is due on Friday 2/11
- HW3 is due on Friday 2/18
- HW4 is due on Friday 2/25
- Homework is due at 11:59 pm (follow Gradescope time for deadline)

Announcements: Poll Results

- Online OH (except MIPS): 80%
- Email only: 62.5%
- Will create Slack for those who would like to join; will check frequently

Announcements: Feedback Form

• Form: https://forms.gle/cnUmKVNYN7WvRbHA6

Notable Concepts

Notable Concepts: 2's Complement Overflow Logic

- Why can you just check the last two carries to detect overflow in 2's complement?

 My complement?
- Let *P* be a positive number and *N* be a positive number. Consider the cases where the carries are different (implying overflow; carries are left to right):
- \rightarrow **1** P + P: if carries are 0 and 1, then result is negative
 - 2N + N: if carries are 1 and 0, then result is positive
 - - To see that the result should be positive, compare P and N
 - Let A be P without the MSB and B be N without MSB
 - In order for the second carry to be 1, A > B (unsigned comparison), so $A < \overline{B}$
 - We consider \overline{B} since we are actually comparing |P| and |N|, and so |P| < |N| and output should be negative
 - \bullet P+N: if carries are 1 and 0, then result is negative (same logic as above)

Notable Concepts: SoP, PoS

- Sum of Products: OR of product terms; e.g. $F = \overline{Y} + \overline{X} Y \overline{Z} + X Y \overline{Z}$
- Product of Sums: AND of sum terms; e.g. $F = X(\overline{Y} + Z)(X + Y + \overline{Z})$
- SoP and PoS are not the simplest form, just a canonical form
 - E.g. $F = X \oplus Y$ is simpler than $F_{SoP} = X\overline{Y} + \overline{X}Y$ and $F_{PoS} = (X + Y)(\overline{X} + \overline{Y})$

Notable Concepts: Minterms and Maxterms

- Minterm: product term with all variables appearing exactly once (either complemented or not); e.g. \overline{ABCD}
 - Evaluates to 1 for exactly one variable assignment combination, 0 otherwise
- Maxterm: sum term with all variables appearing exactly once (either complemented or not); e.g. $A + \overline{B} + \overline{C} + D$
 - Evaluates to 0 for exactly one variable assignment combination, 1 otherwise

$$A = 0$$
 $B = 1$
 $C = 1$
 $D = 0$

$$A = 0$$

$$B = ($$

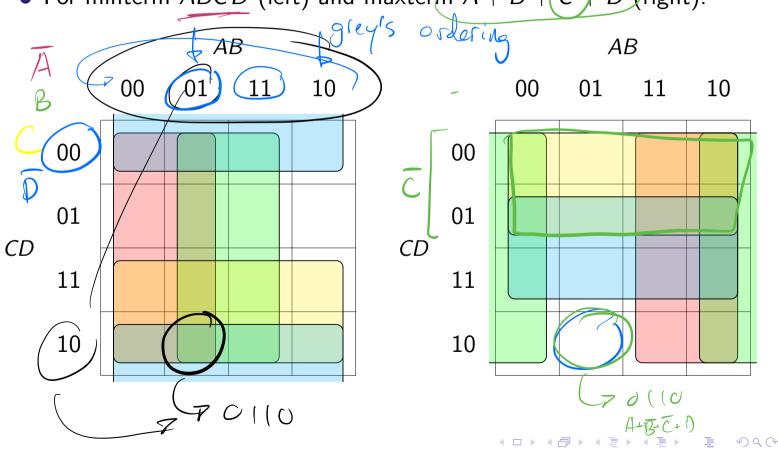
$$C = ($$

$$Q = 0$$

Notable Concepts: Minterms, Maxterms, and K-maps

How do minterms and maxterms relate to K-maps?

• For minterm $\overline{A}BC\overline{D}$ (left) and maxterm $A + \overline{B} + (\overline{C} + D)$ right):

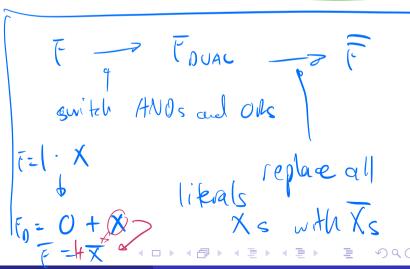


Notable Concepts: Converting SoP to PoS

- ① Compute \overline{F} : swap AND and OR operations, flip literals
- 2 Convert \overline{F} to SoP form
- **3** Compute $\overline{F} = F$: swap AND and OR operations, flip literals
- Complemented F twice
- Complementing F is SoP form gets \overline{F} in PoS\form
- Example: converting $F = \overline{A}\overline{C} + \overline{B}$ into PoS form

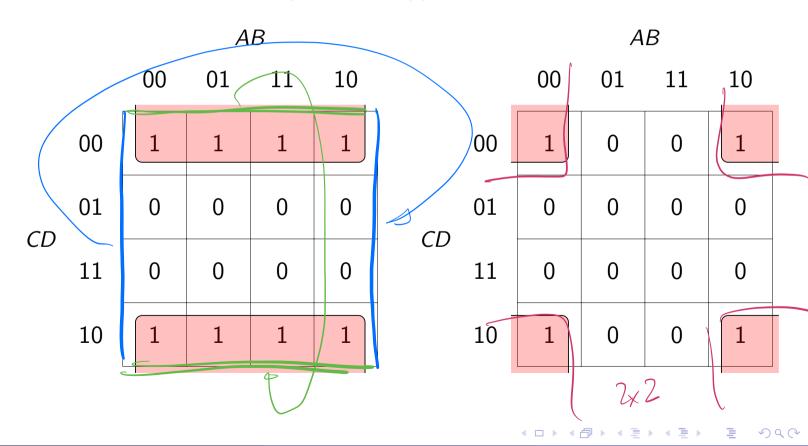
$$\overline{F} = AB + BC$$

$$\overline{F} = F = (\overline{A} + \overline{B})(\overline{B} + \overline{C})$$



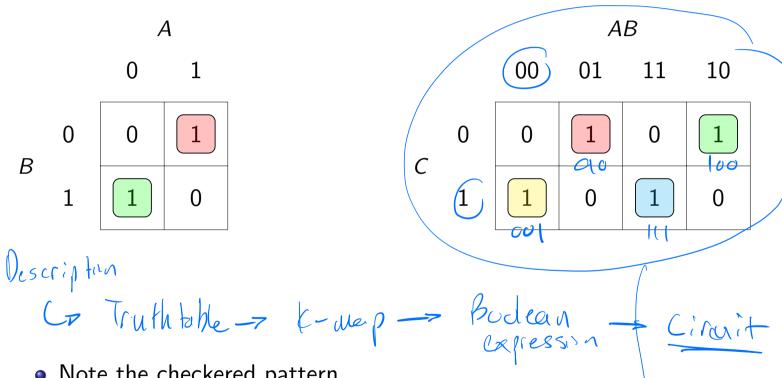
Notable Concepts: Simplifying with K-Maps

- Try to take largest essential prime implicant
- Remember that K-maps are "wrapped around":



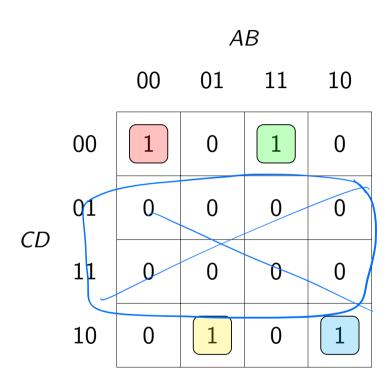
Notable Concepts: Recognizing XOR in K-Maps

• K-maps for $A \oplus B$ (left) and $A \oplus B \oplus C$ (right):



- Note the checkered pattern
- Minterms/maxterms have more literals than the XOR operations

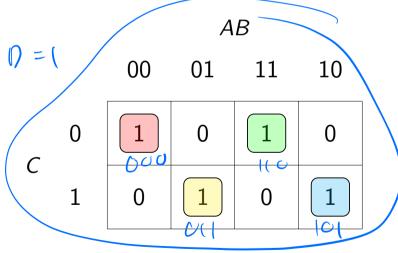
Notable Concepts: XOR in K-Maps Example



Note that the middle two rows
 (D) are all 0

Complements

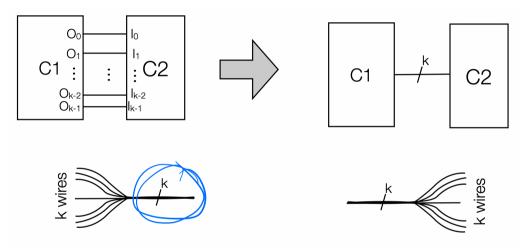
- The final expression is of the form $F = \overline{D}(F')$ where F' is mostly XORs
- Remove middle two rows:



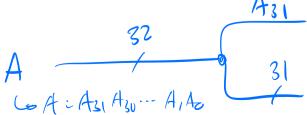
•
$$F = \overline{D}(\overline{A \oplus B \oplus C})$$
 complement

Notable Concepts: Multi-Wire Notation

• Can have multiple wires in parallel (from 3827_Lecture_04.pdf, slide 30):



- But remember: these are just wires
 - E.g. how to get the MSB of a k-bit number represented by k wires?



Notable Concepts: Combinational Circuits

• Enabler: for "turning on/off" inputs



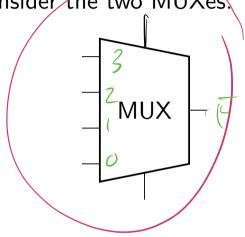
- High-level circuit for other circuits, "enable" functionality can be added with an input labelled "E"
- Decoder: set one of 2^k outputs to 1 (others are 0) based on unsigned binary representation of k-bit input
- Multiplexer (MUX): pass one of 2^k inputs as output based on k-bit "selector" input $Abc \longrightarrow BcA$ • Shifter: shifts bits of k-bit input
- - Can have input to control how much bits are shifted by, or can be hardcoded
- Adders (unsigned, 2's complement): half/full adders can make ripple carry adder
 - Carry lookahead also exists (reduced depth with increased parallelism)

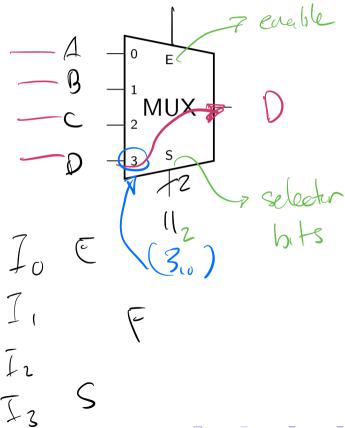
Dec (input GSZC & bits) Z

Notable Concepts: Decoder/MUX Notation

Must label everything in your combinational circuits

Consider the two MUXes;





Homework 2

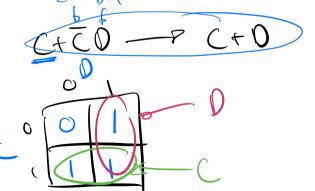
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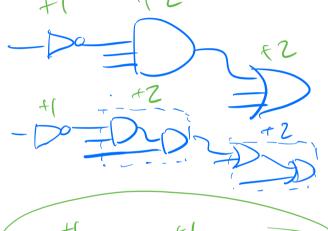
Simplify the following Boolean expressions to a minimum number of literals:

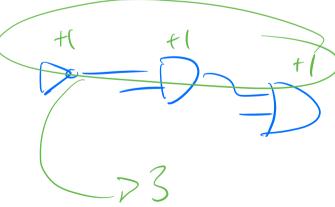
$$\begin{array}{ccc}
\hline
\mathbf{B}C + \overline{B}\overline{C}D \\
\hline
\mathbf{D} & \overline{(\overline{Y} + Z)}(Y + \overline{Z})
\end{array}$$

$$\overline{C}\overline{D} + C\overline{D}A + DA$$

$$(\overline{B}\overline{C} + BD + \overline{C}D)(\overline{B} + C + \overline{B}C)$$







Reduce the following Boolean expressions to the indicated number of literals:

$$ullet$$
 $\overline{Y}X + Y\overline{X}Z + \overline{Y}\overline{X}$ to three literals

$$\overline{Y}X(\overline{W} + ZW) + X(Y + \overline{Y}\overline{Z}W)$$
 to one literal

$$\begin{array}{l}
(+ \overline{z}(w + \overline{w})) \\
= (+ \overline{z}(w + \overline{y})) \\
= (+ \overline{z} + \overline$$

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Using DeMorgan's theorem (as many times as necessary), express the function $F = \overline{X}Z + X\overline{Z}Y + \overline{X}\overline{Y}$ with only:

- OR and complement operations
- AND and complement operations

Find the complement of the following expressions:

- $(C + \overline{B}D)(C + \overline{B} + \overline{D})(\overline{C}\overline{B} + \overline{D})$

Convert the following into sum-of-products and product-of-sums forms:

- $(\overline{A} + \overline{B}\overline{D})(\overline{D} + A\overline{C})$
- $(\overline{Z} + XW + \overline{W}Y)(\overline{X} + \overline{Z}\overline{Y})$
- 1) 50
- z) Pos

Simplify the following boolean expressions using a K-map:

Simplify in Sum-of-product form via a K-map (indicate what you identify as prime implicants and essential prime implicants). Recall that m(i) is the product term whose variables are complemented when and only when their position corresponds to a '0' in the binary representation of i, e.g., $m(5) = \overline{W}X\overline{Y}Z$.

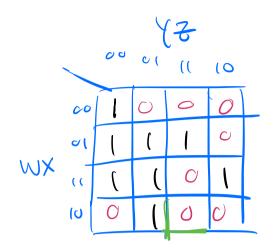
- (a) $F(W, X, Y, Z) = \sum_{m} m(0, 4, 5, 7, 9, 12, 13, 14)$

- $F(W, X, Y, Z) = \sum m(0, 1, 2, 5, 8, 9, 11, 12)$





o a a



Simplify into Sum-of-product form the following Boolean functions F together with the don't care conditions d:

- ① $F(W,X,Y,Z) = \sum m(0,1,3,5,7), d(W,X,Y,Z) = \sum m(2,4,6)$
- $F(A, B, C, D) = \sum m(2, 6, 7, 11, 14, 15), d(A, B, C, D) = \sum m(0, 8, 12, 13)$
- **o** $F(A, B, C, D) = \sum m(2, 7, 9, 10, 15), d(A, B, C, D) = \sum m(3, 6, 14)$