## Midterm

Mar. 27, 2018

Prof. Rubenstein

CSEE W3827 - Fundamentals of Computer Systems Spring 2017

This midterm contains 9 pages: a question 0 and 3 other questions, and totals 110 points. To get full credit you must answer all questions. **NO BOOKS, NOTES OR ELECTRONIC DEVICES PERMITTED!** The time allowed is 75 minutes.

Please answer all questions in the blue book, using a separate page for each question. Show all work! We are not just looking for the right answer, but also how you reached the right answer. Right answers without work get no credit!!!

YOU MAY KEEP YOUR COPY OF THE QUESTIONS.

Some advice:

- Be sure to leave some time to work on each problem. The right answer to each problem does not require a very long answer.
- Be sure to start every problem. And take some time to think about how to set the problem up before you start writing.

- 0. (5 pts) Do the following in the blue book:
  - (a) CLEARLY write your name and UNI on the front cover.
  - (b) start each numbered question's solution on a new page. So question 2 should start on a new page, question 3 on a new page, etc.
  - (c) label solutions (e.g., 2a, 2b, 2c or 2a, b, c)

1. (35 pts) Professor Rubenstein wants to start a gaming company called FunForTwoHands: automation for games involving 2 people and fun things they can do together, with one hand each.

Are you thinking what I'm thinking? Yes - the initial product will implement the 2-player hand game of Rock-Paper-Scissors, helping to identify the winner of each round.

The game of Rock-Paper-Scissors is played by two players (player 0 and player 1), where each round, the two players each simultaneously select one of the three options: Rock, Paper or Scissors. If both players choose the same object, the players tie. Otherwise, the winner is chosen as follows:

- Paper covers Rock (player choosing paper beats player choosing rock)
- Scissors cuts paper (player choosing scissors beats player choosing paper)
- Rock smashes scissors (player choosing rock beats player choosing scissors)

To automate the process, player 0's choice is described by the two-bit input AB, and player 1's choice by CD, where the 2-bit input indicates the following choice:

2-bit input		choice indicated
0	0	Rock
0	1	Paper
1	0	Scissors
1	1	(Unused)

Given inputs AB and CD that select one of the three 2-bit options above (i.e., you **should** assume that AB = 11 or CD = 11 will never be provided as an input), 2 outputs can be generated:

- T: outputs 1 when there is a tie, 0 otherwise.
- W: identifies the winner when the selections do not result in a tie (i.e., equals 0 when player 0 wins, 1 when player 1 wins).

For instance, if ABCD = 0001, then T = 0 and W = 1 since player 0 chose rock, and player 1 chose paper, and paper covers rock, hence player 1 wins.

Note that in the event of a tie, the W output is meaningless, since there is no winner.

As the circuit designer for Prof. Rubenstein, you need to provide **simplified SoP algebraic equations** for T and W as a function of the 4 inputs, A, B, C, D. Make sure your equations will produce circuits that, considered individually, are as simple as possible (in SoP form).

- 2. (35 pts) A k-UnsignedGreaterThan circuit (UGT $_k$  for short) takes two k-bit inputs,  $B = B_{k-1}B_{k-2}\cdots B_1B_0$  and  $A = A_{k-1}A_{k-2}\cdots A_1A_0$ , and returns 1 (TRUE) when B > A where both B and A represent unsigned k-bit binary values. The circuit can be built recursively via a combination of circuitry and UGT $_{k-1}$  circuits.
  - (a) (5 pts) Show a simplified design for the base case: a UGT<sub>1</sub> circuit, built using only AND, OR, and NOT gates, that returns TRUE when  $B_0 > A_0$  (note, we are requiring strictly greater here).



- (b) (8 pts) Now show how to build a  $UGT_k$  circuit for k > 1 recursively by using one or more  $UGT_{k-1}$  circuits and additional basic gates (i.e., AND, OR, NOT gates). Make sure your design clearly indicates which inputs feed in where. You should represent a  $UGT_{k-1}$  circuit as shown in the figure above when using it within your  $UGT_k$  circuit.
- (c) (7 pts) Now show how to build a general  $UGT_k$  recursively with one or more  $UGT_{k-1}$  circuits and 4-to-1 MUXes. Remember to clearly indicate how the inputs are feeding into the MUXes and the  $UGT_{k-1}$  circuits.
- (d) (8 pts) Suppose instead we wish to build a circuit that treats k-bit inputs A and B as signed 2s complement values. Show how to modify the design in part 2c for this purpose. Make sure you clearly indicate for which i the  $UGT_i$  can stay the same and for which it must be modified (as well as showing the modification).
- (e) (7 pts) Returning back to part (2b), suppose the circuit is represented in sum-of-products form (without further simplification). What circuit depth is achievable for the above design, where each AND, NOT, or OR gate used in series increases depth, and where AND and OR gates can take at most 2 inputs? Explain (in one or two sentences) why it has the depth you are claiming.

(Note: if you are finding part (e) difficult, realize it's only 7 points. Perhaps just move onto the next question, and come back after that one. Partial credit can also be received for answers that are approximately right, i.e., having the right intuition).

 Alan Brog

3. (35 pts) This is a MIPS coding problem. The function performed by the code are explained in great detail. However, the solution does not require a detailed understanding of what the code is doing. i.e., without even a detailed understanding, the problem can still be solved.

## What the code is supposed to do

For those interested in the details, the following describes a simple recursive process for multiplying two unsigned integers using only basic MIPS operations (i.e., SHIFT, AND and ADD).

- (i) If a1 is odd (lowest-order bit is 1), then set s0 = a0, otherwise set s0=0.
- (ii) shift-left a0 by 1 bit
- (iii) shift-right a1 by 1 bit
- (iv) Add s0 to the recursive call with the new a0, a1.
- (v) repeat until a1 = 0.

The following shows an example with a0 = 11 and a1 = 9, where the lowest-order bit of a1 is shown underlined and the recursion is expanded out one more iteration in each subsequent line.

```
1011 \cdot 1001 = 1011
                                  +(10110 \cdot 100)
             = 1011
                                                   +(101100 \cdot 10)
                                  +0
             = 1011
                                  +0
                                                   +0
                                                                    +(1011000 \cdot 1)
             = 1011
                                                                                      +(10110000 \cdot 0)
                                  +0
                                                   +0
                                                                    +1011000
             = 1011
                                  +0
                                                   +0
                                                                    +1011000
                                                                                      +0
             = 1011 + 1011000
             = 1100011 = 99
```

## The problem

The following page presents C code that implements the above procedure, using variables whose names correspond to MIPS registers. (i.e., a0, a1, v0, s0, t0). A **main** function loads in values for a0 and a1, and then calls a the recursive function **MIPSMult** to produce the result in v0. The program ends with a print statement that includes a0, a1 and v0.

On the page thereafter is MIPS code which is basically copied from the C-code. However, the code isn't running correctly, even for small values of a0 and a1. What changes should be made to the code? Give specific MIPS instructions and where they need to be added.

Here are some hints:

- The problem **does not** require modification to the code in the sections labeled "Input Info" and "Output Info" (i.e., lines those sections that basically use syscall). However, since those sections set and read values of a0, a1, v0, it may relate to those registers' values.
- The procedure may produce overflow for unsigned int inputs whose values are greater than  $2^{16}$ . You may assume the inputs are always smaller (i.e., not looking for a fix to the overflow problem).
- The code as-is returns the correct answer when a1 = 0 initially, but never terminates (is caught in some circular loop) for any other value of a1.

```
#include <stdlib.h>
   #include <stdio.h>
2
3
4
5
6
7
   int MIPSMult(int a0, int a1){
8
      unsigned long s0;
9
      unsigned long v0;
10
      unsigned long t0;
11
      if (a1==0){
12
          v0 = 0;
13
          return v0;
14
      }
15
16
      s0 = 0;
      t0 = a1 & 1;
17
18
      if (t0 != 0){
          s0 = a0;
19
      }
20
      a0 = a0 << 1;
21
      a1 = a1 >> 1;
22
      v0 = MIPSMult(a0,a1);
23
      v0 = v0 + s0;
24
25
      return v0;
   }
26
27
28
  int main(){
29
   30
   // Input info
31
      unsigned long a0, a1, v0;
32
      printf("Enter First Value: ");
33
      scanf("%lu", &a0);
34
      printf("Enter Second Value: ");
35
      scanf("%lu", &a1);
36
   // End Input Info
37
   38
39
      v0 = MIPSMult(a0,a1);
40
41
42
   // Output Info and terminate
43
      printf("%lu \times %lu = %lu \setminus n", a0, a1, v0);
44
      return 1;
45
   // End Output Info
46
   47
48
49
   }
50
51
52
```

(Intentionally left blank)

```
.data
1
2
                         .asciiz "Enter first value: "
   EnterFirstValue:
3
   EnterSecondValue:
                          .asciiz "Enter second value: "
4
5
   .text
6
7
   MIPSMult:
8
                                             ## C Code line 12
            bne $a1, $zero, NotBaseCase
9
                                             ## C Code line 13
            move $v0, $zero
10
                                             ## C Code line 13 cont'd
            jr $ra
11
   NotBaseCase:
12
            move $s0, $zero
                                             ## C Code line 16
13
            and $t0, $a1, 1
                                             ## C Code line 17
14
                                             ## C Code line 18
            beq $t0, $zero, SkipSets0
15
            move $s0, $a0
                                             ## C Code line 19
16
17
   SkipSets0:
                                             ## C Code line 21
18
            sll $a0, $a0, 1
            srl $a1, $a1, 1
                                             ## C Code line 22
19
            jal MIPSMult
                                             ## C Code line 23
20
            add $v0, $v0, $s0
                                             ## C Code line 24 & 25
21
                                             ## C Code line 26
            jr $ra
22
23
24
25
   ######### Input Info: this snippet correctly
26
   ########## implements C code lines 31-36
27
            la $a0, EnterFirstValue
                                             ## C code line 33
28
            li $v0, 4
29
30
            syscall
                                             ## C code line 34
            li $v0,
                    5
31
            syscall
32
                                             ## result should go in $a0
            move $t0, $v0
33
                                             ## but syscall will use $a0
34
                                             ## so keep in $t0 for now
35
            la $a0, EnterSecondValue
                                             ## C code line 35
36
            li $v0,
37
            syscall
38
            li $v0,
                                             ## C code line 36
39
            syscall
40
                                             ## Move values in $v0 and
41
            move $a1, $v0
            move $a0, $t0
                                             ## $t0 respectively
42
                                             ## into $a1 and $a0
43
44
   ########## End Input Info
45
46
            jal MIPSMult
                                             ## C Code line 40
47
48
49
```

50

(This page may be detached from the rest of the exam)

50 ## This page intentionally left blank

```
########### Output Info: this snippet correctly
51
   ############## implements C code lines 44 & 45
52
53
           move $s0, $v0
                                ## Saving $v0 (in $s0) since
54
                                ## needed for syscall
55
                                ## C code line 44, first %lu
            li $v0, 1
56
57
            syscall
            li $a0, 'x'
                                ## C code line 44, print "x"
58
            li $v0, 11
59
            syscall
60
           move $a0, $a1
                                ## move $a1 into $a0 for
61
                                ## printing (syscall reqm't)
62
            li $v0,1
                                ## C code line 44 second %lu
63
            syscall
64
            li $a0, '='
                                ## C code line 44, print "="
65
            li $v0, 11
66
67
            syscall
                                ## solution, initially in $v0,
68
           move $a0, $s0
                                ## now in $so, move into
69
                                ## $a0 for printing (syscall reqm't)
70
                                ## C code line 44, third %lu
            li $v0, 1
71
            syscall
72
            li $a0, '\n'
                                ## C code line 44, print "\n" (newline)
73
            li $v0, 11
74
            syscall
75
            li $v0, 10
                                ## Exit program (similar to C code line 45)
76
            syscall
77
78
   ########## End Output Info
79
80
   ############ THIS IS THE LAST PAGE OF THE EXAM
81
```