

Universal NAND and NOR

Ella Xu, Digital Electronics

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1 Alarm

1.1 AOI

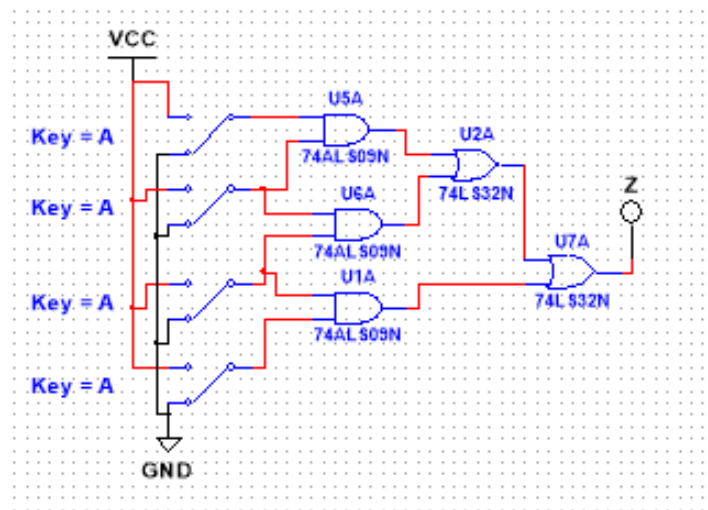


Figure 1: This is the AOI logic circuit of the Alarm.

1.2 Universal NOR

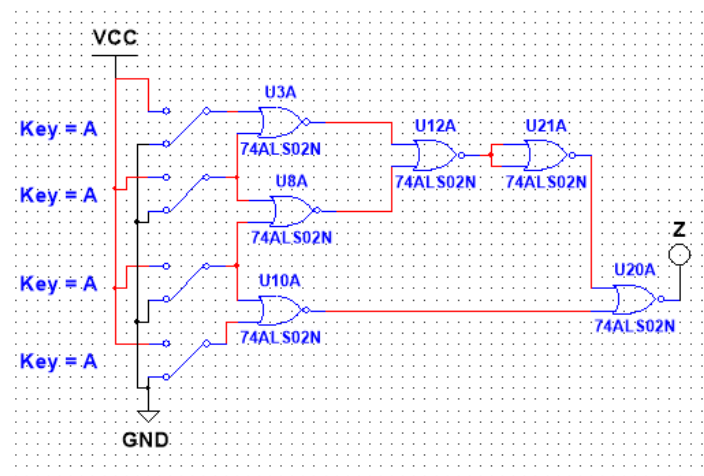


Figure 2: This is the logic circuit for the Alarm using universal NOR gates.

1.3 Universal NAND

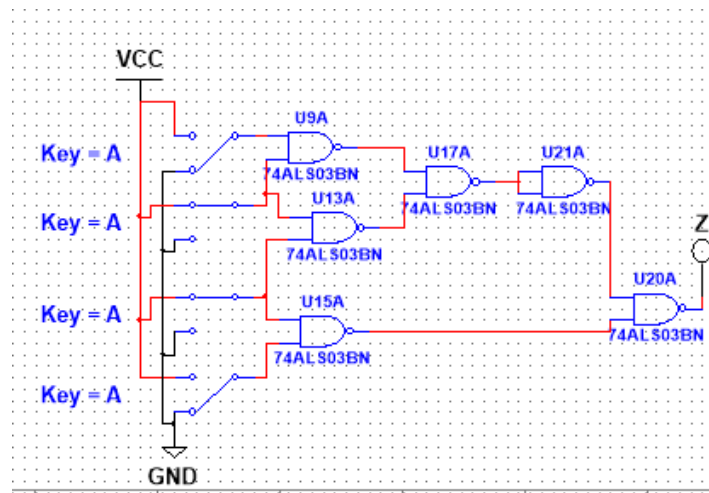


Figure 3: This is the logic circuit for the Alarm using universal NAND gates.

2 Booth

2.1 AOI

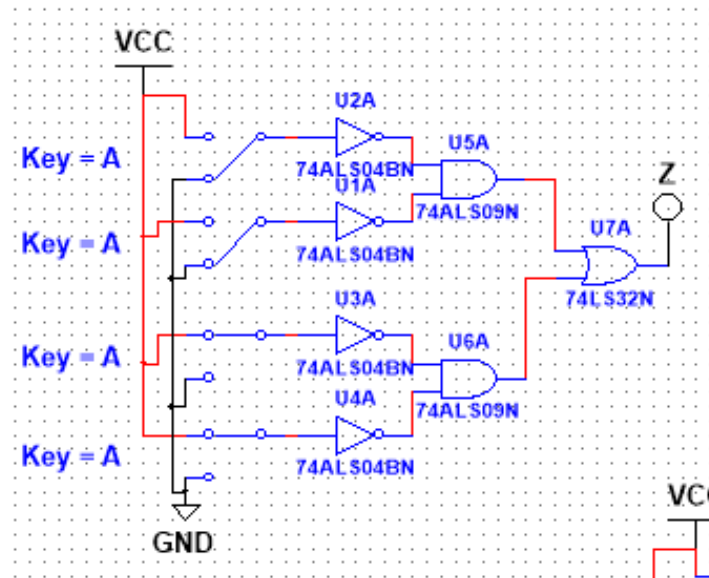


Figure 4: This is the AOI logic circuit of the Booth.

2.2 Universal NOR

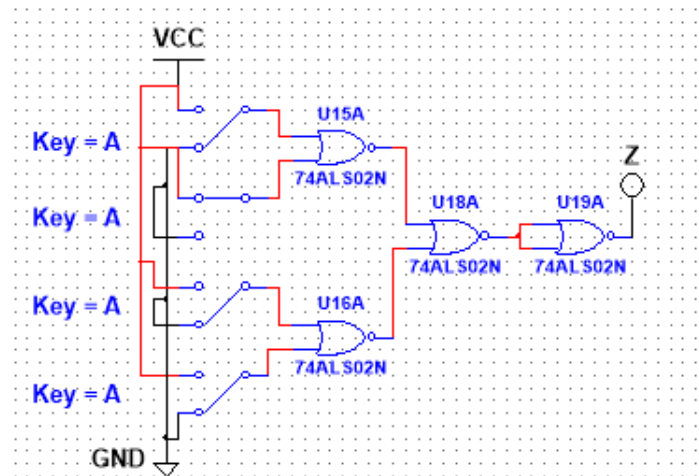


Figure 5: This is the logic circuit for the Booth using universal NOR gates.

2.3 Universal NAND

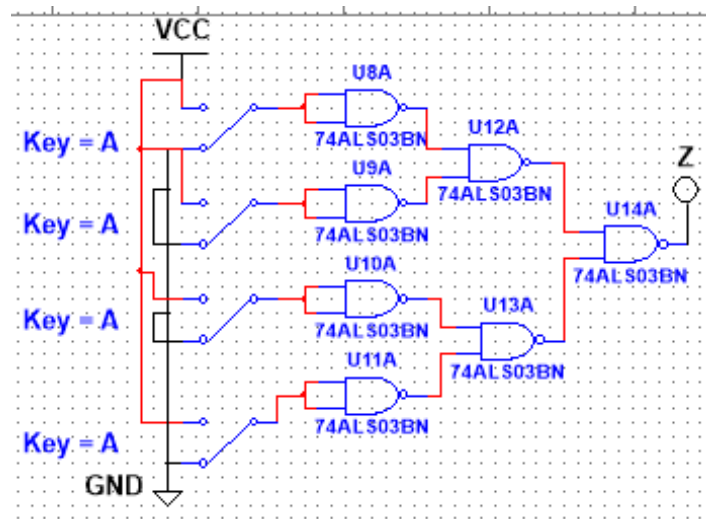


Figure 6: This is the logic circuit for the Booth using universal NAND gates.