

**Recommendations:**

**Hotspots: Start with Hotspots analysis to understand the efficiency of your algorithm.**

Use Hotspots analysis to identify the most time consuming functions. Drill down to see the time spent on every line of code.

**Microarchitecture Exploration: There is low microarchitecture usage (36.9%) of available hardware resources. of Pipeline Slots**

Run Microarchitecture Exploration analysis to analyze CPU microarchitecture bottlenecks that can affect application performance.

**Threading: There is poor utilization of logical CPU cores (30.7%) in your application.**

Use Threading to explore more opportunities to increase parallelism in your application.

**Elapsed Time:** 46.849s

**CPU:**

**IPC:** 1.205

**SP GFLOPS:** 0.000

**DP GFLOPS:** 0.051

**x87 GFLOPS:** 0.005

**Average CPU Frequency:** 2.021 GHz

**GPU:**

**Time:** 0.1% (0.062s) of Elapsed time

GPU utilization is low. Consider offloading more work to the GPU to increase overall application performance.

**IPC Rate:** 1.565

**Effective Logical Core Utilization:** 30.7% (2.453 out of 8)

The metric value is low, which may signal a poor logical CPU cores utilization. Consider improving physical core utilization as the first step and then look at opportunities to utilize logical cores, which in some cases can improve

processor throughput and overall performance of multi-threaded applications.

**Effective Physical Core Utilization:** 53.4% (2.136 out of 4)

The metric value is low, which may signal a poor physical CPU cores utilization caused by:

- load imbalance
- threading runtime overhead
- contended synchronization
- thread/process underutilization
- incorrect affinity that utilizes logical cores instead of physical cores

Explore sub-metrics to estimate the efficiency of MPI and OpenMP parallelism or run the Locks and Waits analysis to identify parallel bottlenecks for other parallel runtimes.

**Microarchitecture Usage:** 36.9% of Pipeline Slots

You code efficiency on this platform is too low.

Possible cause: memory stalls, instruction starvation, branch misprediction or long latency instructions.

Next steps: Run Microarchitecture Exploration analysis to identify the cause of the low microarchitecture usage efficiency.

**Retiring:** 36.9% of Pipeline Slots  
**Front-End Bound:** 39.5% of Pipeline Slots

Issue: A significant portion of Pipeline Slots is remaining empty due to issues in the Front-End.

Tips: Make sure the code working size is not too large, the code layout does not require too many memory accesses per cycle to get enough instructions for filling four pipeline slots, or check for microcode assists.

<b>Back-End Bound:</b>	15.3% of Pipeline Slots
<b>Memory Bound:</b>	7.5% of Pipeline Slots
<b>Core Bound:</b>	7.8% of Pipeline Slots
<b>Bad Speculation:</b>	8.3% of Pipeline Slots

<b>Memory Bound:</b>	7.5% of Pipeline Slots
<b>L1 Bound:</b>	10.8% of Clockticks
<b>L2 Bound:</b>	1.5% of Clockticks
<b>L3 Bound:</b>	7.9% of Clockticks
<b>DRAM Bound:</b>	1.0% of Clockticks
<b>DRAM Bandwidth Bound:</b>	0.0% of Elapsed Time
<b>Store Bound:</b>	0.4% of Clockticks

**Vectorization:** 1.7% of Packed FP Operations

A significant fraction of floating point arithmetic instructions are scalar. Use Intel Advisor to see possible reasons why the code was not vectorized.

<b>Instruction Mix:</b>	
<b>SP FLOPs:</b>	0.0% of uOps
<b>Packed:</b>	0.2% from SP FP
<b>128-bit:</b>	0.2% from SP FP
<b>256-bit:</b>	0.1% from SP FP
<b>Scalar:</b>	99.8% from SP FP

A significant fraction of floating point arithmetic instructions are scalar. Use Intel Advisor to see possible reasons why the code was not vectorized.

<b>DP FLOPs:</b>	0.8% of uOps
<b>Packed:</b>	1.8% from DP FP
<b>128-bit:</b>	1.8% from DP FP
<b>256-bit:</b>	0.0% from DP FP
<b>Scalar:</b>	98.2% from DP FP

A significant fraction of floating point arithmetic instructions are scalar. Use Intel Advisor to see possible reasons why the code was not vectorized.

<b>x87 FLOPs:</b>	0.1% of uOps
<b>Non-FP:</b>	99.1% of uOps

**FP Arith/Mem Rd Instr. Ratio:** 0.034

The metric value is low. This can be a result of unaligned access to data for vector operations. Use Intel Advisor to find possible data access inefficiencies for vector operations.

**FP Arith/Mem Wr Instr. Ratio:** 0.063

The metric value is low. This can be a result of unaligned access to data for vector operations. Use Intel Advisor to find possible data access inefficiencies for vector operations.

**GPU Active Time:** 0.1%

GPU utilization is low. Consider offloading more work to the GPU to increase overall application performance.

**GPU Utilization when Busy:** 61.7%

The percentage of time when the EUs were stalled or idle is high, which has a negative impact on compute-bound applications.

<b>IPC Rate:</b>	1.565
<b>EU State:</b>	61.7%
<b>Active:</b>	61.7%
<b>Stalled:</b>	24.7%

A significant portion of GPU time is lost due to stalls. For compute-bound code, this could indicate that performance is limited by memory or sampler accesses.

**Idle:** 13.6%

A significant portion of GPU time is spent idle. This is usually caused by imbalance or thread scheduling problems.

**Occupancy:** 73.5% of peak value

Low value of the occupancy metric may be caused by inefficient work scheduling. Make sure work items are neither too small nor too large.

### Collection and Platform Info:

**Application Command Line:** ./codecs/hm/encoder/TAppEncoderStatic "-c" "./configs/hm/encoder\_intra\_main.cfg" "-i" "./sequences/CLASS\_B/BasketballPass\_416x240\_50.yuv" "-wdt" "416" "-hgt" "240" "-b" "./bin/hm/encoder\_intra\_main.cfg/CLASS\_B/BasketballPass\_416x240\_50\_QP\_27\_hm.bin" "-o" "./rec\_yuv/hm/encoder\_intra\_main.cfg/CLASS\_B/BasketballPass\_416x240\_50\_QP\_27\_hm.yuv" "-fr" "50" "-fs" "0" "-f" "50" "-q" "27"

**Operating System:** 5.4.0-65-generic DISTRIB\_ID=Ubuntu  
DISTRIB\_RELEASE=18.04 DISTRIB\_CODENAME=bionic  
DISTRIB\_DESCRIPTION="Ubuntu 18.04.5 LTS"

**Computer Name:** eimon

**Result Size:** 5.1 MB

**Collection start time:** 00:38:52 10/02/2021 UTC

**Collection stop time:** 00:39:39 10/02/2021 UTC

**Collector Type:** Event-based sampling driver,Event-based counting driver

### CPU:

**Name:** Intel(R) Processor code named Kabylake ULX

**Frequency:** 1.992 GHz

**Logical CPU Count:** 8

**Max DRAM Single-Package Bandwidth:** 11.000 GB/s

### Cache Allocation Technology:

**Level 2 capability:** not detected

**Level 3 capability:** not detected

### GPU:

**Name:** Display controller: Intel Corporation Device 22807

<b>Vendor:</b>	Intel Corporation
<b>EU Count:</b>	24
<b>Max EU Thread Count:</b>	7
<b>Max Core Frequency:</b>	1.150 GHz