Elapsed Time: 628.197s

Clockticks: 1,407,020,400,000 **Instructions Retired:** 3,511,652,400,000

CPI Rate: 0.401 MUX Reliability: 1.000

Retiring: 64.1% of Pipeline Slots **Light Operations:** 59.3% of Pipeline Slots

 FP Arithmetic:
 1.1% of uOps

 FP x87:
 0.1% of uOps

 FP Scalar:
 1.1% of uOps

 FP Vector:
 0.0% of uOps

 Other:
 98.9% of uOps

Heavy Operations:Microcode Sequencer:
Assists:
4.7% of Pipeline Slots
1.1% of Pipeline Slots
0.0% of Pipeline Slots

Front-End Bound:

Front-End Latency:
ICache Misses:
ITLB Overhead:
Branch Resteers:
Mispredicts Resteers:
Clears Resteers:

0.0 % of Pipeline Slots
6.6% of Pipeline Slots
2.7% of Clockticks
3.3% of Clockticks
3.3% of Clockticks
0.0% of Clockticks

Unknown Branches: 0.6% of Clockticks
DSB Switches: 1.7% of Clockticks
Length Changing Prefixes: 0.0% of Clockticks
MS Switches: 0.8% of Clockticks
Front-End Bandwidth: 12.6% of Pipeline Slots
Front-End Bandwidth MITE: 30.7% of Clockticks

Front-End Bandwidth DSB: 5.4% of Clockticks

(Info) DSB Coverage: 31.8%

Bad Speculation: 7.3% of Pipeline Slots **Branch Mispredict:** 7.1% of Pipeline Slots **Machine Clears:** 0.1% of Pipeline Slots

```
9.5% of Pipeline Slots
Back-End Bound:
                              2.1% of Pipeline Slots
  Memory Bound:
     L1 Bound:
                                 4.5% of Clockticks
        DTLB Overhead:
                                    8.2% of Clockticks
           Load STLB Hit:
                                       8.2% of Clockticks
           Load STLB Miss:
                                       0.0% of Clockticks
        Loads Blocked by Store Forwarding: 5.3% of Clockticks
                                    0.0% of Clockticks
        Lock Latency:
        Split Loads:
                                    0.0% of Clockticks
        4K Aliasing:
                                    1.5% of Clockticks
        FB Full:
                                    0.7% of Clockticks
     L2 Bound:
                                 0.4% of Clockticks
     L3 Bound:
                                 0.4% of Clockticks
        Contested Accesses:
                                    0.0% of Clockticks
        Data Sharing:
                                    0.0% of Clockticks
        L3 Latency:
                                    1.9% of Clockticks
                                    0.1% of Clockticks
        SO Full:
     DRAM Bound:
                                 0.1% of Clockticks
        Memory Bandwidth:
                                    0.7% of Clockticks
        Memory Latency:
                                    2.9% of Clockticks
     Store Bound:
                                 0.8% of Clockticks
        Store Latency:
                                    9.0% of Clockticks
                                    0.0% of Clockticks
        False Sharing:
        Split Stores:
                                    0.2% of Clockticks
        DTLB Store Overhead:
                                    7.9% of Clockticks
           Store STLB Hit:
                                       7.9% of Clockticks
                                       0.0% of Clockticks
           Store STLB Hit:
  Core Bound:
                               7.5% of Pipeline Slots
     Divider:
                                 0.7% of Clockticks
                                 22.2% of Clockticks
     Port Utilization:
        Cycles of 0 Ports Utilized: 5.8% of Clockticks
           Serializing Operations:
                                     0.6% of Clockticks
           Mixing Vectors:
                                       0.0% of uOps
        Cycles of 1 Port Utilized: 5.0% of Clockticks
        Cycles of 2 Ports Utilized: 8.8% of Clockticks
        Cycles of 3+ Ports Utilized: 30.7% of Clockticks
           ALU Operation Utilization: 38.2% of Clockticks
              Port 0:
                                          32.5% of Clockticks
              Port 1:
                                          38.5% of Clockticks
                                          37.6% of Clockticks
              Port 5:
                                          44.2% of Clockticks
              Port 6:
           Load Operation Utilization:
                                         36.0% of Clockticks
              Port 2:
                                          45.0% of Clockticks
                                          46.2% of Clockticks
              Port 3:
           Store Operation Utilization:
                                          37.7% of Clockticks
              Port 4:
                                          37.7% of Clockticks
              Port 7:
                                          18.5% of Clockticks
        Vector Capacity Usage (FPU): 24.4%
Average CPU Frequency: 2.274 GHz
Total Thread Count:
                            1
Paused Time:
                            0s
```

Effective Physical Core Utilization: 24.5% (0.979 out of 4)

The metric value is low, which may signal a poor physical CPU cores utilization caused by:

- load imbalance
- threading runtime overhead
- contended synchronization
- thread/process underutilization
- incorrect affinity that utilizes logical cores instead of physical cores

Explore sub-metrics to estimate the efficiency of MPI and OpenMP parallelism or run the Locks and Waits analysis to identify parallel bottlenecks for other parallel runtimes.

Effective Logical Core Utilization: 12.3% (0.985 out of 8)

The metric value is low, which may signal a poor logical CPU cores utilization. Consider improving physical core utilization as the first step and then look at opportunities to utilize logical cores, which in some cases can improve processor throughput and overall performance of multi-threaded applications.

Collection and Platform Info:

Application Command Line: ./codecs/hm/encoder/TAppEncoderStatic "-c" "./configs/hm/encoder_intra_main.cfg" "-i" "./sequences/CLASS_A/Kimono_1920x1080_24.yuv" "-wdt" "1920" "-hgt" "1080" "-b" "./bin/hm/encoder_intra_main.cfg/CLASS_A/Kimono_1920x1080_24_QP_27_hm.bin" "-o" "./rec_yuv/hm/encoder_intra_main.cfg/CLASS_A/Kimono_1920x1080_24_QP_27_hm.yuv" "-fr" "24" "-fs" "0" "-f" "50" "-q" "27"

User Name: root

Operating System: 5.4.0-65-generic DISTRIB_ID=Ubuntu DISTRIB_RELEASE=18.04 DISTRIB_CODENAME=bionic DISTRIB_DESCRIPTION="Ubuntu 18.04.5 LTS"

Computer Name: eimon

Result Size: 905.0 MB

Collection start time: 22:20:08 09/02/2021 UTC

Collection stop time: 22:30:36 09/02/2021 UTC

Collector Type: Event-based sampling driver

CPU:

Name: Intel(R) Processor code named Kabylake

ULX

Frequency: 1.992 GHz

Logical CPU Count: 8

Cache Allocation Technology:

Level 2 capability: not detected

Level 3 capability: not detected