Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science

Proposal for Thesis Research in Partial Fulfillment of the Requirements for the Degree of Doctor of Philosophy

TITLE: Parallel Processor Architecture

SUBMITTED BY: Peter Nuth

305 Memorial Drive, 606C Cambridge, MA 02139

(Signature of Author)

Date of Submission: February 28, 2018
Expected Date of Completion: September 1990

Laboratory: Artificial Intelligence Laboratory

Brief Statement of the Problem:

The proposed research is a study of processor architecture for large scale parallel computer systems. The thesis introduces mechanisms for fast context switching, synchronization between tasks, and run-time binding of variable names to processor memory. Various design tradeoffs are evaluated through simulation of a processor running a typical load. This work contains estimates of the speed and complexity of the different alternatives as implemented in VLSI.

Doctoral Thesis Supervision Agreement

Professor William J. Dally

From:

Department Graduate Committee

The program outlined in the proposal:				
TITLE: Parallel Processor Architecture AUTHOR: Peter Nuth DATE: February 28, 2018				
is adequate for a Doctoral thesis. I believe that appropriate readers for this thesis would be:				
READER 1: Professor Arvind READER 2: Professor Thomas Knight				
Facilities and support for the research outlined in the proposal are available. I am willing to supervise the thesis and evaluate the thesis report.				
Signed:				
	ASSOCIATE PROFESSOR OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE			
DATE:				
Comments:				

Doctoral Thesis Reader Agreement

	partment Graduate Committee	
From: P	ofessor Arvind	
The program	outlined in the proposal:	
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	or a Doctoral thesis. I am willing to aid in guiding the research g the thesis report as a reader.	
	SIGNED: PROFESSOR OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE	<u></u>
	Date:	
Comments:		

Doctoral Thesis Reader Agreement

To:	Departn	nent Graduate Committee	
From:	Professo	r Thomas Knight	
The prog	gram outli	ned in the proposal:	
	TITLE: AUTHOR: DATE:	Parallel Processor Architec Peter Nuth February 28, 2018	ture
	PERVISOR: READER:	Professor William J. Dally Professor Arvind	
		Octoral thesis. I am willing thesis report as a reader.	o aid in guiding the research
		Signed:	Assistant Professor of Electrical Engineering and Computer Science
		DATE:	
Commen	nts:		

Doctoral Thesis Reader Agreement

_	nent Graduate Committee or William J. Dally			
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The program outli	ned in the proposal:			
TITLE:	Parallel Processor Architec	turo		
AUTHOR:	Peter Nuth			
DATE:	February 28, 2018			
Supervisor:	Professor William J. Dally			
Other Reader:	Professor Arvind			
OTHER READER:	Professor Thomas Knight			
	Octoral thesis. I am willing thesis report as a reader.	to aid in guiding the research		
	SIGNED:			
		Associate Professor of Electrical Engineering and Computer Science		
	Date:			
Comments:				