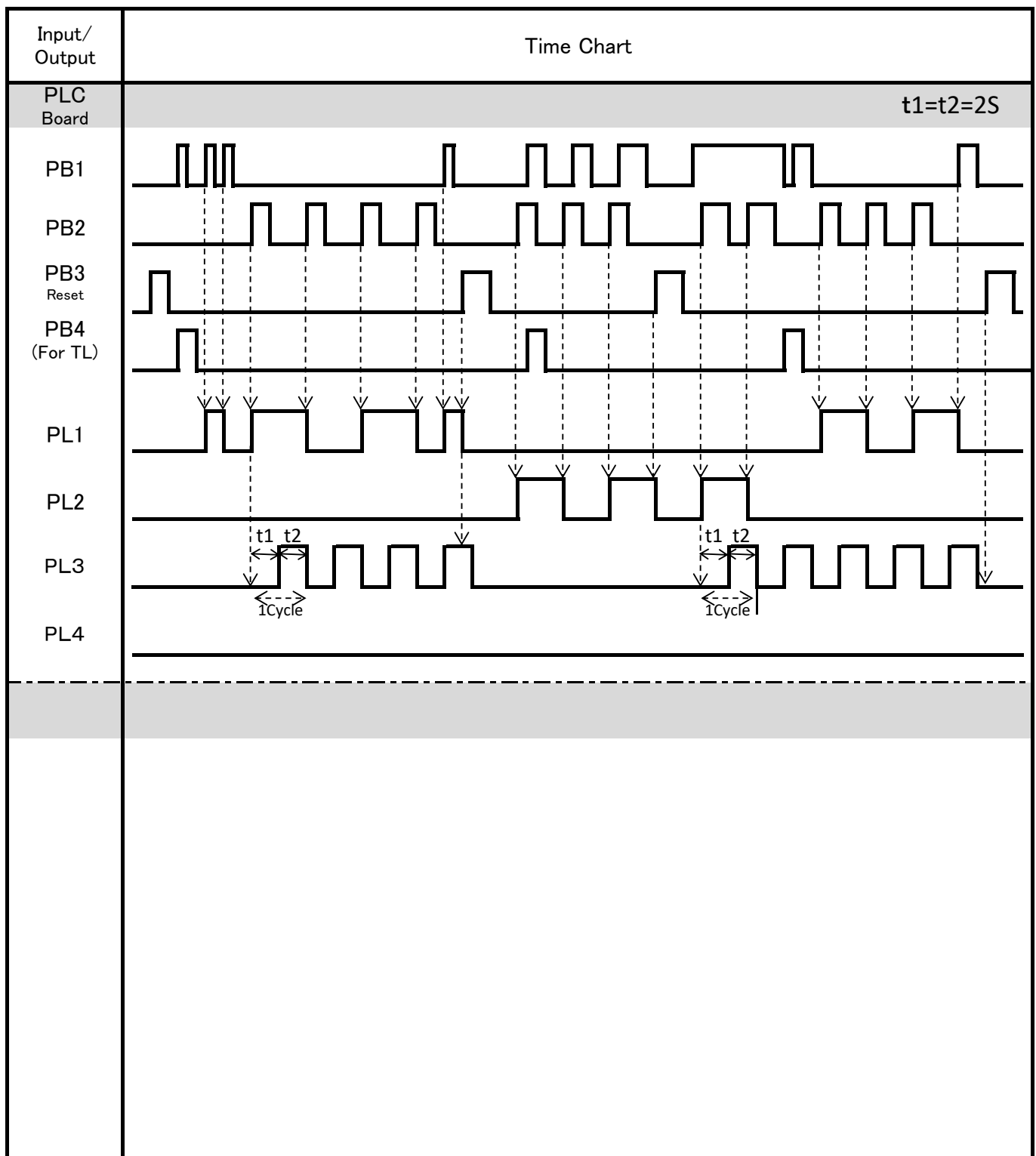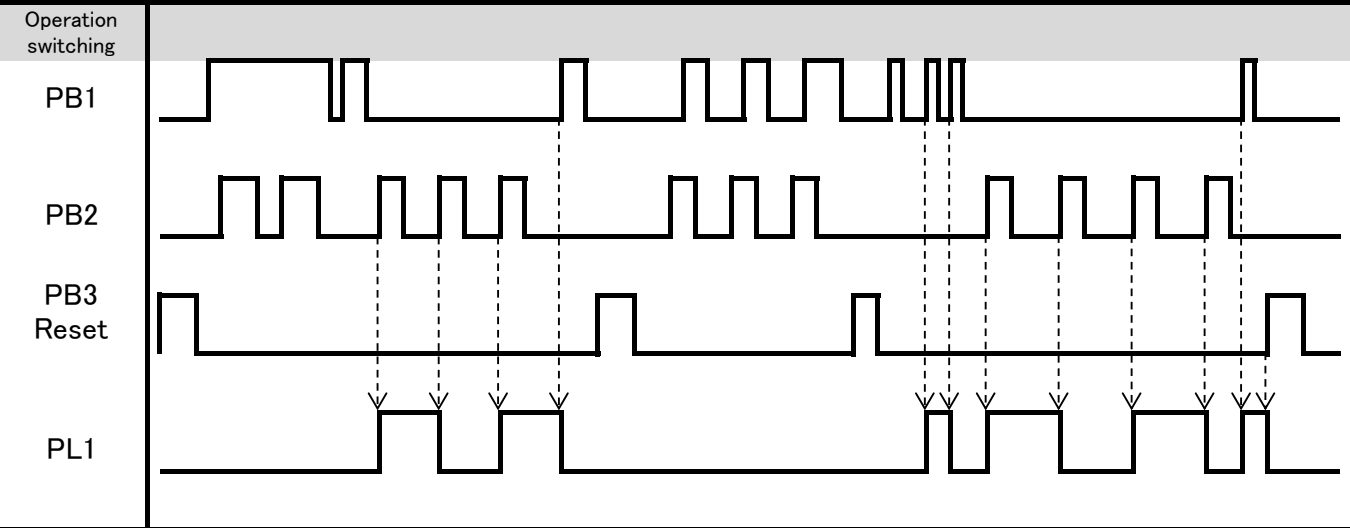# Assignment 1 PLC circuit design

Assignment content

(1) Design the ladder circuit reference to the time chart.

(2) Follow the specifications and instructions (timer settings, count timing, etc.).

(3) Operate repeatedly (twice or more) according to the time chart.

Design ladder circuit after the final circuit, and Do not modify the existing circuit such as the lamp check circuit.

※ Buttons are PB1 (X000), PB2 (X001), PB3 (X002), PB4 (X003); Lamps are PL 1 (Y011), PL2 (Y012), PL3 (Y013), PL4 (Y014)

[TM] use only basic commands (application commands cannot be used), but edge detection (P) can be used.

[TL] Basic and application commands can be used (no restrictions)

Complete data reading and writing with FL−NET communication destinations.

For communication settings, refer to the FL−NET communication area diagram

Input/ Output	Time Chart
PLC Board	t1=t2=2S

PB1

PB2

PB3 Reset

PB4 (For TL)

PL1

PL2

PL3   t1   t2   t1   t2
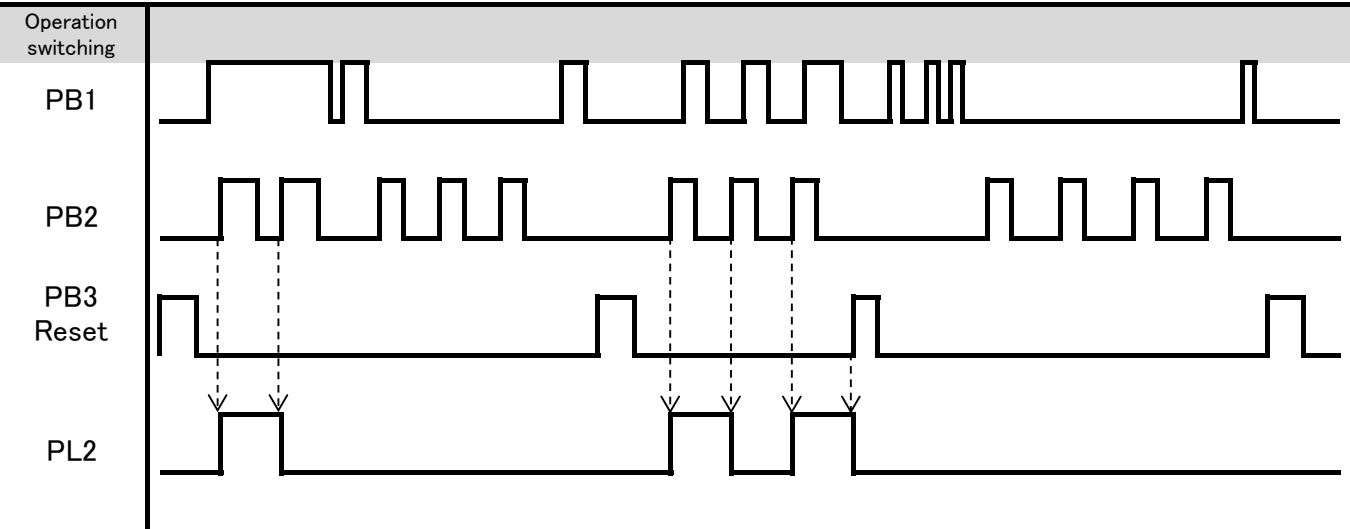
1Cycle   1Cycle

PL4

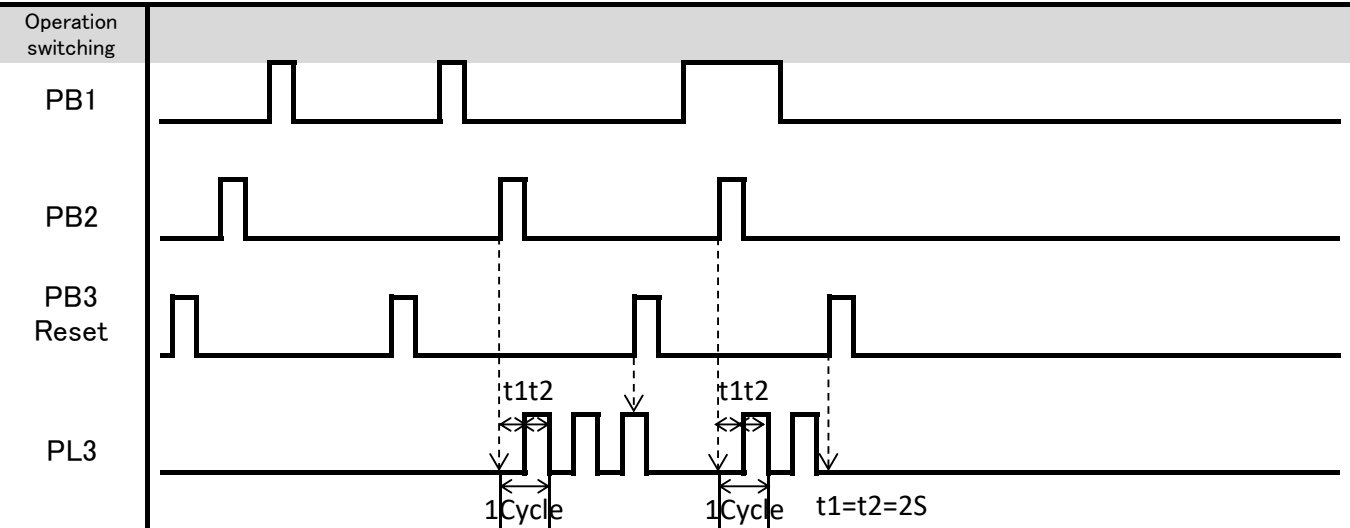# Specification instructions (Being able to operate repeatedly. No need to consider HL other than instructions)

【PL1】Specification check cycle chart

Operation switching	
PB1	
PB2	
PB3 Reset	
PL1	

【PL2】Specification check cycle chart

Operation switching	
PB1	
PB2	
PB3 Reset	
PL2	

【PL3】Specification check cycle chart

Operation switching	
PB1	
PB2	
PB3 Reset	
PL3	t1t2 ... t1t2 ... 1Cycle 1Cycle t1=t2=2S

Applied problem [Multi-sequence] Specification check

peration switching	There is no relation to actions ① and ②.

Prepared Edy Kozwara 7/5/2024