Computer Organization and Architecture (EET2211)

LAB V: Analyze and evaluate different Array operations using ARM processor.

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Branch:		**Section:**	
S. No.	**Name**	**Registration No.**	**Signature**

Marks: _____/10

Remarks:

Teacher's Signature

I. OBJECTIVE:

1. Find the largest/smallest number in an array of size N.
2. Separate Even numbers and odds numbers in an array of size N.

II. PRE-LAB

- Explain the addressing modes involved in instructions.

For each objective in prelab describe the following points:

- Write the assembly code with description (ex. Mov ax,3000h – ax<-3000h)
- Examine & analyze the input/output of assembly code.

III. LAB

Note: For each objective do the following job and assessment:

- Screen shots of the Assembly language program (ALP)
- Observations (with screen shots)

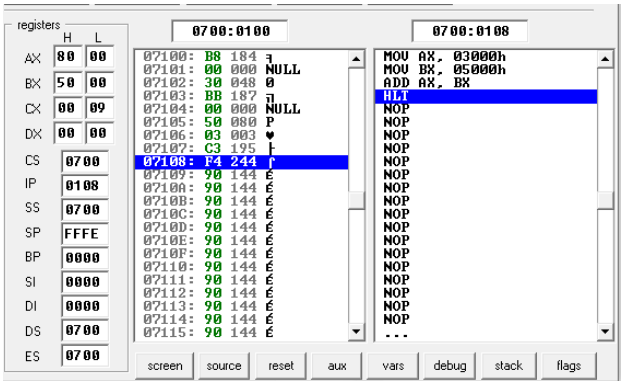

Fig. 1. Execution results of addition using immediate addressing mode of 8086 emulator.

From this result I have observed……

Input:

Sl. No.	Memory Location	Operand (Data)
1		
2		
…		

Output:

Sl. No.	Memory Location	Operand (Data)
1		
2		
…		

IV. CONCLUSION

V. POST LAB

1. Explain briefly condition codes (flags) of ARM processor.
2. Which condition codes (flags) is considered for the following branch instructions?
    a. B Label
    b. BEQ label
    c. BLT label