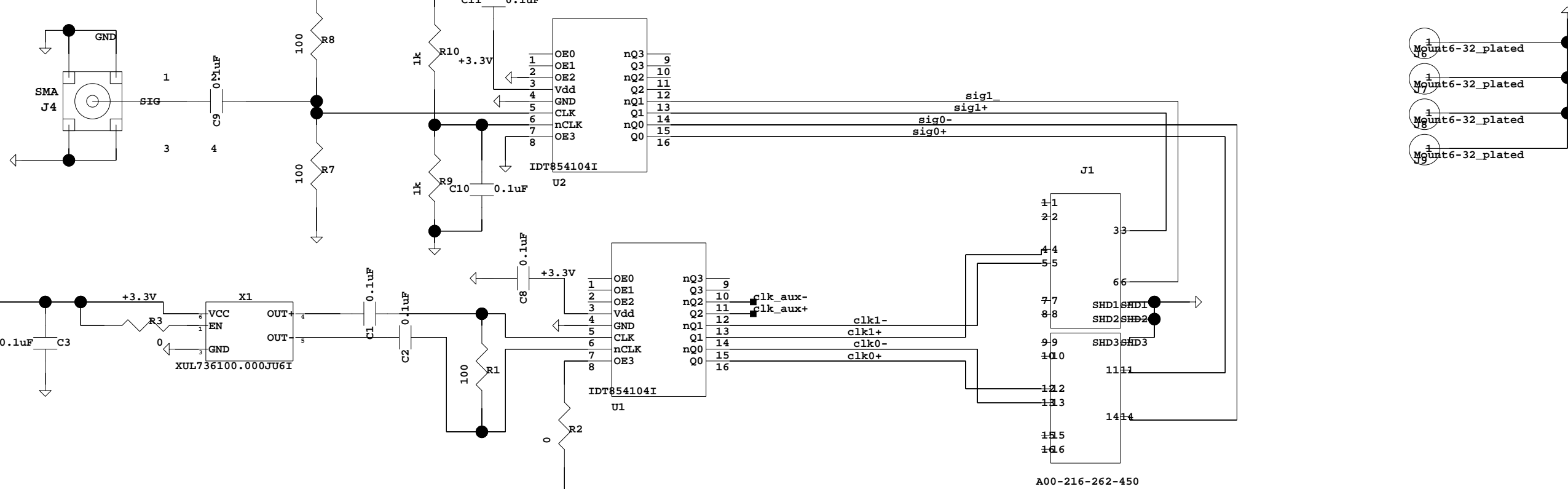


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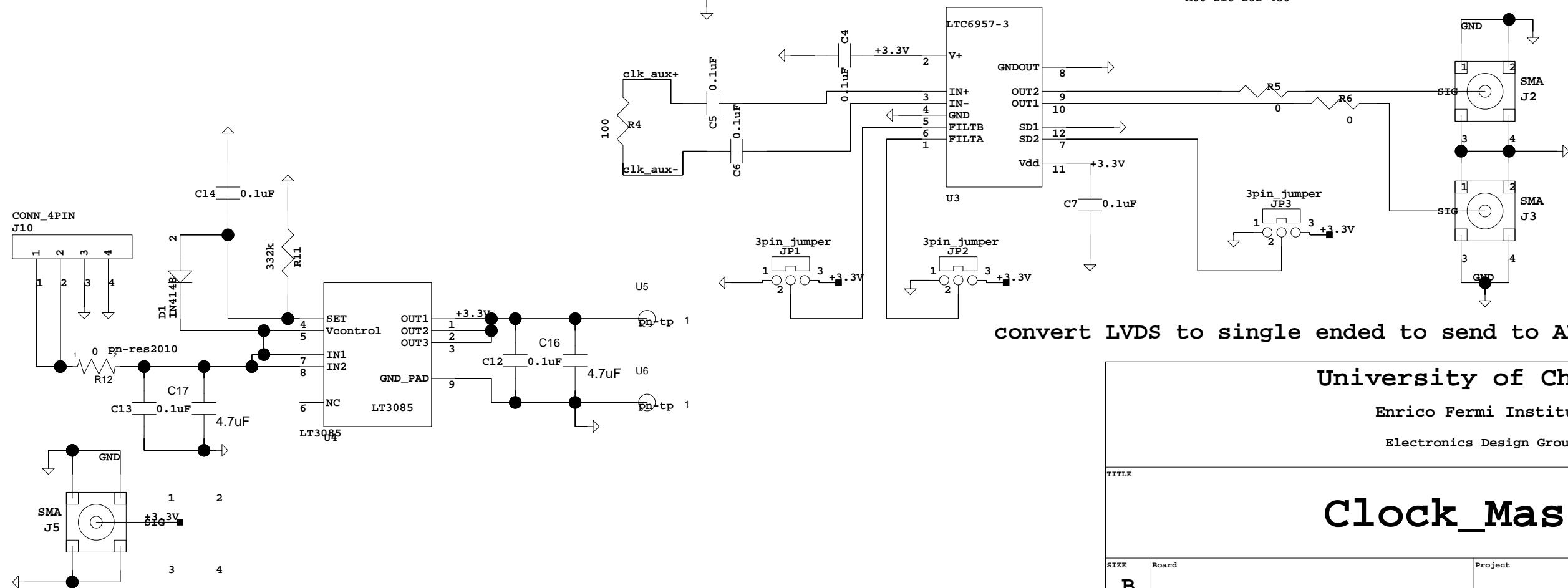
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Mount6-32_plated
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Mount6-32_plated



convert LVDS to single ended to send to ARA DAQ

University of Chicago

Enrico Fermi Institute

Electronics Design Group

Clock_Master

TITLE			
SIZE	Board	Project	REV
B			0
SCALE	SHEET	of	
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11/04/2017:09:30			

DRAWN BY
EJO