## 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 1 A
- Reverse voltage: V<sub>R</sub> ≤ 20 V
- · Low forward voltage
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package
- Capable for reflow and wave soldering

## 3. Applications

- Low voltage rectification
- · High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- · Reverse polarity protection
- Low power consumption applications

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le 130$ °C; square wave	[1]	-	-	1	А
		$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le 145$ °C; square wave		-	- 1	1	А
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C		-	310	340	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 20 V; T <sub>j</sub> = 25 °C		-	250	1000	μA

[1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.



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# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	1 2	1 - 2
2	Α	anode	CFP3 (SOD123W)	sym001

<sup>[1]</sup> The marking bar indicates the cathode.

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PMEG2010ER	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMEG2010ER	B5

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## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_R$	reverse voltage	T <sub>j</sub> = 25 °C		-	20	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; f = 20 kHz; $T_{amb} \le 130$ °C; square wave	[1]	-	1	А
		$\delta$ = 0.5 ; f = 20 kHz; $T_{sp} \le 145$ °C; square wave		-	1	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	50	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	0.57	W
			[3]	-	0.95	W
			[1]	-	1.8	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	220	K/W
			[3] [2]	-	-	130	K/W
			<u>[4] [2]</u>	-	-	70	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		<u>[5]</u>	-	-	18	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [5] Soldering point of cathode tab.

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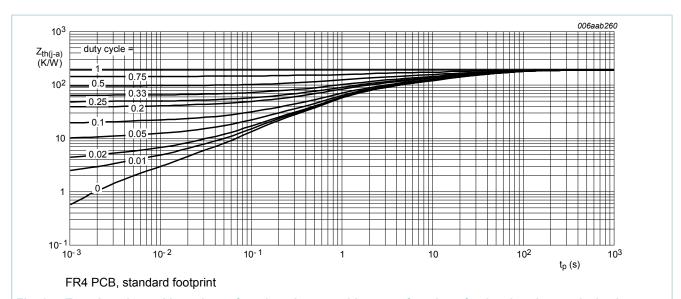


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

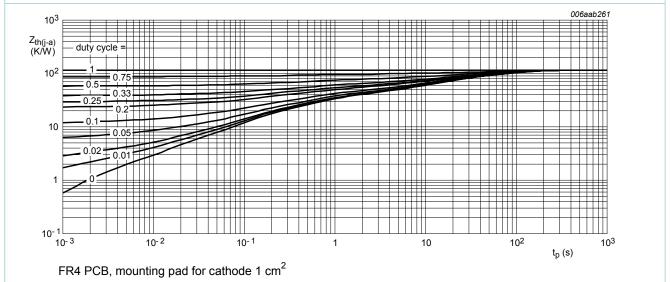
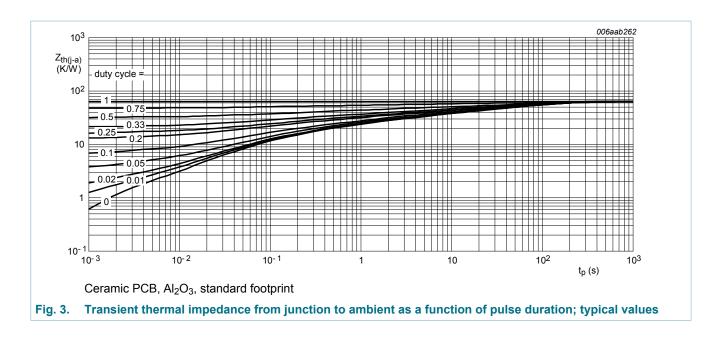


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

#### 1 A low VF MEGA Schottky barrier rectifier



## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 0.1 A; T <sub>j</sub> = 25 °C	-	220	250	mV
		I <sub>F</sub> = 0.7 A; T <sub>j</sub> = 25 °C	-	290	320	mV
		I <sub>F</sub> = 1 A; T <sub>j</sub> = 25 °C	-	310	340	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 5 V; T <sub>j</sub> = 25 °C	-	60	-	μΑ
		V <sub>R</sub> = 20 V; T <sub>j</sub> = 25 °C	-	250	1000	μΑ
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	175	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	65	-	pF

#### 1 A low VF MEGA Schottky barrier rectifier

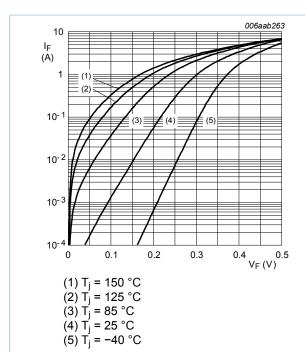


Fig. 4. Forward current as a function of forward voltage; typical values

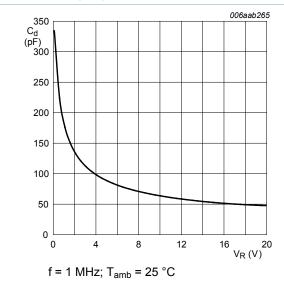


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

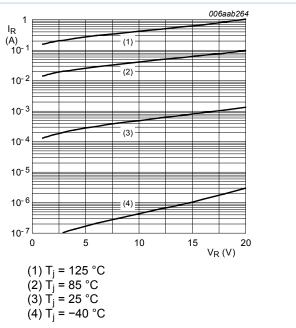
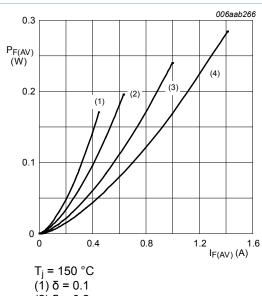


Fig. 5. Reverse current as a function of reverse voltage; typical values



 $(1) \delta = 0.1$   $(2) \delta = 0.2$   $(3) \delta = 0.5$  $(4) \delta = 1$ 

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

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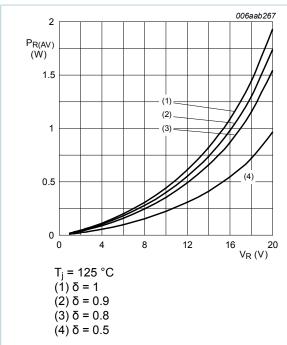
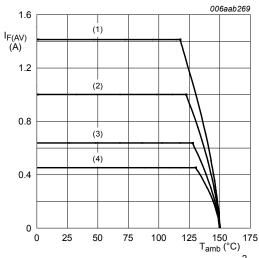


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

T<sub>i</sub> = 150 °C

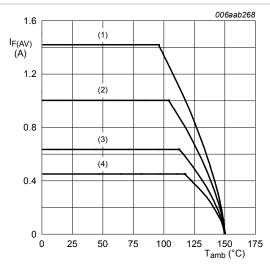
 $(1) \delta = 1; DC$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



FR4 PCB, standard footprint

 $T_j = 150 \,^{\circ}\text{C}$ 

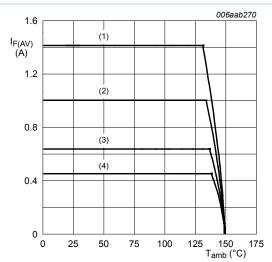
 $(1) \delta = 1; DC$ 

(2)  $\delta$  = 0.5; f = 20 kHz

(3)  $\delta$  = 0.2; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



Ceramic PCB,  $Al_2O_3$ , standard footprint

 $T_i = 150 \,^{\circ}C$ 

 $(1) \delta = 1; DC$ 

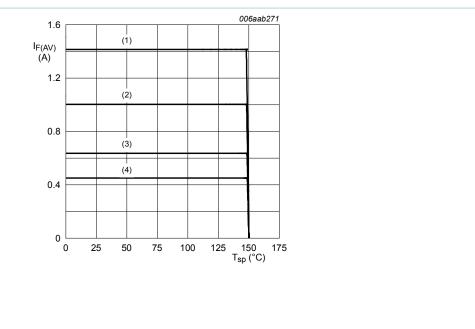
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta$  = 0.1; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values

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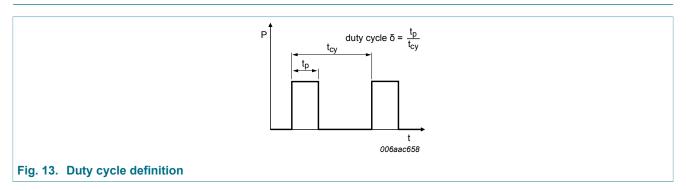


(1)  $\delta$  = 1; DC (2)  $\delta$  = 0.5; f = 20 kHz (3)  $\delta$  = 0.2; f = 20 kHz (4)  $\delta$  = 0.1; f = 20 kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

### 11. Test information

T<sub>i</sub> = 150 °C



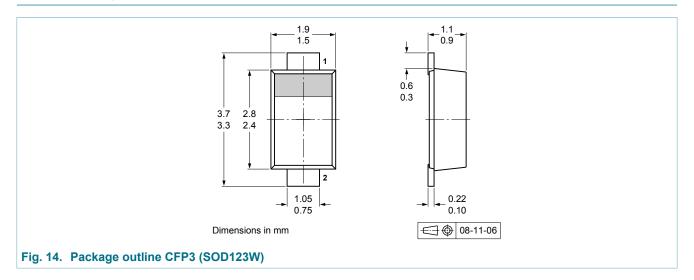
The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### **Quality information**

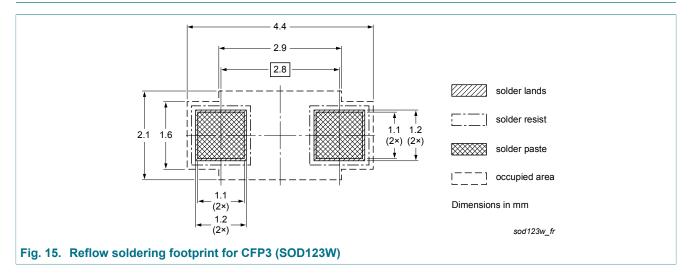
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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## 12. Package outline

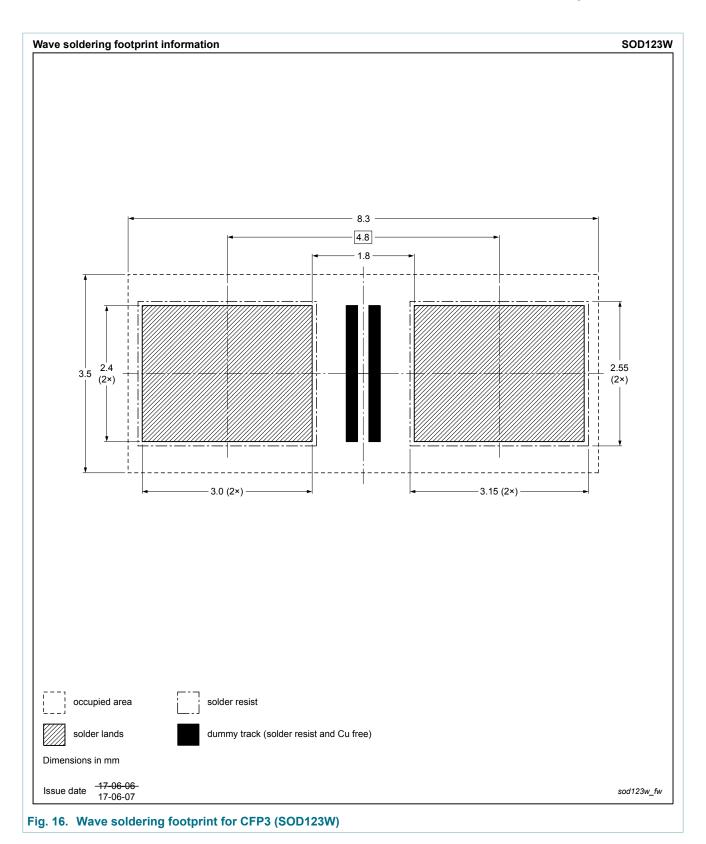


## 13. Soldering



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# 14. Revision history

#### Table 8. Revision history

	~ 7						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG2010ER v.2	20171117	Product data sheet	-	PMEG2010ER_1			
Modifications:	<ul> <li>Features and benefits: Capable for reflow and wave soldering added</li> <li>Soldering: Wave soldering footprint added</li> </ul>						
PMEG2010ER_1	20081229	Product data sheet	-	-			

#### 1 A low VF MEGA Schottky barrier rectifier

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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PMEG2010ER

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