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| Ref. | ID | Requirement | ASIL |
| FSR1 | SYSR1 | The brake signal shall be monitored in a cycle lower than 50ms. | D |
| FSR1 | SYSR2 | If the monitored brake signal value differs from the logic unit calculated value by more than 10%, and the logic output is to enable the brake lamps, the brake lamps shall be illuminated. | D |
| FSR2 | SYSR3 | The acceleration of the vehicle shall be monitored in a cycle lower than 50ms using an accelerometer. | D |
| FSR2 | SYSR4 | The vehicle acceleration shall be compared with the calculated acceleration derived from velocity, in the logic unit, in a cycle lower than 50ms. | D |
| FSR3 | SYSR5 | If the acceleration of the vehicle is measured to be within the brake lamp activation range, and the driving mode is regenerative braking, and the logic unit does not assert that the brake lights are to be activated, the brake lights shall be activated. | D |
| FSR3 | SYSR6 | The logic unit shall detect faults within itself or the brake lamps, which would result in the unintended activation of brake lamps within 50ms and provide a message to the driver display with the code “ErBrkON”, which the display controller may interpret. | D |
| FSR4 | SYSR7 | The logic unit shall detect faults within itself or the brake lamps, which would result in failure to illuminate brake lamps when the brake signal is active within 50 ms and provide a message to the driver display with the code “ErBrkOFF”, which the display controller may interpret. | D |