SHUTTLE

Project Name : N10CNXX Rev:3.a

Platform : NV Tegra250

PAGE	CONTENT
1.	INDEX
2.	SYSTEM BLOCK DIAGRAM
3.	POWER BLOCK DIAGRAM
4.	TERGA CORE / OSC / PLL / SYS
5.	TERGA NAND / DDR / CSI / DSI
6.	TERGA BB / UART /AUDIO / USB
7.	TERGA VI / LVDS / CRT / HDMI
8.	TERGA PCIe / SDIO
9.	LOW PWR DDR2 1GB
10.	PMU
11.	LCD 10" / THERMAL IC
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14.	I2C / EMMC
15.	SWITCH / BTN / MCU / DB CONN
16.	MB TO AUDIO BD
17.	MB TO IO BD
18.	+5V/ 3.3V/+1.8V
19.	HIGHSPEED-CAP / SCREW
20.	AUDIO BD / TP----------
21.	AUDIO CODEC / HP JACK / SPK
22.	MIPI / CCD
23.	SENSOR
24.	BTN / SIDE BD
25.	BOOST FOR LCD
26.	IO BD / USB HUB / SHAKE--------
27.	DOCKING / HDMI
28.	M-SD / USB
29.	3G
30.	MINI USB DC IN
31.	CHARGER / +5VA

M/B Schematic Version Change List

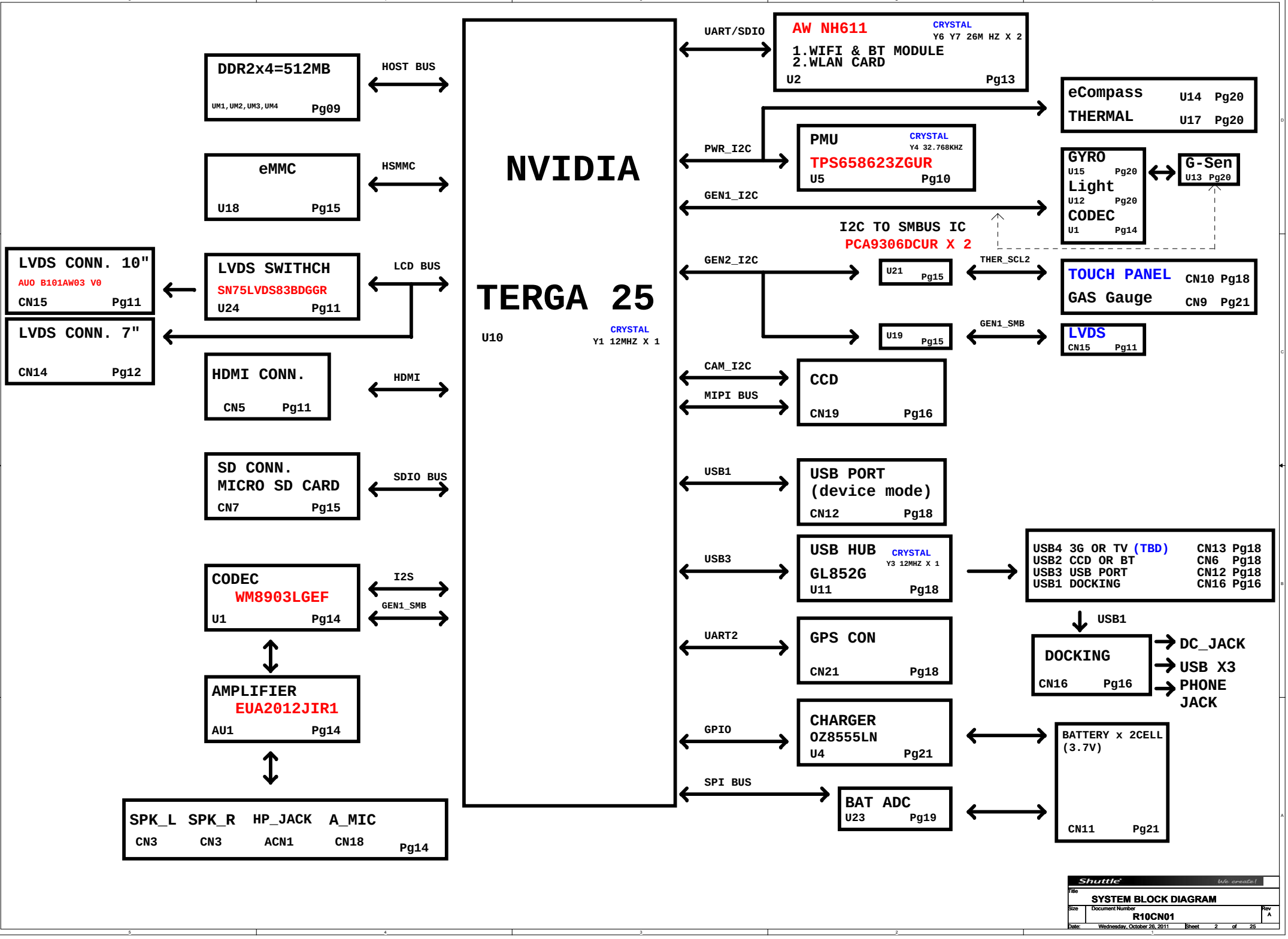
Release Date	Version	PCB P/N	PCB Description
	Rev:A		
	Rev:A1		
	Rev:B0		

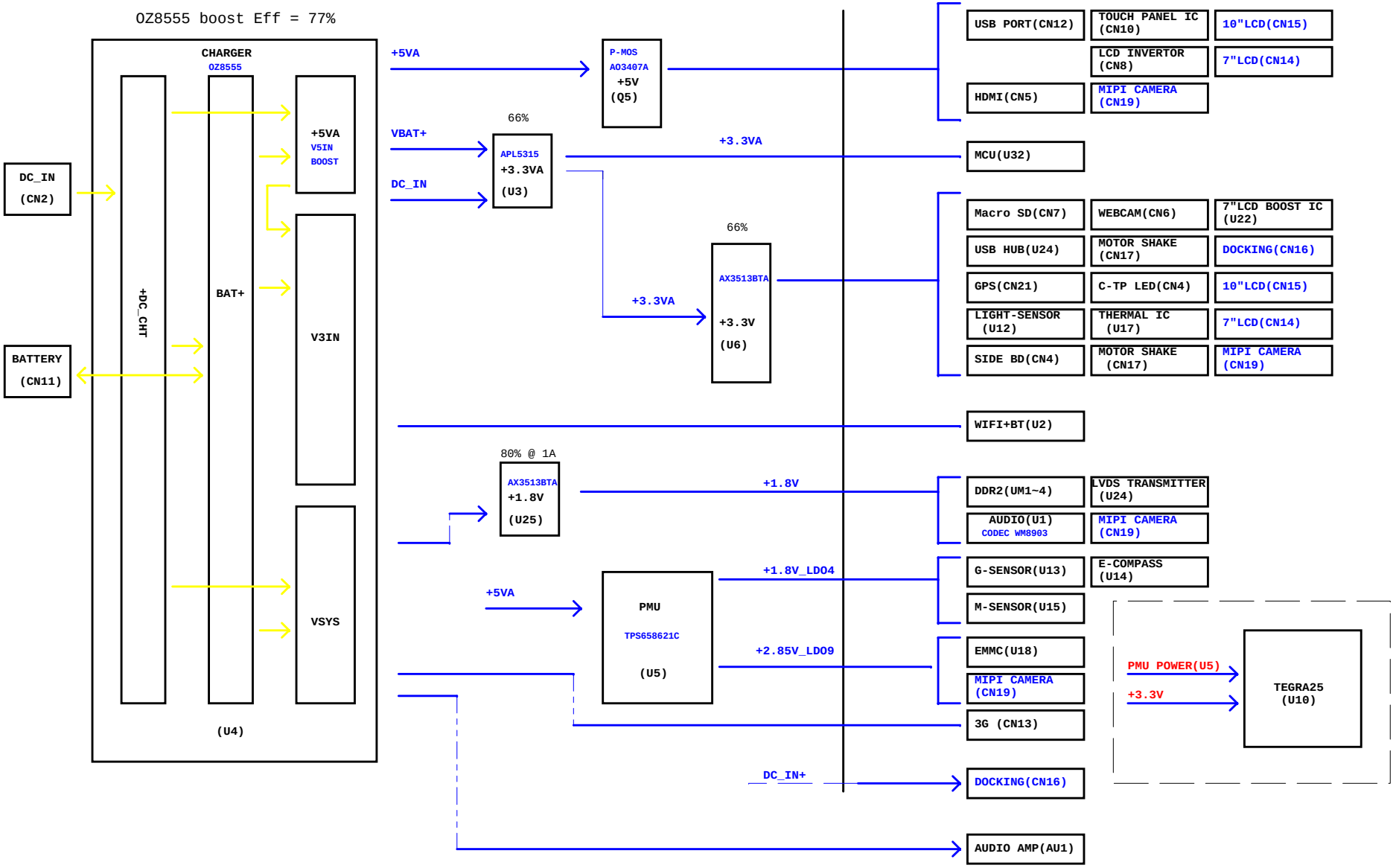
Shuttle We create!

Title: V10AN01LIST

Size: Custom | Document Number: R10CN01 | Rev: A

Date: Wednesday, October 26, 2011 | Sheet 1 of 25


NVIDIA
TERGA 25
U10
CRYSTAL
Y1 12MHZ X 1
DDR2x4=512MB
UM1,UM2,UM3,UM4
Pg09
HOST BUS
eMMC
U18
Pg15
HSMMC
LVDS CONN. 10"
AUO B101AW03 V0
CN15
Pg11
LVDS CONN. 7"
CN14
Pg12
LVDS SWITHCH
SN75LVDS83BDGGR
U24
Pg11
LCD BUS
HDMI CONN.
CN5
Pg11
HDMI
SD CONN.
MICRO SD CARD
CN7
Pg15
SDIO BUS
CODEC
WM8903LGEF
U1
Pg14
I2S
GEN1_SMB
AMPLIFIER
EUA2012JIR1
AU1
Pg14
SPK_L
SPK_R
HP_JACK
A_MIC
CN3
CN3
ACN1
CN18
Pg14
UART/SDIO
AW NH611
CRYSTAL
Y6 Y7 26M HZ X 2
1.WIFI & BT MODULE
2.WLAN CARD
U2
Pg13
PWR_I2C
PMU
CRYSTAL
Y4 32.768KHZ
TPS658623ZGUR
U5
Pg10
GEN1_I2C
I2C TO SMBUS IC
PCA9306DCUR X 2
GEN2_I2C
U21
Pg15
THER_SCL2
U19
Pg15
GEN1_SMB
CAM_I2C
MIPI BUS
CCD
CN19
Pg16
USB1
USB PORT
(device mode)
CN12
Pg18
USB3
USB HUB
CRYSTAL
Y3 12MHZ X 1
GL852G
U11
Pg18
UART2
GPS CON
CN21
Pg18
GPIO
CHARGER
OZ8555LN
U4
Pg21
SPI BUS
BAT ADC
U23
Pg19
eCompass
U14
Pg20
THERMAL
U17
Pg20
GYRO
U15
Pg20
Light
U12
Pg20
CODEC
U1
Pg14
G-Sen
U13 Pg20
TOUCH PANEL
CN10 Pg18
GAS Gauge
CN9 Pg21
LVDS
CN15
Pg11
USB4 3G OR TV (TBD)
CN13 Pg18
USB2 CCD OR BT
CN6 Pg18
USB3 USB PORT
CN12 Pg18
USB1 DOCKING
CN16 Pg16
USB1
DOCKING
CN16
Pg16
DC_JACK
USB X3
PHONE JACK
BATTERY x 2CELL
(3.7V)
CN11
Pg21
Shuttle
We create!
Title
SYSTEM BLOCK DIAGRAM
Size
Document Number
R10CN01
Rev
A
Date:
Wednesday, October 26, 2011
Sheet
2
of
25



OZ8555	VSYS	V3IN	+5VA
ADAPTOR MODE	+5.5V/+5.0V ADAPTOR OUTPUT	VSYS >3.5V,V3IN= VSYS VSYS<=3.5V,V3IN= +5VA	+5.0V BUCK
BATTERY MODE (1S2P)	+4.2V~3.2V BATTERY OUTPUT		+5.0V BOOST

Shuttle We create!

Title: POWER BLOCK

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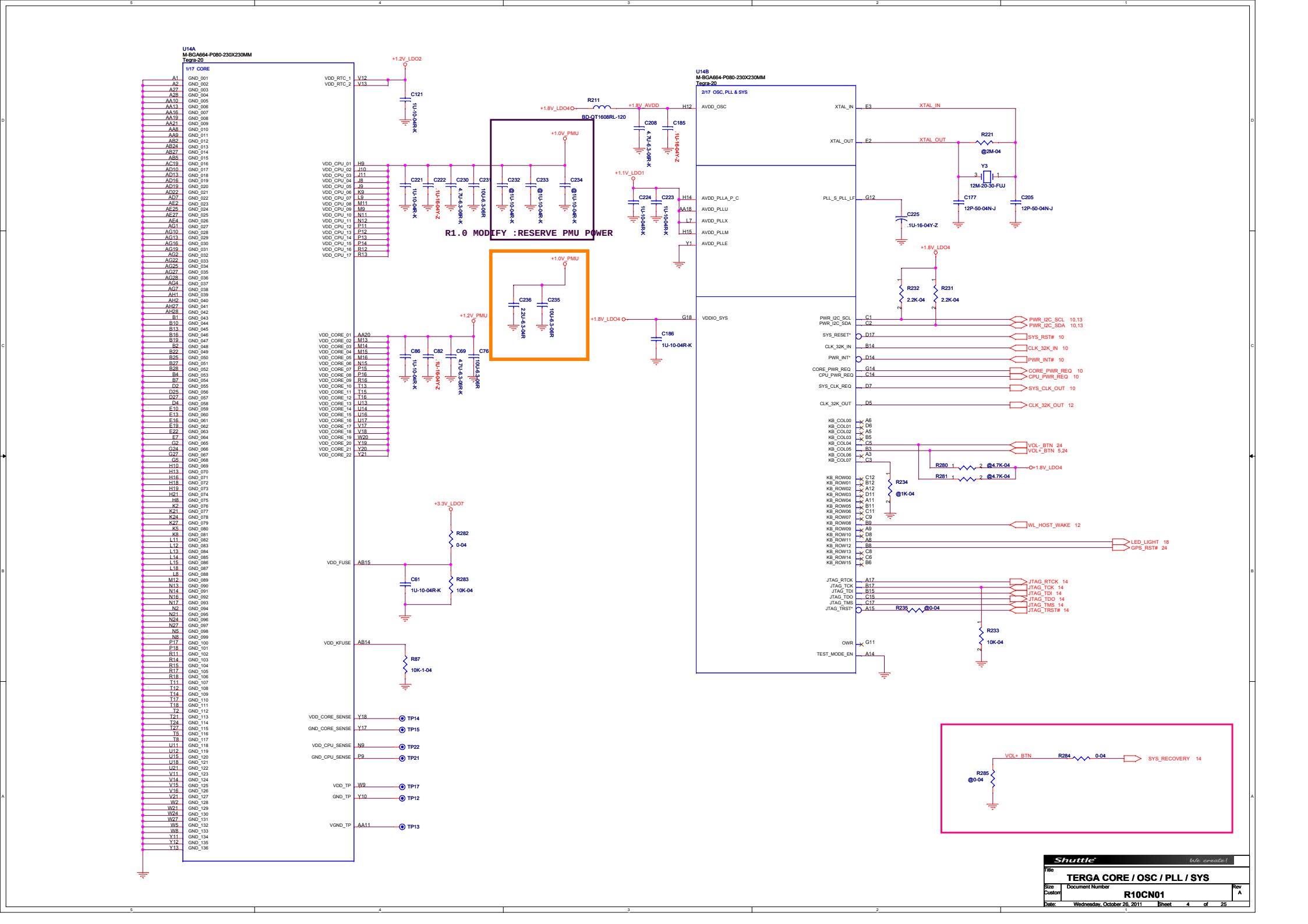
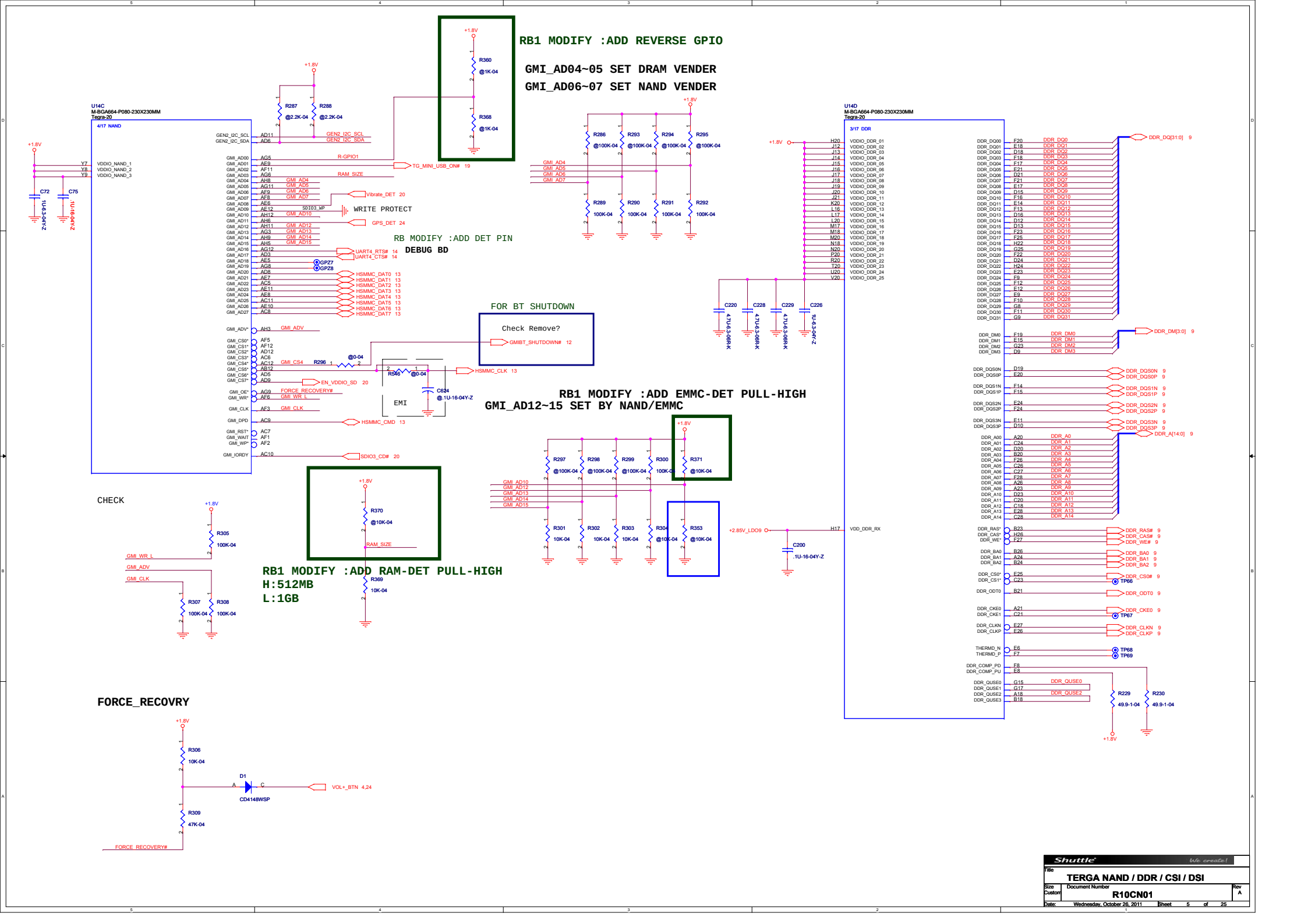
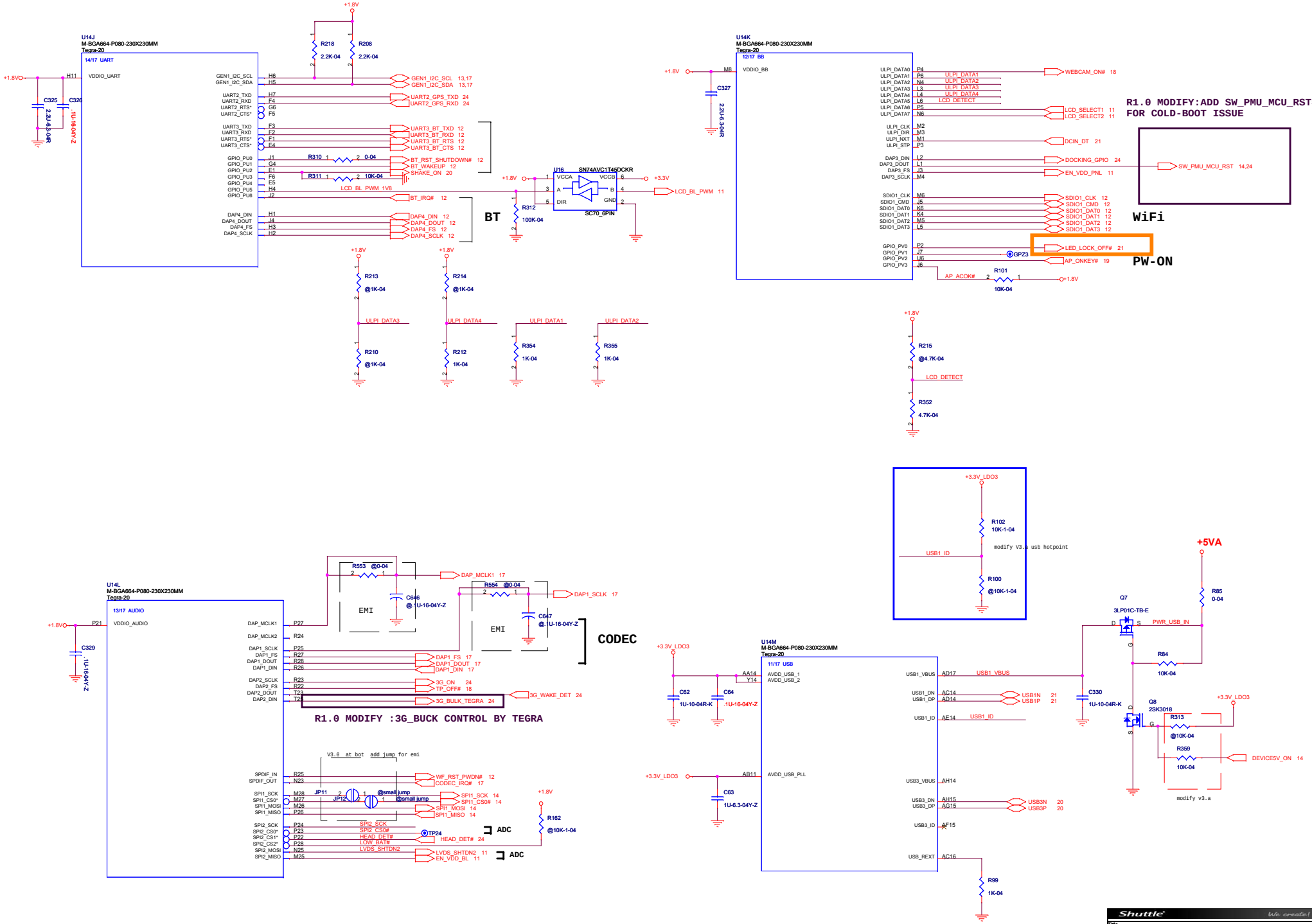

U14A
M-BGA664-P080-230X230MM
Tegra-20
1/17 CORE
U14B
M-BGA664-P080-230X230MM
Tegra-20
2/17 OSC, PLL & SYS
R1.0 MODIFY :RESERVE PMU POWER
+1.2V_LDO2
+1.8V_LDO4
+1.8V_AVDD
+1.0V_PMU
+1.1V_LDO1
+1.2V_PMU
+3.3V_LDO7
BD-QT1608RL-120
XTAL_IN
XTAL_OUT
12M-20-30-FUJ
PWR_I2C_SCL 10,13
PWR_I2C_SDA 10,13
SYS_RST# 10
CLK_32K_IN 10
PWR_INT# 10
CORE_PWR_REQ 10
CPU_PWR_REQ 10
SYS_CLK_OUT 10
CLK_32K_OUT 12
VOL- BTN 24
VOL+ BTN 5,24
WL_HOST_WAKE 12
LED_LIGHT 18
GPS_RST# 24
JTAG_RTCK 14
JTAG_TCK 14
JTAG_TDI 14
JTAG_TDO 14
JTAG_TMS 14
JTAG_TRST# 14
SYS_RECOVERY 14
Shuttle
We create!
TERGA CORE / OSC / PLL / SYS
R10CN01
Wednesday, October 26, 2011


RB1 MODIFY :ADD REVERSE GPIO
GMI_AD04~05 SET DRAM VENDER
GMI_AD06~07 SET NAND VENDER
U14C
M-BGA664-P080-230X230MM
Tegra-20
4/17 NAND
U14D
M-BGA664-P080-230X230MM
Tegra-20
3/17 DDR
WRITE PROTECT
RB MODIFY :ADD DET PIN
DEBUG BD
FOR BT SHUTDOWN
Check Remove?
RB1 MODIFY :ADD EMMC-DET PULL-HIGH
GMI_AD12~15 SET BY NAND/EMMC
CHECK
RB1 MODIFY :ADD RAM-DET PULL-HIGH
H:512MB
L:1GB
FORCE_RECOVRY
EMI
Shuttle
We create!
TERGA NAND / DDR / CSI / DSI
R10CN01
Wednesday, October 26, 2011


U14J
M-BGA664-P080-230X230MM
Tegra-20
14/17 UART
VDDIO_UART
GEN1_I2C_SCL
GEN1_I2C_SDA
UART2_TXD
UART2_RXD
UART2_RTS*
UART2_CTS*
UART3_TXD
UART3_RXD
UART3_RTS*
UART3_CTS*
GPIO_PU0
GPIO_PU1
GPIO_PU2
GPIO_PU3
GPIO_PU4
GPIO_PU5
GPIO_PU6
DAP4_DIN
DAP4_DOUT
DAP4_FS
DAP4_SCLK
R218 2.2K-04
R208 2.2K-04
BT_RST_SHUTDOWN#
BT_WAKEUP
SHAKE_ON
LCD_BL_PWM_1V8
BT_IRQ#
BT
U16 SN74AVC1T45DCKR
SC70_6PIN
LCD_BL_PWM
U14K
M-BGA664-P080-230X230MM
Tegra-20
12/17 BB
VDDIO_BB
ULPI_DATA0
ULPI_DATA1
ULPI_DATA2
ULPI_DATA3
ULPI_DATA4
ULPI_DATA5
ULPI_DATA6
ULPI_DATA7
ULPI_CLK
ULPI_DIR
ULPI_NXT
ULPI_STP
WEBCAM_ON#
LCD_SELECT1
LCD_SELECT2
DCIN_DT
DOCKING_GPIO
EN_VDD_PNL
SW_PMU_MCU_RST
R1.0 MODIFY:ADD SW_PMU_MCU_RST#
FOR COLD-BOOT ISSUE
WiFi
SDIO1_CLK
SDIO1_CMD
SDIO1_DAT0
SDIO1_DAT1
SDIO1_DAT2
SDIO1_DAT3
GPIO_PV0
GPIO_PV1
GPIO_PV2
GPIO_PV3
LED_LOCK_OFF#
AP_ONKEY#
AP_ACOK#
PW-ON
R213 @1K-04
R214 @1K-04
R210 @1K-04
R212 1K-04
R354 1K-04
R355 1K-04
R215 @4.7K-04
R352 4.7K-04
U14L
M-BGA664-P080-230X230MM
Tegra-20
13/17 AUDIO
VDDIO_AUDIO
DAP_MCLK1
DAP_MCLK2
DAP1_SCLK
DAP1_FS
DAP1_DOUT
DAP1_DIN
DAP2_SCLK
DAP2_FS
DAP2_DOUT
DAP2_DIN
EMI
CODEC
3G_ON
TP_OFF#
3G_BULK_TEGRA
3G_WAKE_DET
R1.0 MODIFY :3G_BUCK CONTROL BY TEGRA
SPDIF_IN
SPDIF_OUT
SPI1_SCK
SPI1_CS0*
SPI1_MOSI
SPI1_MISO
SPI2_SCK
SPI2_CS0*
SPI2_CS1*
SPI2_CS2*
SPI2_MOSI
SPI2_MISO
WF_RST_PWDN#
CODEC_IRQ#
HEAD_DET#
LOW_BAT#
LVDS_SHTDN2
EN_VDD_BL
ADC
R162 @10K-1-04
U14M
M-BGA664-P080-230X230MM
Tegra-20
11/17 USB
AVDD_USB_1
AVDD_USB_2
AVDD_USB_PLL
USB1_VBUS
USB1_DN
USB1_DP
USB1_ID
USB3_VBUS
USB3_DN
USB3_DP
USB3_ID
USB_REXT
USB1N
USB1P
USB3N
USB3P
R99 1K-04
R102 10K-1-04
R100 @10K-1-04
modify V3.a usb hotpoint
+5VA
Q7 3LP01C-TB-E
Q8 2SK3018
R85 0-04
R84 10K-04
R313 @10K-04
R359 10K-04
DEVICE5V_ON
modify v3.a
V3.0 at bot add jump for emi
@small jump
Shuttle
We create!
TERGA BB / UART /AUDIO / USB
R10CN01
Rev A
Wednesday, October 26, 2011
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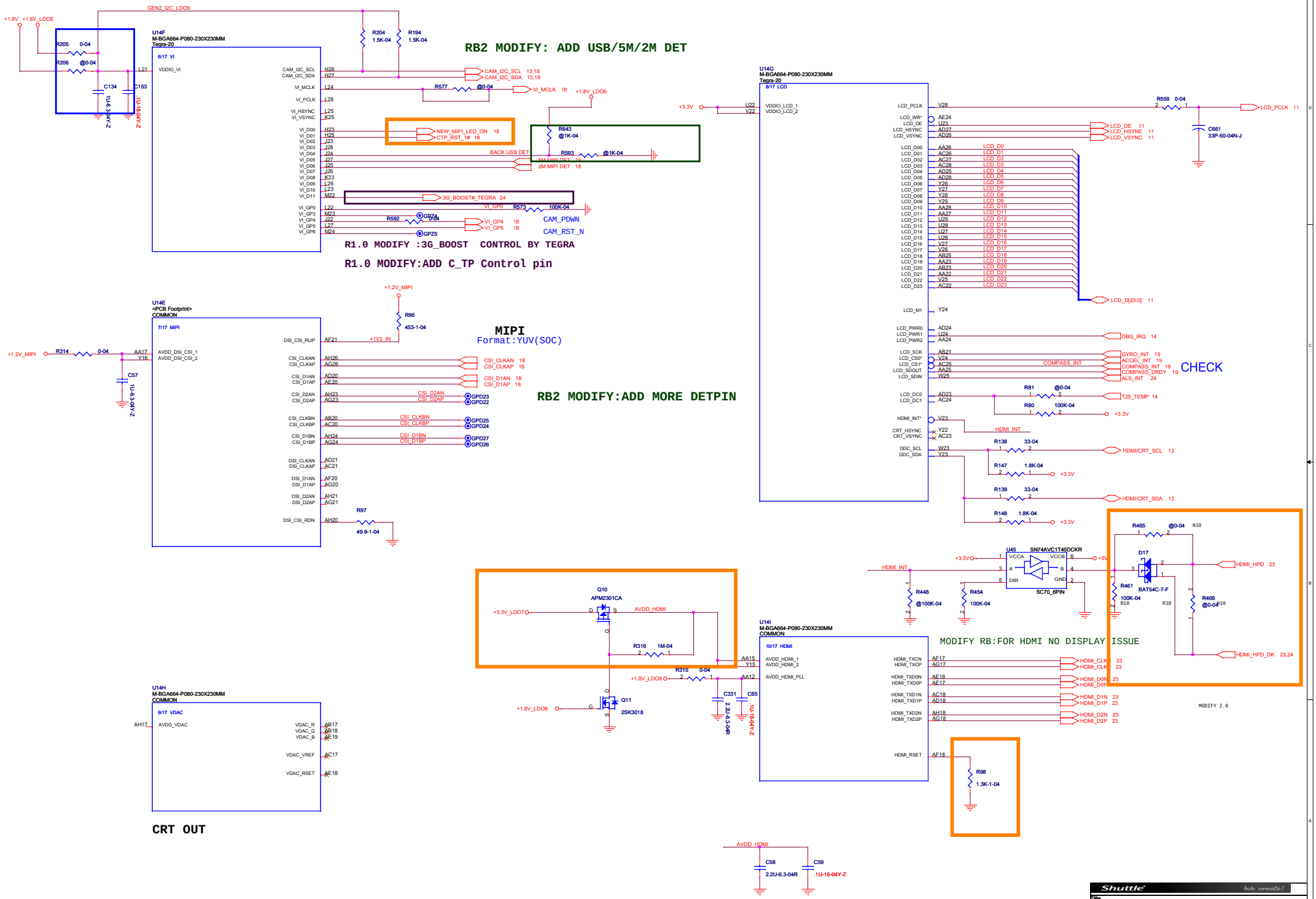

Shuttle	We create!
Title	TERGA VI / LVDS / CRT / HDMI
Size: Custom	Document Number: R10CN01
Rev	A
Date:	Wednesday, October 26, 2011
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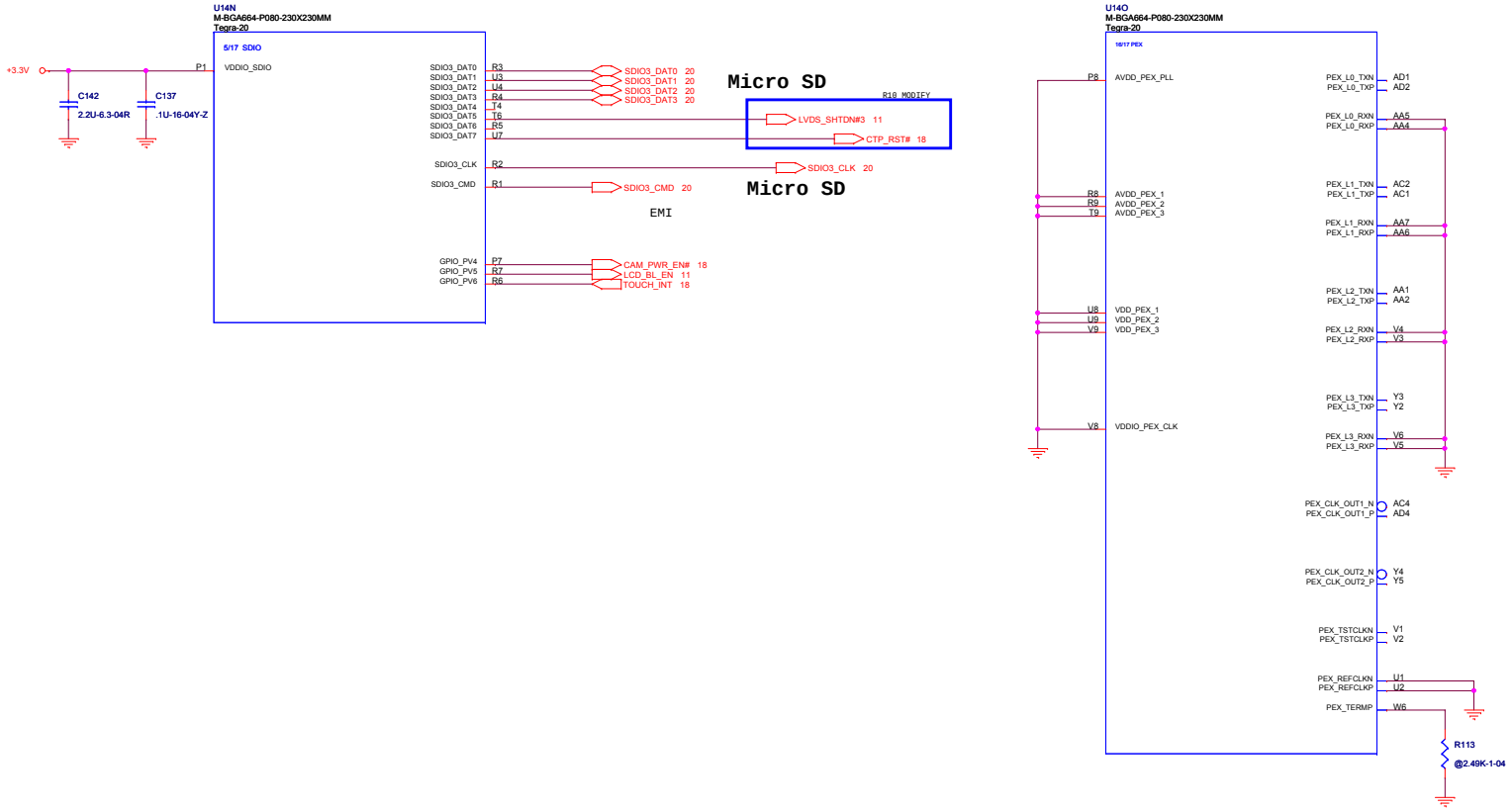

Shuttle We create!

Title: TERGA PCIe / SDIO

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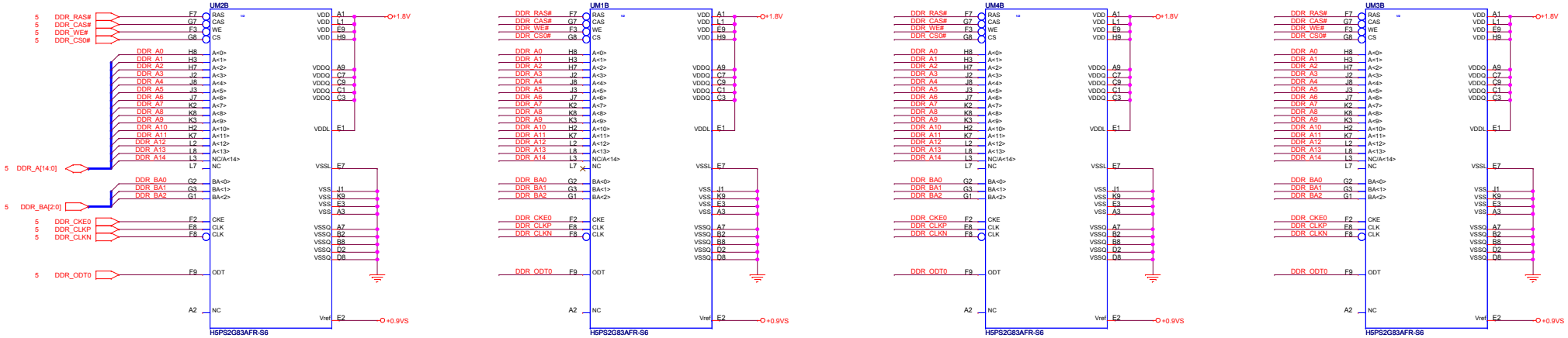
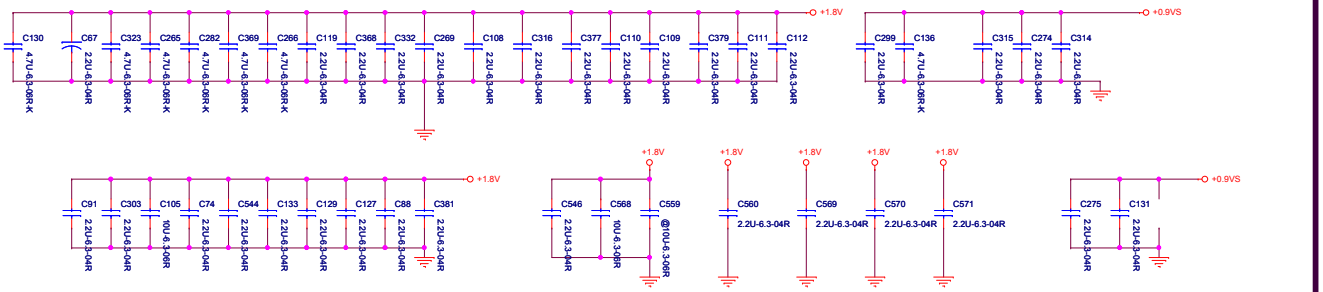
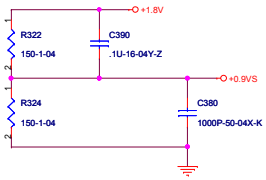
RA1 05R-2G83AF-H1S0
H5PS2G83AFR-S6 (虫聋256/800mhz)

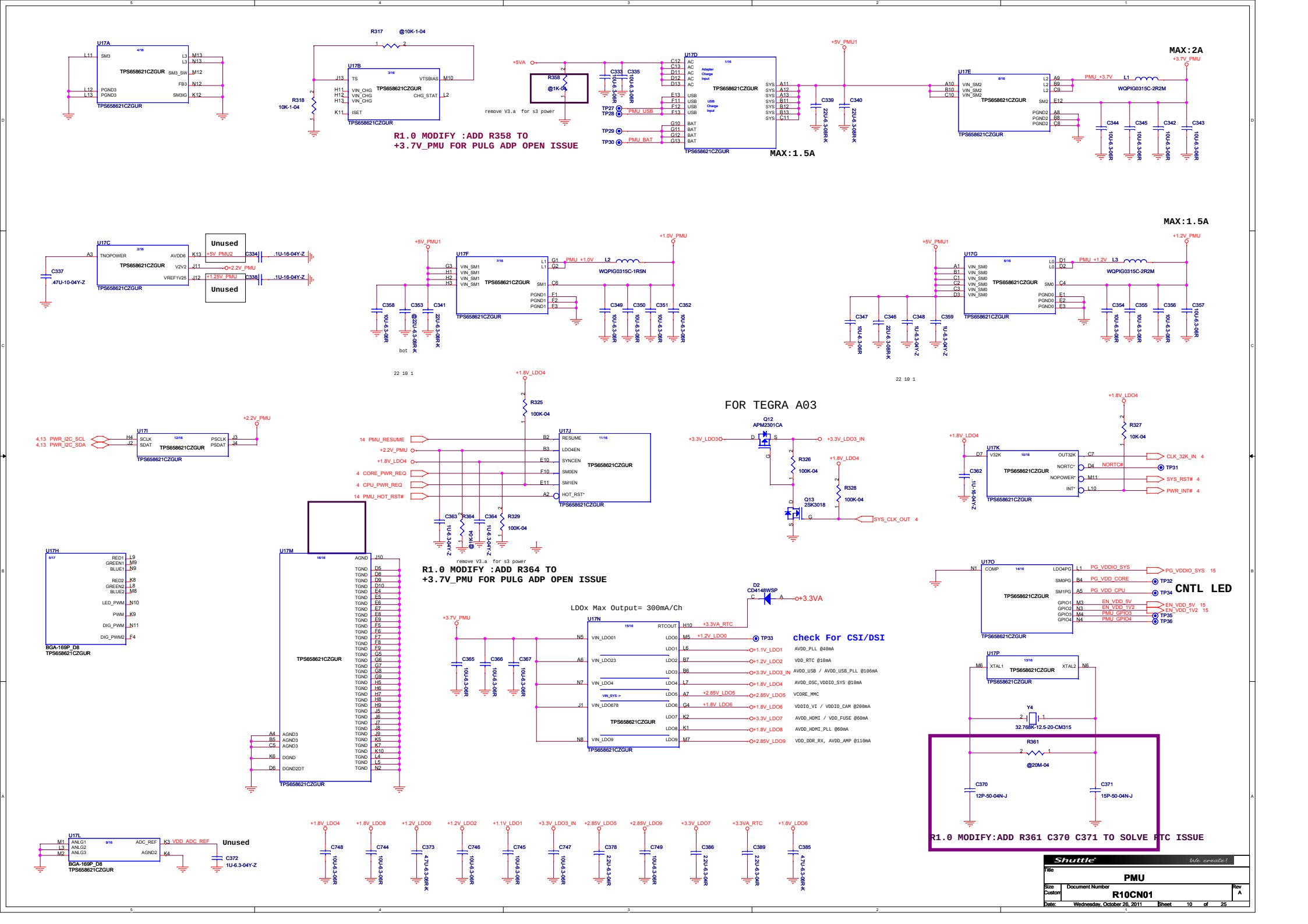


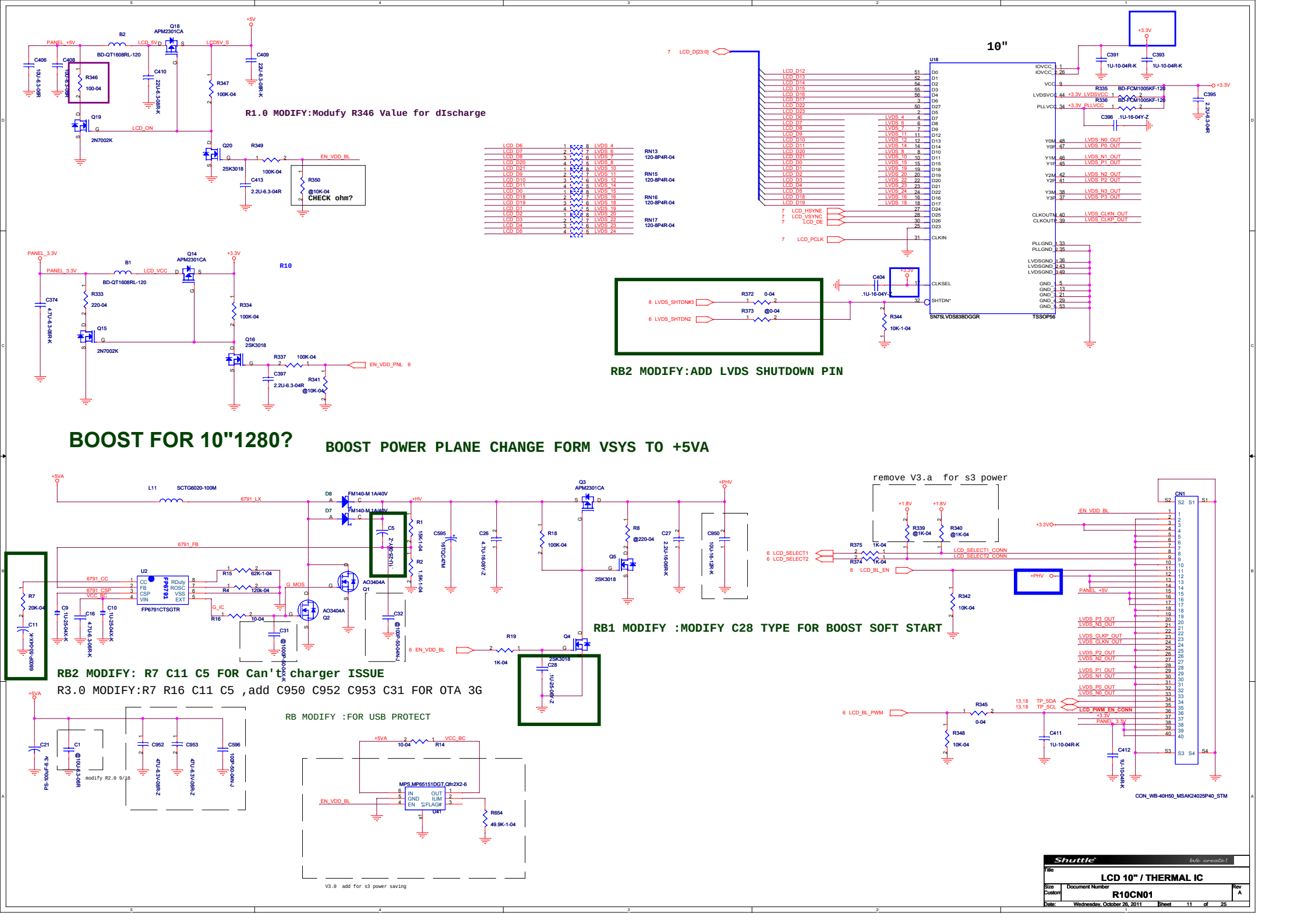
R1.0 MODIFY:Modufy DRAM CAP Value for NEO CORE



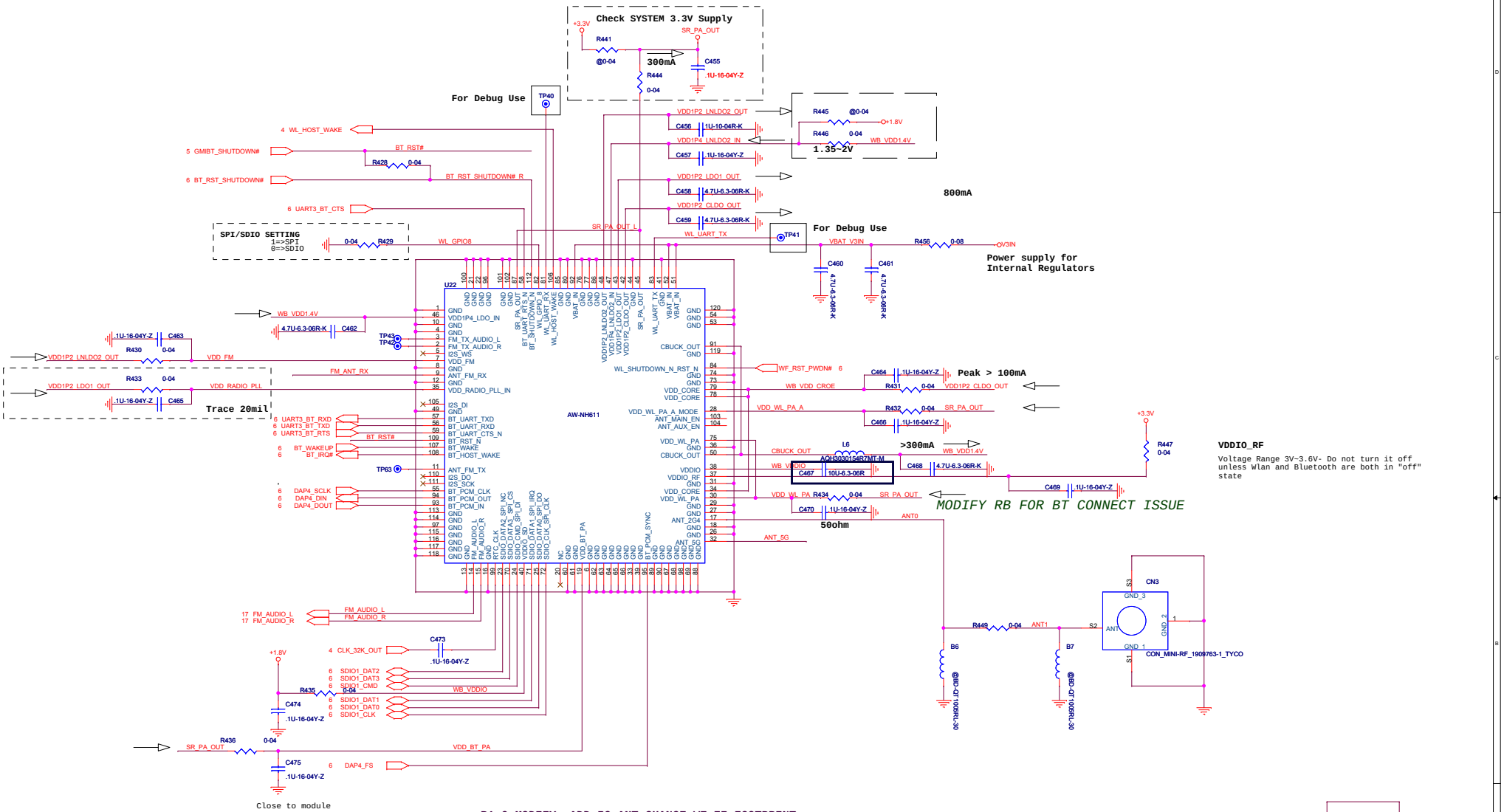


Shuttle	We create!		
Title: LOW PWR DDR2 1GB			
Size: Custom	Document Number: R10CN01		Rev: A
Date: Wednesday, October 26, 2011	Sheet 9 of 25		


U17A
TPS658621CZGUR
R317 @10K-1-04
R318 10K-1-04
U17B
TPS658621CZGUR
R1.0 MODIFY :ADD R358 TO
+3.7V_PMU FOR PULG ADP OPEN ISSUE
R358 @1K-04
remove V3.a for s3 power
U17D
MAX:1.5A
U17E
MAX:2A
WQPIG0315C-2R2M
U17C
Unused
U17F
WQPIG0315C-1R5N
U17G
WQPIG0315C-2R2M
MAX:1.5A
FOR TEGRA A03
U17I
U17J
R325 100K-04
R1.0 MODIFY :ADD R364 TO
+3.7V_PMU FOR PULG ADP OPEN ISSUE
Q12 APM2301CA
Q13 2SK3018
R326 100K-04
R328 100K-04
U17K
U17H
BGA-169P_D8
U17M
U17N
LDOx Max Output= 300mA/Ch
D2 CD4148WSP
check For CSI/DSI
U17O
CNTL LED
U17P
Y4 32.768K-12.5-20-CM315
R361 @20M-04
C370 12P-50-04N-J
C371 15P-50-04N-J
R1.0 MODIFY:ADD R361 C370 C371 TO SOLVE RTC ISSUE
U17L
Unused
Shuttle
We create!
PMU
R10CN01
Wednesday, October 26, 2011


10"
R1.0 MODIFY:Modufy R346 Value for dIscharge
R10
RB2 MODIFY:ADD LVDS SHUTDOWN PIN
BOOST FOR 10"1280?
BOOST POWER PLANE CHANGE FORM VSYS TO +5VA
remove V3.a for s3 power
RB1 MODIFY :MODIFY C28 TYPE FOR BOOST SOFT START
RB2 MODIFY: R7 C11 C5 FOR Can't charger ISSUE
R3.0 MODIFY:R7 R16 C11 C5 ,add C950 C952 C953 C31 FOR OTA 3G
RB MODIFY :FOR USB PROTECT
CHECK ohm?
SN75LVDS83BDGGR
TSSOP56
FP6791CTSGTR
CON_WB-40H50_MSAK24025P40_STM
Shuttle
We create!
LCD 10" / THERMAL IC
R10CN01
Wednesday, October 26, 2011

WiFi+BT



Title	WiFi+BT		
Size: Custom	Document Number: R10CN01		Rev: A
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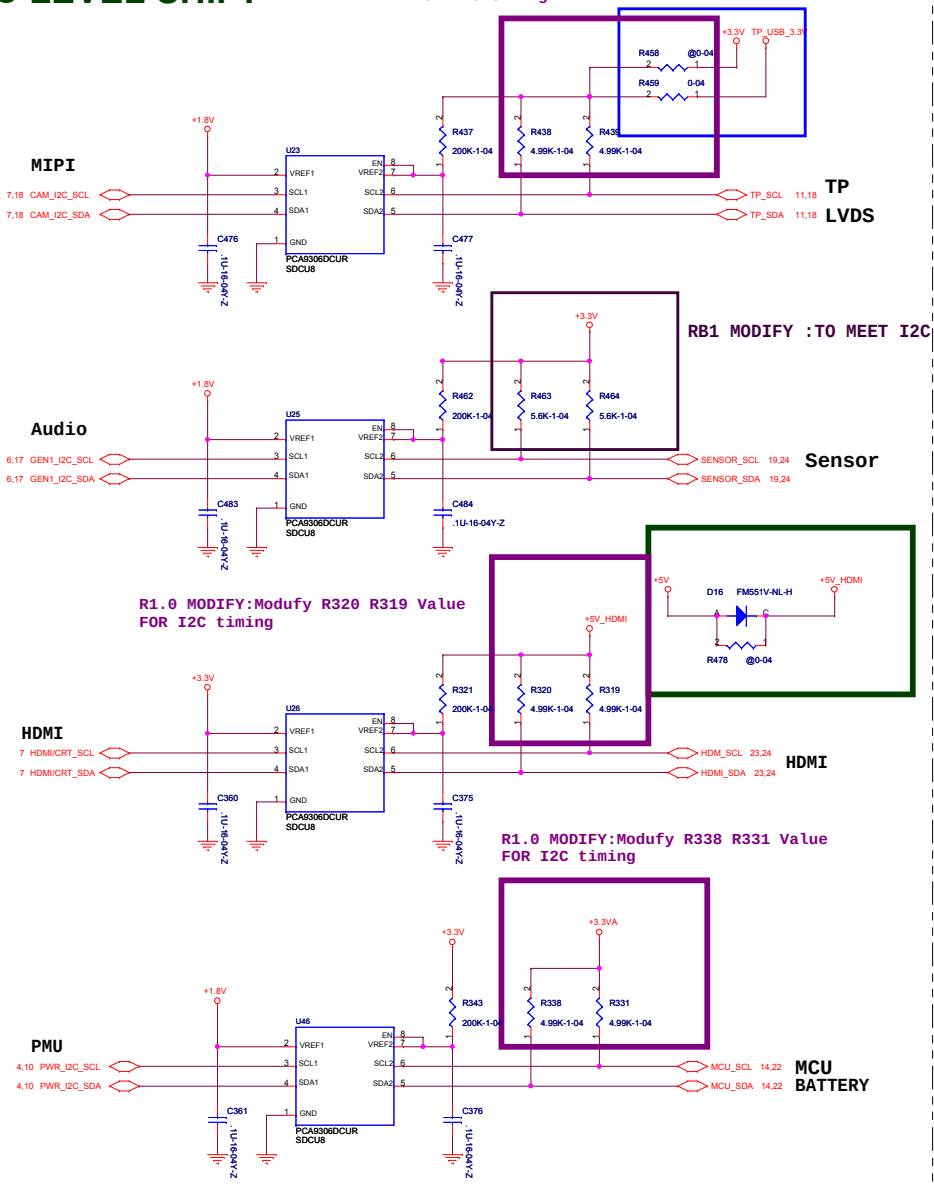
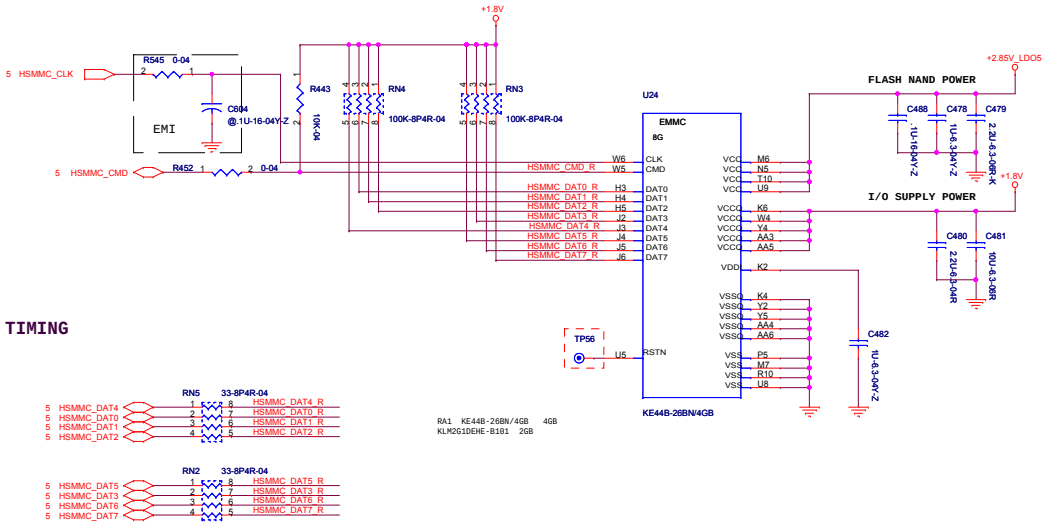
I2C LEVEL SHIFT

R1.0 MODIFY:Modufy R438 R439 Value
FOR I2C timing



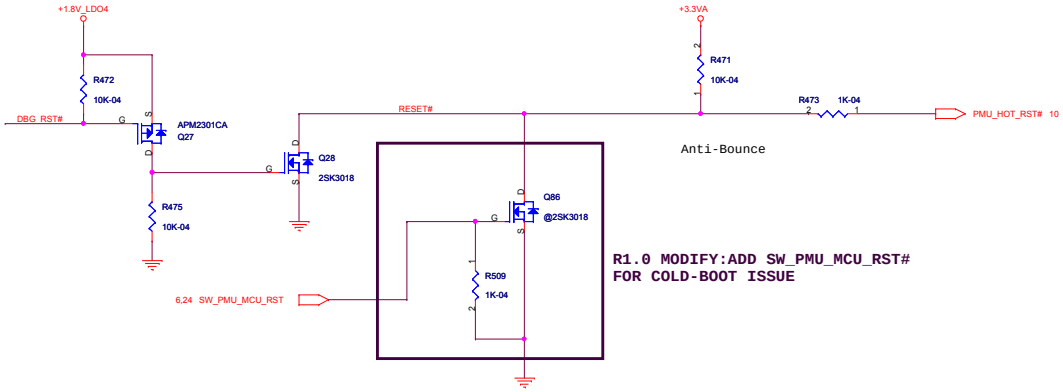
eMMC



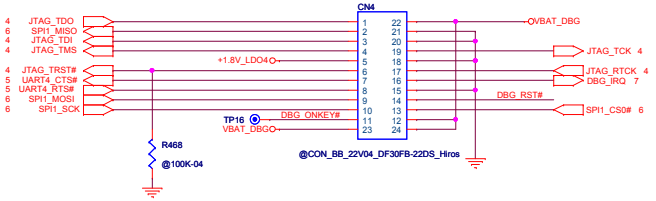
THERMAL IC

Shuttle	We create!		
Title: I2C TO SMBUS / EMMC / M-SD			
Size: Custom	Document Number: R10CN01		Rev: A
Date: Wednesday, October 26, 2011	Sheet 13 of 25		

RESET PMU

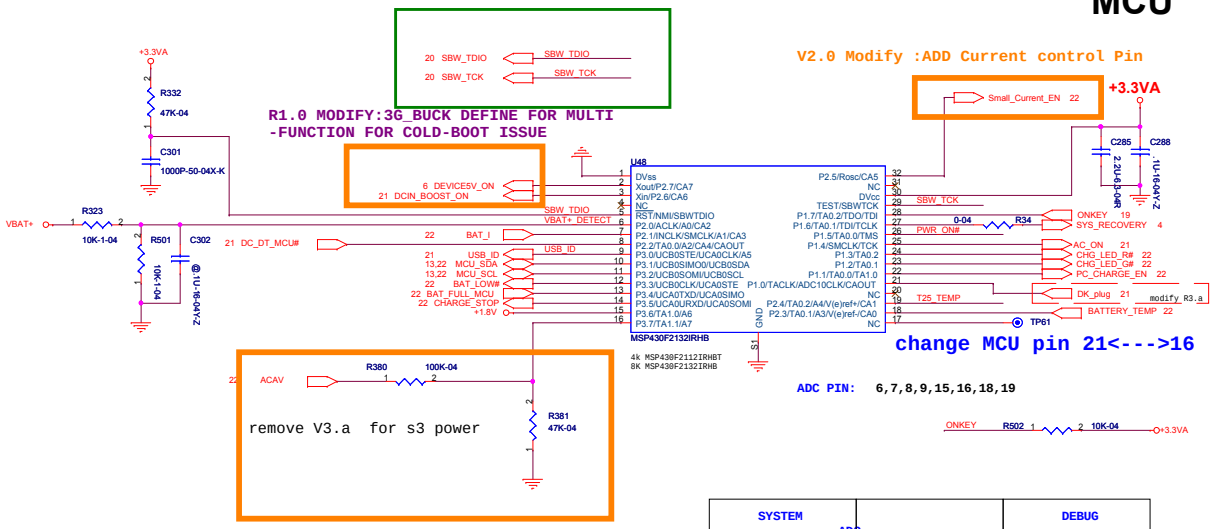


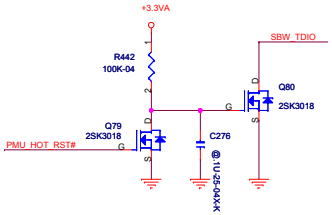
DEBUG CONN.



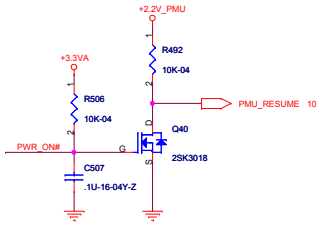
MCU

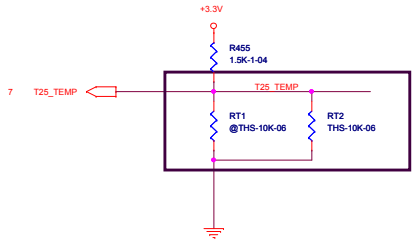
RB MODIFY :ADD DEBUG MODE FOR MCU





PMU_HOT_RST# = RESET MCU





Thermistor close chip

R3.a
pin21 ⊃DK_plug

SYSTEM		ADC	
MCU_SDA	P3.1	VBAT+_DETECT	P2.0
MCU_SCL	P3.2	USB_ID	P3.0
		DC_DT_MCU	P2.2
		T25_TEMP	P2.4
		+1.8V	P3.6
		BAT_I	P2.1
		MCU_BAT_LOW_POP	P3.7

DEBUG	
SBW_TCK	SBWTCK
SBW_TDIO	SBWTDIO

BAT_LOW#	P3.3
BAT_FULL_MCU	P3.4
CHARGE_STOP	P3.5
SYS_RECOVERY	P1.6
3G_BULK	P2.6
AC_ON	P2.3
ONKEY	P1.0
TG_PWR_ON/OFF	P1.1
CHG_LED_G#	P1.2
CHG_LED_R#	P1.3
ONKEY	P1.4
PWR_ON#	P1.5
SYS_RECOVERY	P1.6
3G_ON	P1.7
3G_BOOST#	P2.5

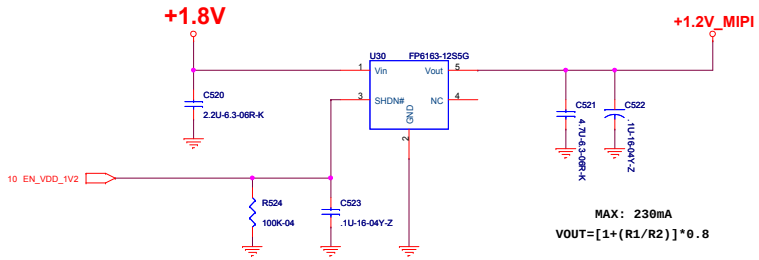
Shuttle We create!

Title: SWITCH / BTN / DEBUG / SHAKE

Size: | Document Number: R10CN01 | Rev: A

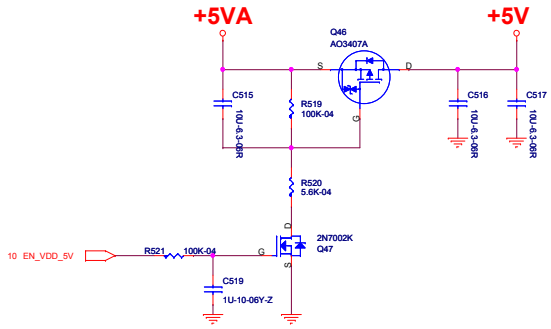
Date: Wednesday, October 26, 2011 | Sheet 14 of 25

MIPI PWR *+1.8V -> +1.2V_MIPI 300mA*



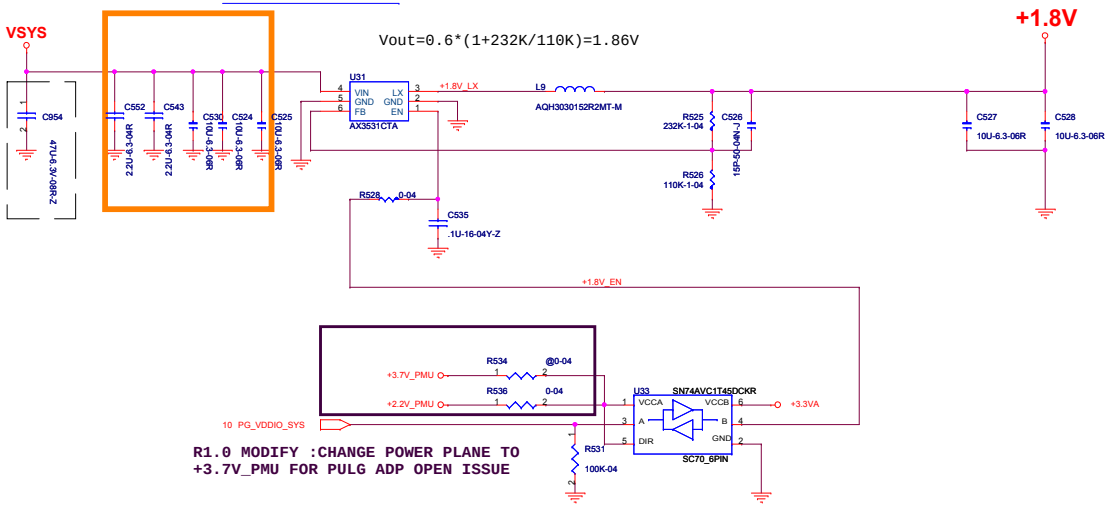
+5V

5VA->5V 2A



VSYS->1.8 1A

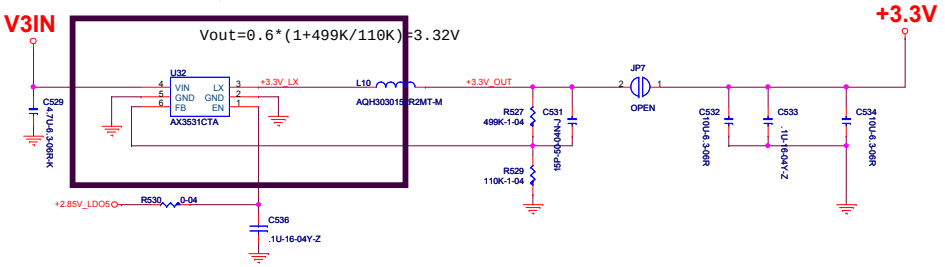
R1.0 MODIFY:U31 Change to AX3531CTA



+3.3V

V3IN->3.3V 1A

R1.0 MODIFY:U31 Change to AX3531CTA



Shuttle We create!

Title: +5V/+3.3VA / 3.3V

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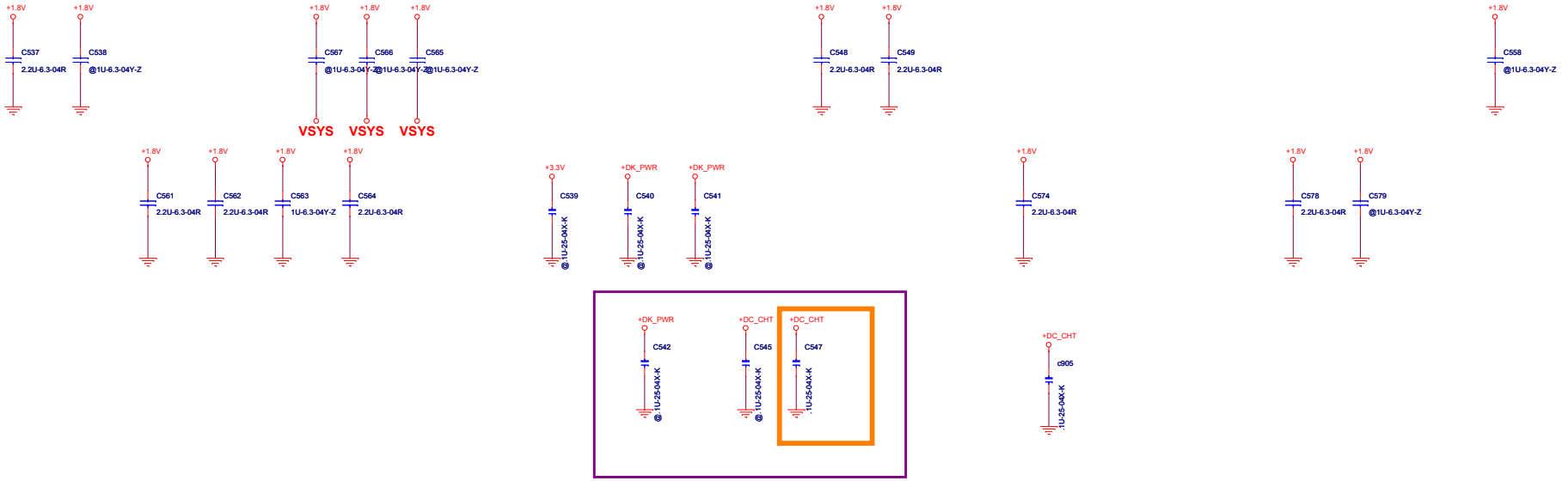
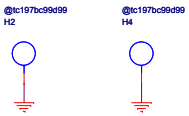
HIGH SPEED CAP (COUPLING CAP)

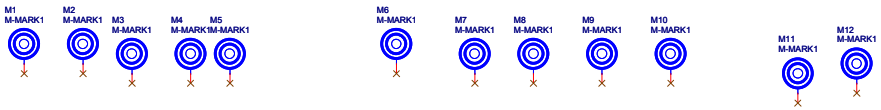


R1.0 MODIFY:ADD CAP FOR EMI

SCREW HOLE

M/B





Shuttle We create!

Title: HIGHSPEED-CAP / SCREW

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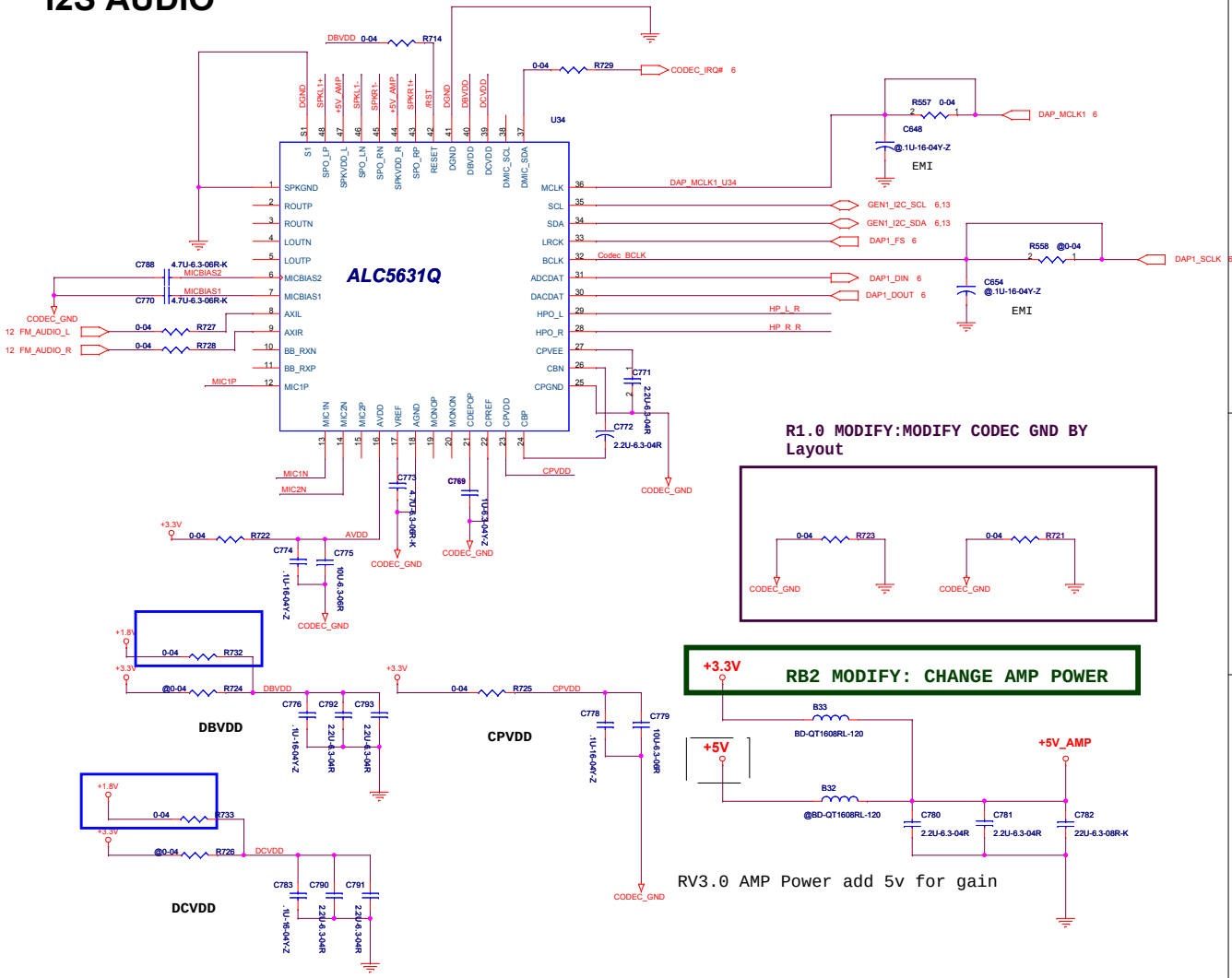
I2S AUDIO



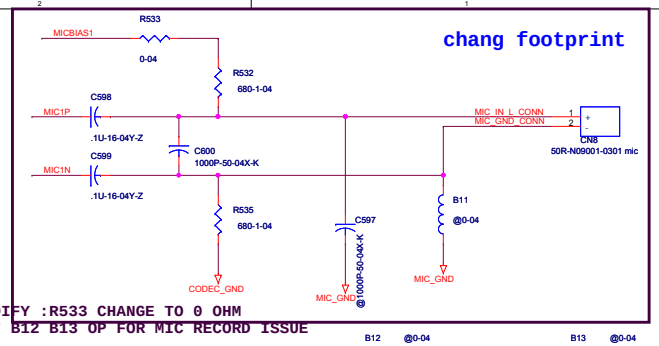
R1.0 MODIFY:MODIFY CODEC GND BY Layout

+3.3V RB2 MODIFY: CHANGE AMP POWER

RV3.0 AMP Power add 5v for gain

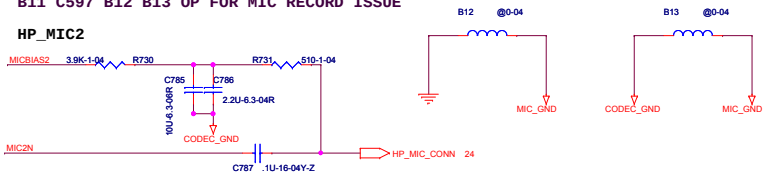
MIC

chang footprint



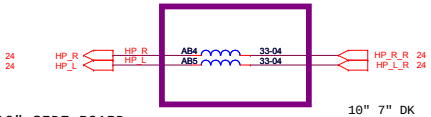
R1.0 MODIFY :R533 CHANGE TO 0 OHM
B11 C597 B12 B13 OP FOR MIC RECORD ISSUE

HP_MIC2



HP JACK-->TO SIDE BD

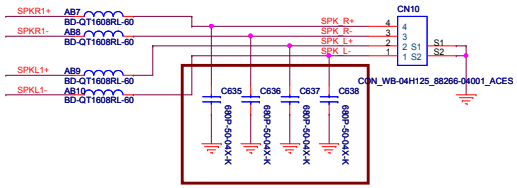
R1.0 MODIFY:Modufy AB4 AB5 FOR Crosstalk



10" SIDE BOARD

10" 7" DK
7" IO BOARD

SPEAKER



R1.0 MODIFY: FOR EMI

Title	AUDIO CODEC / HP JACK / SPK	
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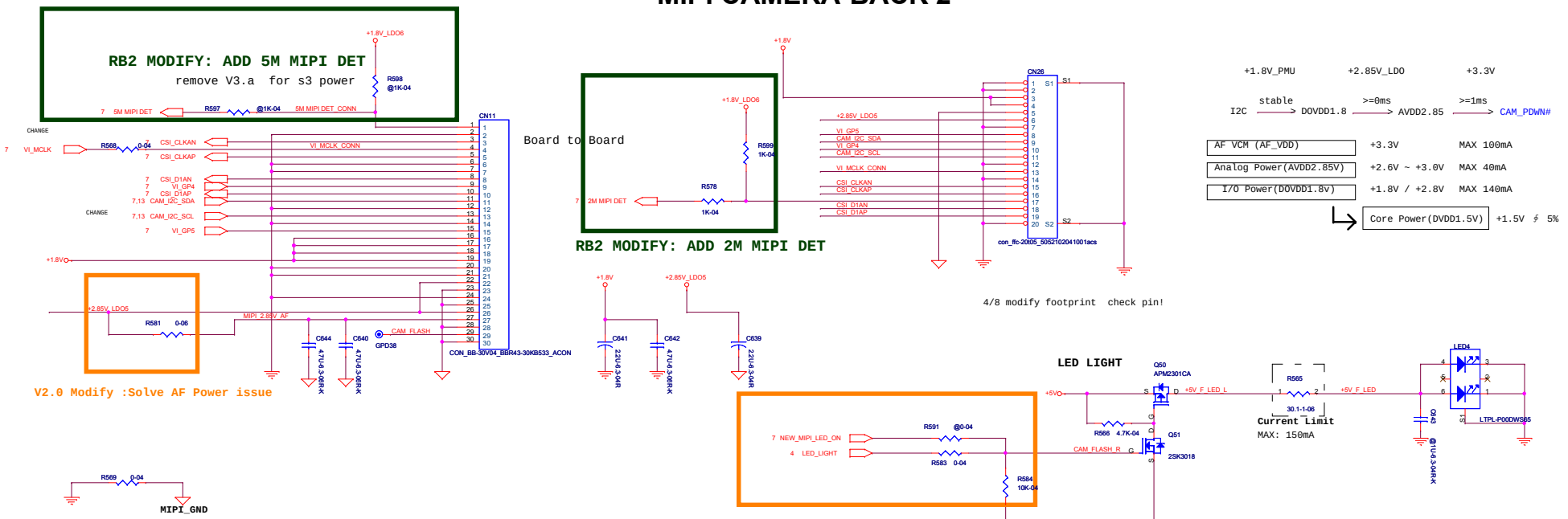
MIPI CAMERA-BACK 1

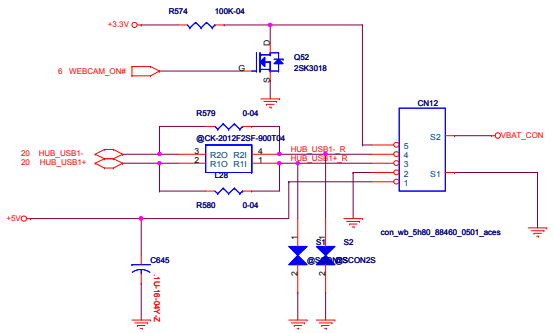


MIPI CAMERA-BACK 2

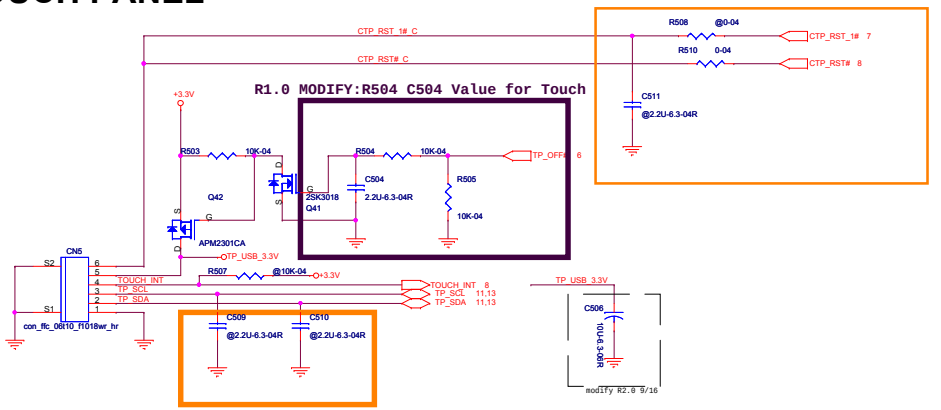
4/8 modify footprint check pin!

R1.0 MODIFY:MODIFY CHANGE VALUE TO 0 OHM

USB CAMERA-FRONT 1



TOUCH PANEL



Shuttle We create!

Title: <Title>

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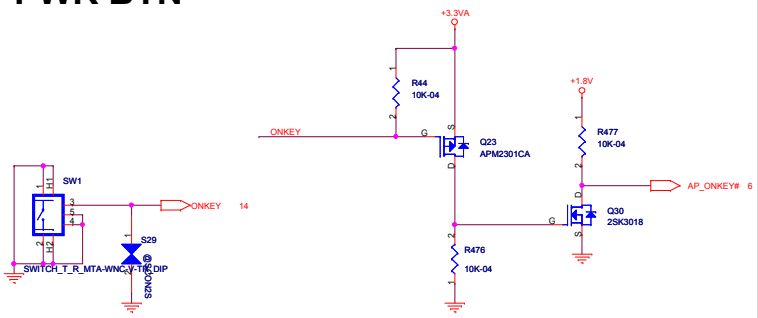
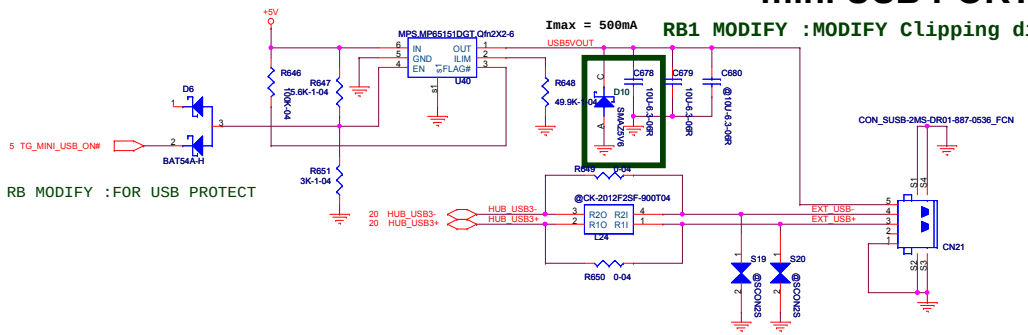
PWR BTN



mini USB PORT1

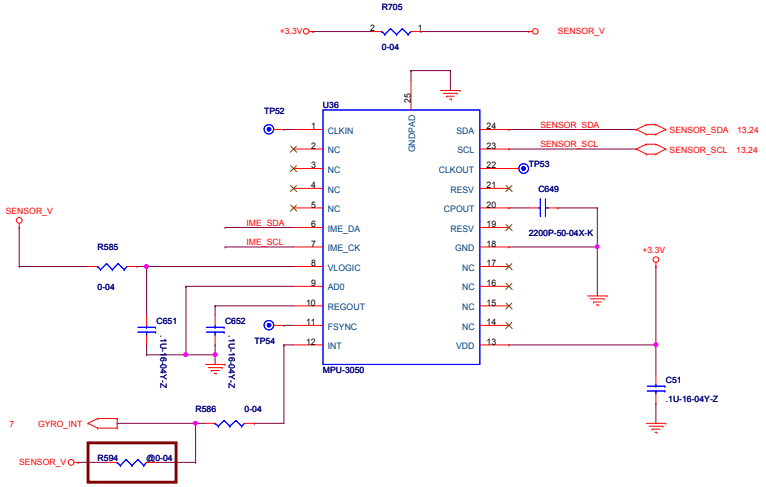
Imax = 500mA

RB1 MODIFY :MODIFY Clipping diode type



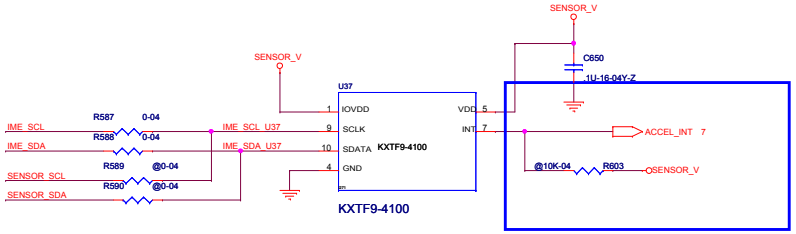
RB MODIFY :FOR USB PROTECT

GYROSCOPE

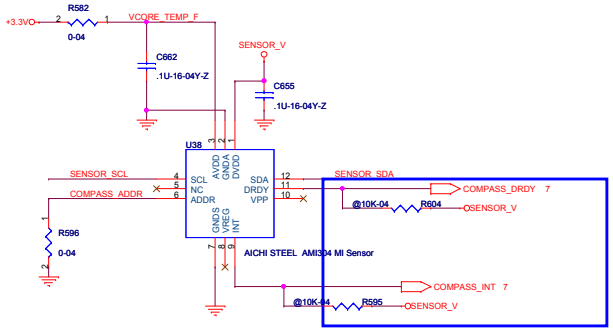


R1.0 MODIFY :MODIFY CHANGE TO OP

ACCELEROMETER (G-SENSOR)



COMPASS



Shuttle We create!

Title <Title>

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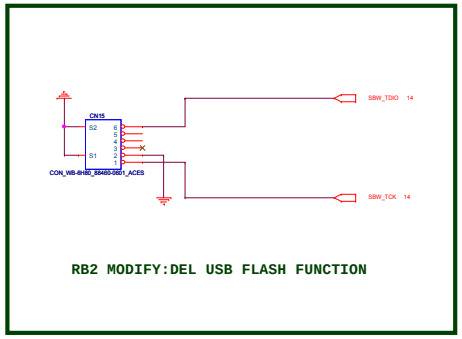
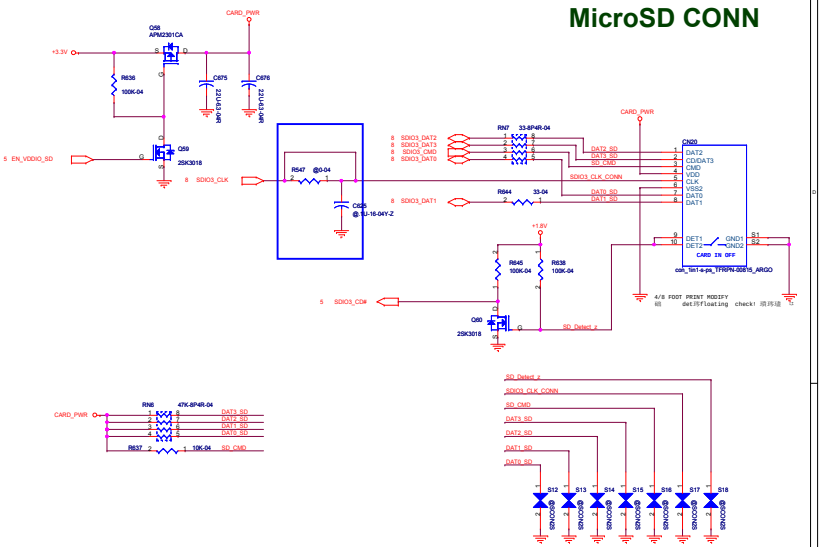
MCU FLASH CONN

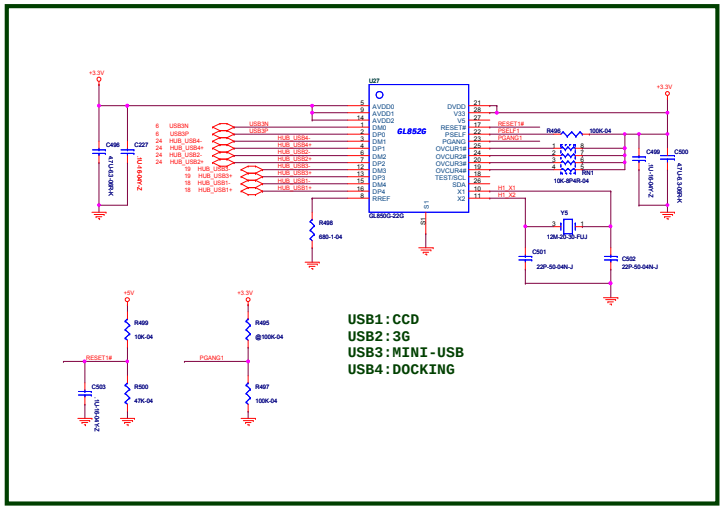


RB2 MODIFY:DEL USB FLASH FUNCTION

MicroSD CONN

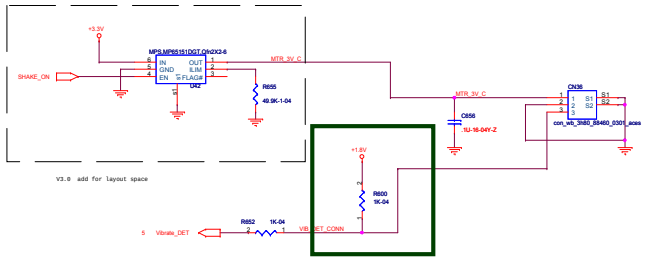


USB HUB



RB2 MODIFY:DEL USB HUB

VIB MOTOR SHAKE



RB1 MODIFY :ADD VIB PULL-HIGH

Shuttle			
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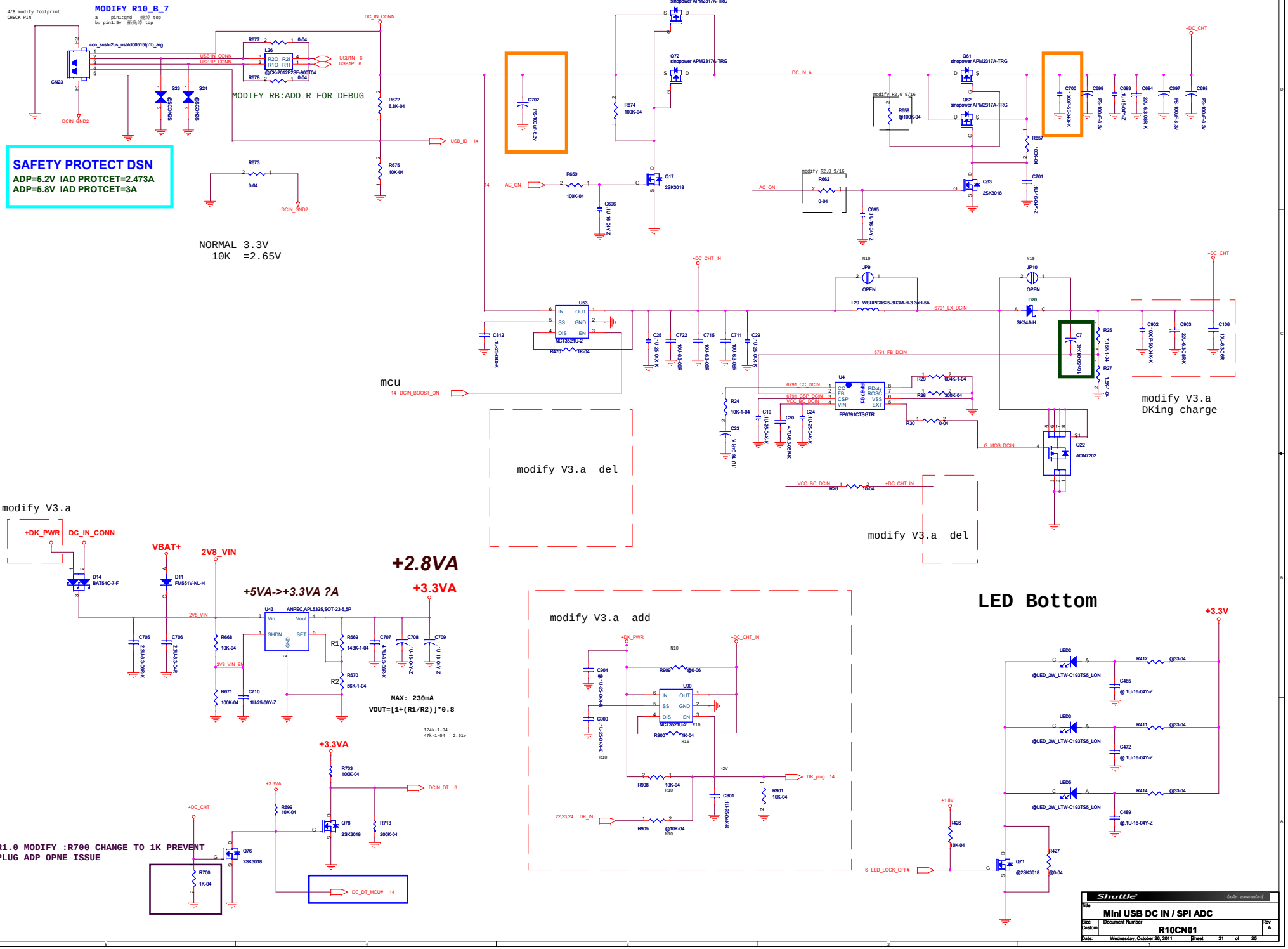
Micro USB DC-IN 5.5V DC Input 10W



CHARGER IC (MID_CB)

OZ8555

Charger LED Stat
Charging LED1 ON
Charge complete/no battery LED1 OFF
STOP CHARGE LED1 ON

BAT_INT
LOW: Charging
High: Charge complete/no battery

+5VA

modify V3.a for s3 power

+DC_CHT

R685 100K-04

R696 120k-04

14 BAT_FULL_MCU

C712 22U-6.3-08R-K

C717 PS-100uF-6.3v

R1.0 MODIFY:FOR INCREASE CHARGE VOTLAGE TO 4.24V

R679 10K-1-04

R680 30K-1-04

R681 56K-1-04

C714 1U-10-04R-K

ACAV

OZ8555_3.3V

ADPRE

C719 1U-25-04X-K

VBAT

U44 OZ8555LN

THM IAD ISET VSET VSIN CCHG ENCHG

VSYS VSYS V5V V5V LX LX NC LX S1

ICHP ICHM BSEL SSEL GNDP GNDP NC

14 ACAV

C720 10U-6.3-06R

R683 100-04

10W@3.5A

Q66 APM2327CA

Q68 APM2327CA

Q67 APM2301CA

Q69 APM2301CA

C716 10U-6.3-06R

VSYS

+5VA

C713 10U-6.3-06R

C718 10U-6.3-06R

C735 PS-100uF-6.3v

R3.0 ADD Q95 ,reduce power consumption

Q95 sinopower APM2317A-TRG

V3IN

C721 10U-6.3-06R

C724 10U-6.3-06R

Q81 sinopower APM2317A-TRG

VBATIN

VBAT+

Q70 sinopower APM2317A-TRG

5A

R691 1K-04

R682 10K-1-04

O2_BATTERY_TEMP

R1.0 MODIFY:FOR DOCKING CHARGE

R701 11K-1-04

modify R2_9 9/16

R697 5.1K-1-04

IAD

ISET

VSET

C725 1000P-50-04X-K

C723 10U-6.3-06R

L27 WSRPG0625-3R3M-H-3.3uH-5A

25m-1w-1-1206

U66 LX

BAT+ LX

R684

D12 1A/30V

R687 1-04

C728 470P-50-04X-K

R693 0-04

C727 1000P-50-04X-K

C729 10n-50-04X-K

C726 10U-6.3-06R

R1.0 MODIFY :FOR EMI

14 PC_CHARGE_EN

ADP :2.65A

Q85 2SK3018

C736

R689 9.53K-1-04

C730 10U-6.3-06R

C731 22U-6.3-08Y-Z

R688 90.9K-1-04

R686 80.6K-1-04

SSEL

BSEL

ICHM

ICHP

C732 @470P-50-04X-K

RB1 MODIFY :ADD C736 FOR CHARGE STABLE

V2.0 Modify :ADD Current control Pin

NORMAL :0.92A

21,23,24 DK_IN

Q87 MTN2002ZS3

DK:1.6A

+DC_CHT

R692 10K-04

R894 0-04

Q74 2SK3018

R690 1M-04

14 CHARGE_STOP

PJP1 CLOSE

Q75 2SK3018

R695 100K-04

BAT_LOW# 14

C733 2.2U-6.3-04R

modify R3.a

IAD VOLTAGE

3A output 4.95V
2A output 3.3V
1A output 1.65V

CHARGER CURRENT

ICHG=ISET/(50 X RCS)
闽诀
ISET=3.3V X R207/(R206+r207)
ISET=2.5V
ICHG=2.5V/(50 X 25mR)=2A

APC Table

Normal =14.7W
PC-CHARGE=2.5W
DOCKING =12W

APC CURRENT

APC=2667 X (3.3V / R689)
APC=2667X (3.3V/18K)
APC=0.5 * 5 =2.5W

CHARGER VOLTAGE

CHARGER Voltage =4V+(VSET/8)
VSET=3.3V X R13602/(R13602+R13607)
VSET=1.611V
VCHG=4+(1.611/8)=4.2V

Normal: 2A

ISET

R704 8.2K-1-02

Q90 2SK3018

PC: 0.5A

14 Small_Current_EN

+DK_PWR

R902 64.9K-1-04

R903 47K-04

+1.8V

Q91 MTN2002ZS3

R904 10K-1-04

Q92 2SK3018

DK: power off 1.45A

modify V3.a

2V8_VIN

U50

IN+ EN GND OUT NC

+2V

C84 1U-6.3-04Y-Z

R83 100K-1-04

S-1132B20-M5T1G

C85 1U-6.3-04Y-Z

+5VA

C277 @1000P-25-04X-K

C278 .1U-16-04R-K

+2V

R65 20K-1-04

U49 GS321

IN+ IN- VCC GND OUT

BAT_I 14

R61 1.5K-1-04

R62 0-06

C83 1U-6.3-04Y-Z

2W

RCH1 25m-1w-1-1206

C81 1U-6.3-04Y-Z

R356 16.5K-1-04

R357 1.5K-1-04

D15 UDZSNPTE-173.3B

C283 @.1U-16-04Y-Z

BATTERY CONN

VBAT+

B31 BD-QT2012RL-120

+3.3VA

VBAT_CON

CN24 CON_WB-12H125_882661200106_ACES

MCU_SDA

MCU_SCL

OZ8555_THERMAL

+3.3V

R382 10K-1-04

C750 1U-6.3-04Y-Z

C734 1U-6.3-04Y-Z

EMI

14 BATTERY_TEMP

13,14 MCU_SDA

13,14 MCU_SCL

CHARGER LED

+3.3VA

R5 220-04

R6 220-04

LED1 LED-C195-BR-S

R

B

CHG_LED_R# 14

CHG_LED_G# 14

C505 .1U-16-04Y-Z

C508 .1U-16-04Y-Z

Shuttle We create!

Title: CHARGER / +5VA

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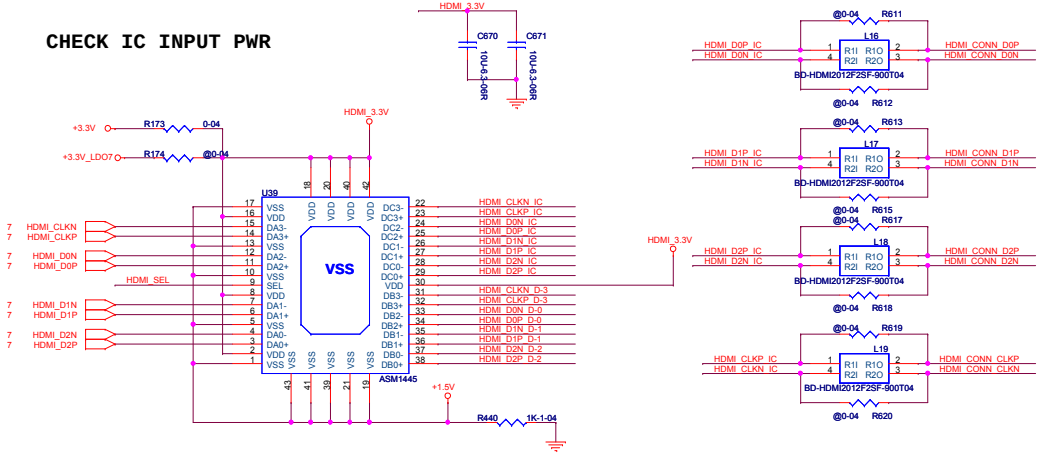
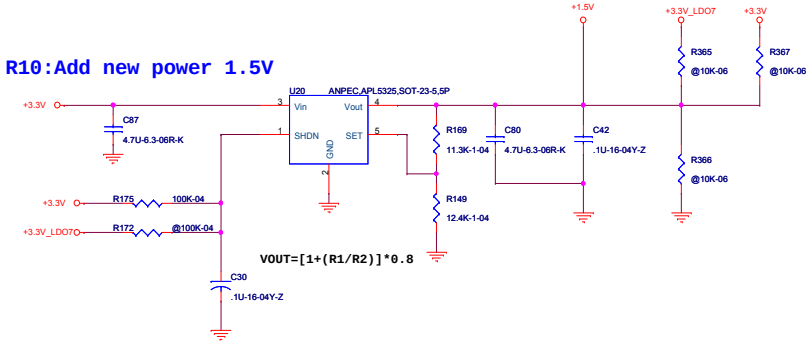
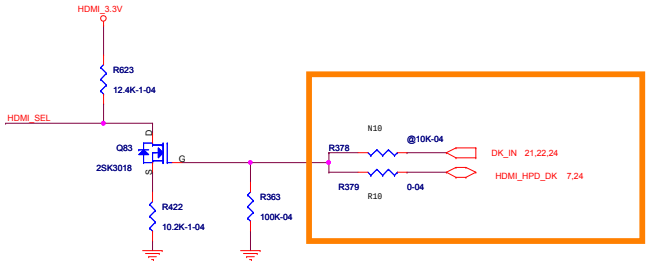
HDMI SW



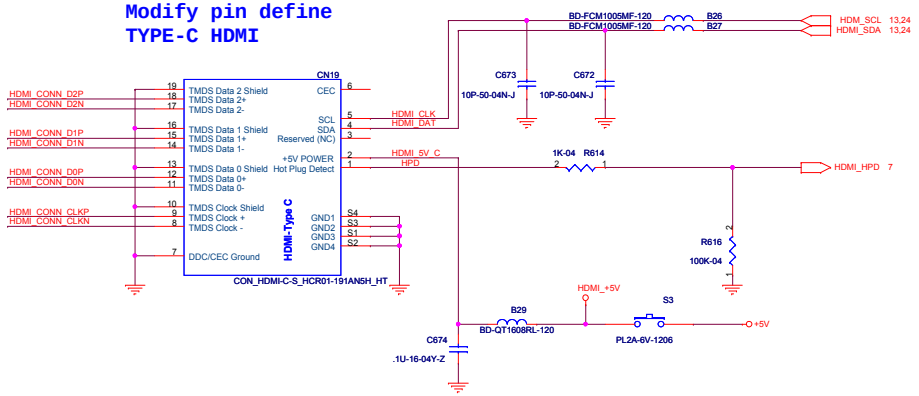




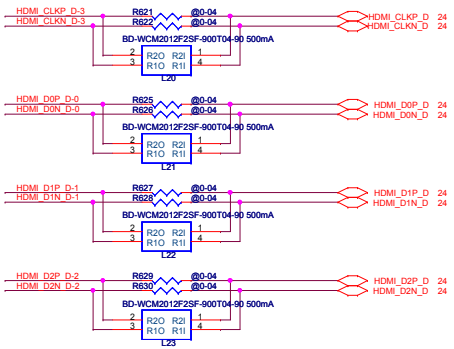
SEL PIN SET HIGH=DA TO DC(TO M/B)
SEL PIN SET LOW=DA TO DB(TO DOCKING)

HDMI CONN.

Modify pin define
TYPE-C HDMI



DOCKING



Shuttle We create!

Title	DOCKING / SIDE BD /MIPI	
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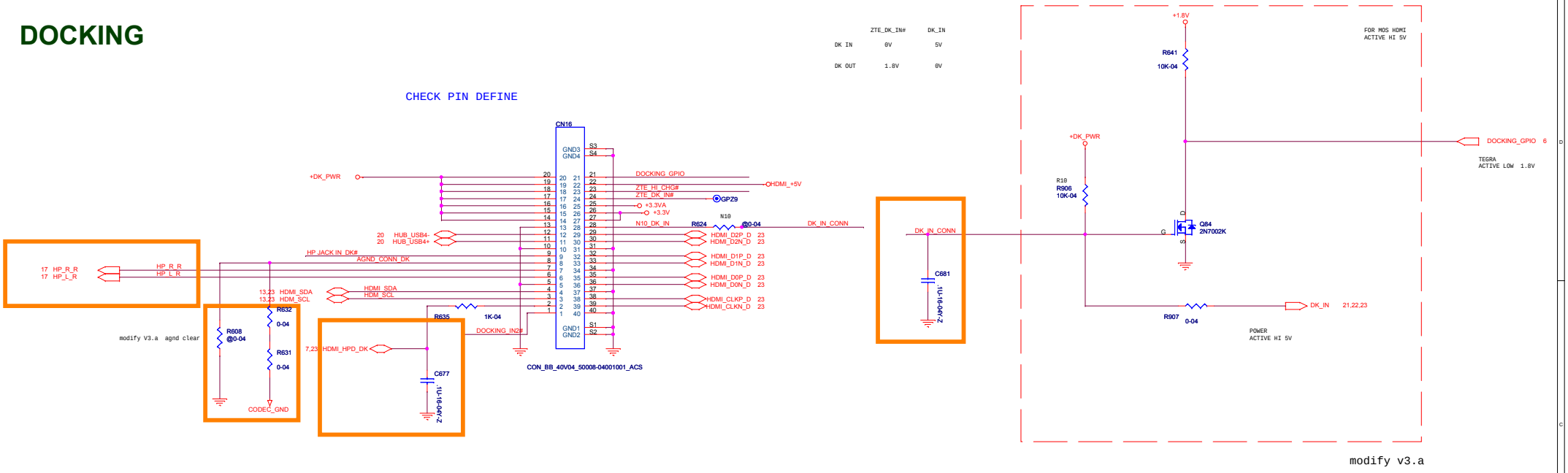
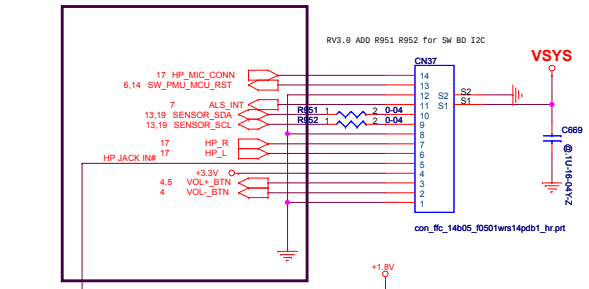
DOCKING

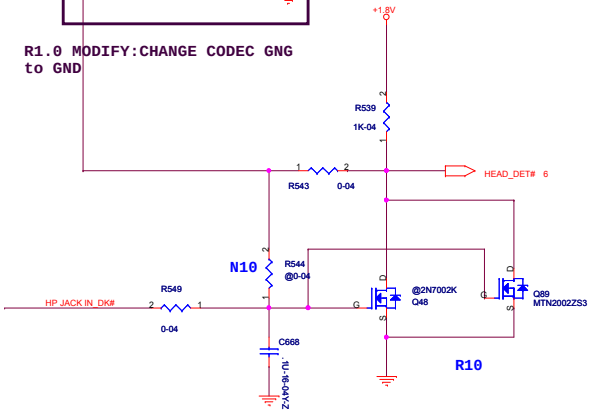


modify v3.a

SIDE BD TO AUDIO BD

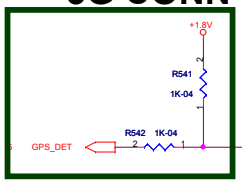


R1.0 MODIFY:CHANGE CODEC GNG to GND

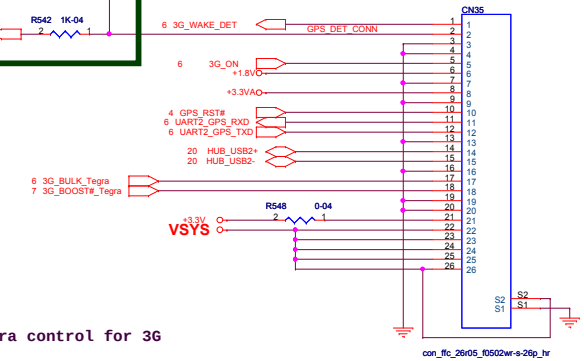


V2.0 Modify :FOR N/R Share Head_Det PIN

3G CONN



RB1 MODIFY :ADD GPS PULL-HIGH



HP JACK IN_DK#
ZTE Hi active R544 OP Q48
N10 Low active R544 Q48 OP

R1.0 MODIFY:ADD Tegra control for 3G

Shuttle	We create!		
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P11, MODIFY:R7 R16 C11 C5 ,add C950 FOR OTA 3G
     ADD U41 R654 for cut boost power
P22, ADD Q95 ,reduce power consumption
P17, AMP Power add 5v for gain
P24, ADD R951 R952 for SW BD I2C
P6,ADD JP11 &JP12 FOR EMI (BOTTOM SIDE)
P15,ADD C954 for 1.8V vin

Shuttle We create!

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Size: Custom	Document Number: <Doc>	Rev: <RevCode>
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