

Q1.2.3 Circuit Simulation for hw1.bench

abc = 011

2) Circuit Simulation

input test vector for inputs a, b, c,

test vector: 011

output w = 0

output x = 1

output y = 0

output z = 0

id	name	value
0	a	0
10	a'	1
1	b	1
11	b'	0
5	c	1
2	c'	0
9	d	0
4	e	1
12	f	0
13	g	0
14	h	0
3	w	0
6	x	1
7	y	0
8	z	0

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abc = 101

2) Circuit Simulation

input test vector for inputs a, b, c,

test vector: 101

output w = 0

output x = 1

output y = 1

output z = 0

id	name	value
0	a	1
10	a'	0
1	b	0
11	b'	1
5	c	1
2	c'	0
9	d	1
4	e	0
12	f	0
13	g	1
14	h	0
3	w	0
6	x	1
7	y	1
8	z	0

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