ISA TABLE OF OUR DESIGN

R0-15 (DEST)	R0-15 (SRC1)	R0-15 (SRC2)	00 (UNUSED)
R0-15 (DEST)	R0-15 (SRC1)	R0-15 (SRC2)	00 (UNUSED)
R0-15 (DEST)	R0-15 (SRC1)	R0-15 (SRC2)	00 (UNUSED)
R0-15 (DEST)	R0-15 (SRC1)	R0-15 (SRC2)	00 (UNUSED)
R0-15 (DEST)	R0-15 (SRC1)	IMM (6 bits)	
R0-15 (DEST)	R0-15 (SRC1)	IMM (6 bits)	
R0-15 (DEST)	R0-15 (SRC1)	IMM (6 bits)	
R0-15 (DEST)	R0-15 (SRC1)	IMM (6 bits)	
R0-15 (OP1)	R0-15 (OP2)	ADDR (6 bits)	
R0-15 (OP1)	R0-15 (OP2)	ADDR (6 bits)	
R0-15 (OP1)	R0-15 (OP2)	ADDR (6 bits)	
R0-15 (OP1)	R0-15 (OP2)	ADDR (6 bits)	
R0-15 (OP1)	R0-15 (OP2)	ADDR (6 bits)	
R0-15 (DEST)	ADDR (10 bits)		
R0-15 (SRC1)	ADDR (10 bits)		
ADDR (14 bits)			
	R0-15 (DEST) R0-15 (OP1)	R0-15 (DEST) R0-15 (SRC1) R0-15 (OPST) ADDR (10 bits) R0-15 (SRC1) ADDR (10 bits)	R0-15 (DEST) R0-15 (SRC1) R0-15 (SRC2) R0-15 (DEST) R0-15 (SRC1) R0-15 (SRC2) R0-15 (DEST) R0-15 (SRC1) R0-15 (SRC2) R0-15 (DEST) R0-15 (SRC1) IMM (6 bits) R0-15 (DEST) R0-15 (OP2) ADDR (6 bits) R0-15 (OP1) R0-15 (OP2) ADDR (6 bits) R0-15 (OP3) ADDR (10 bits)

R0 0000

R1 0001

R2 0010

R3 0011

R4 0100

R5 0101

R6 0110

R7 0111

R8 1000

R9 1001

R10 1010

R11 1011

R12 1100

R13 1101

R14 1110

R15 1111