## Grey Box Fuzzing the IoT

Program Monitoring for Memory and Power Constraint Embedded Devices

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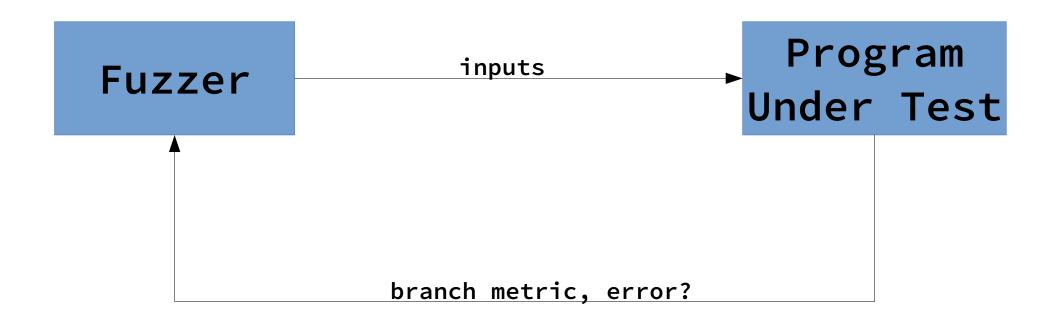
#### **Problems in Embedded Programming and Testing**

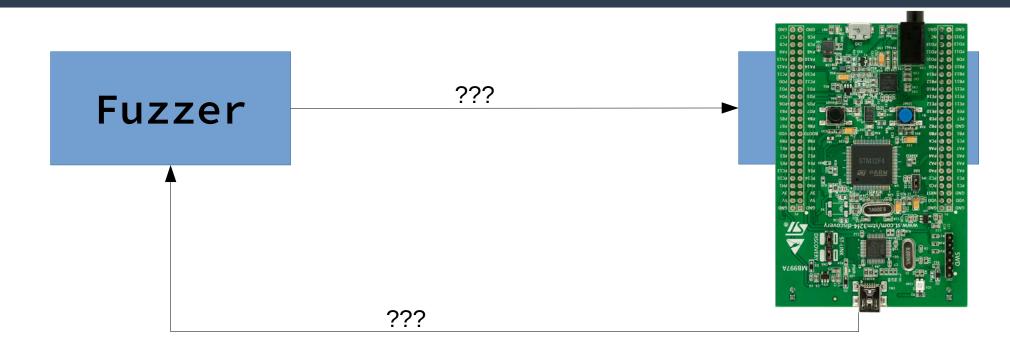
- No memory protection:
  - all processes can access all memory
  - hard to detect and contain failures
- Still predominantly programmed in C
- No logging, no automatic stack traces
- Lacks tools for automated testing

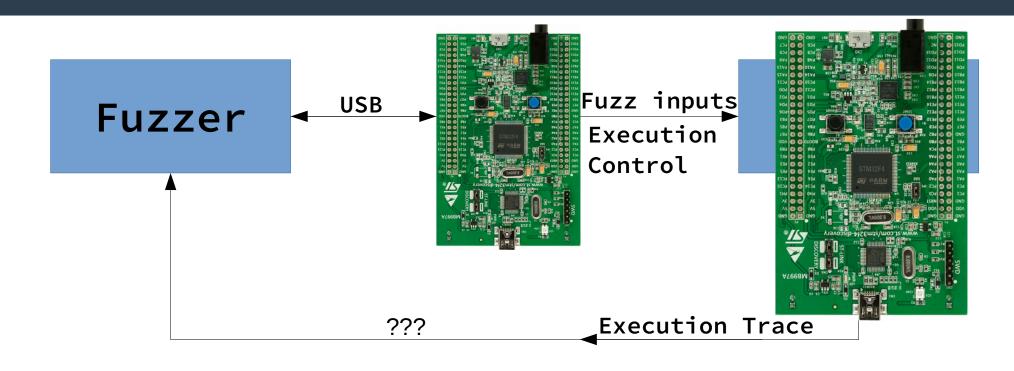
# Goal: Enable Fuzzing on Low Power Embedded Devices

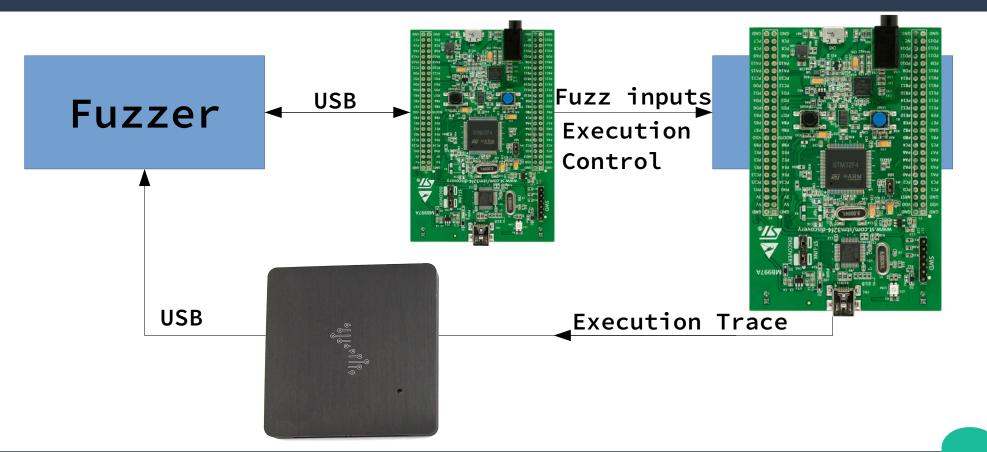
How?

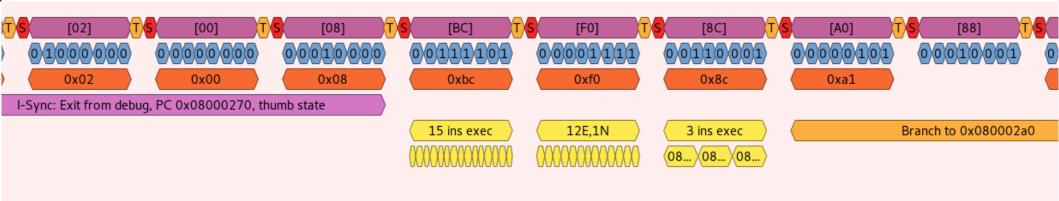
#### **Fuzzing**





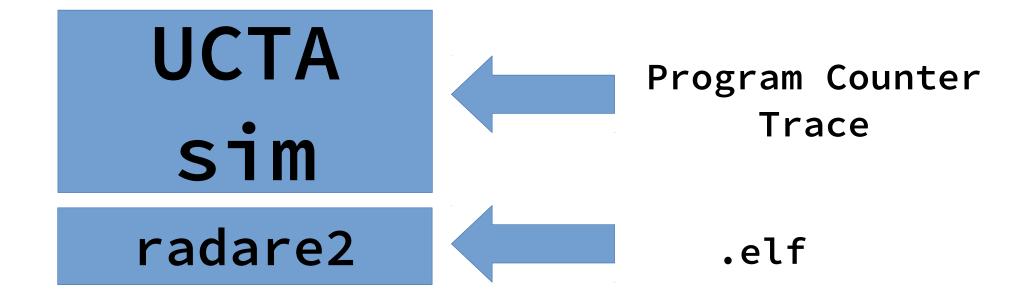






Instruction trace provides us with all program counter values, but no information about the memory state

#### Micro Controller Trace Analysis (UCTA)



```
void buggy_function(const uint8_t* packet) {
   char buffer[8];
   const uint8_t length = packet[0];
   std::memcpy(buffer, packet + 1, length);
}
```

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}
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```
;-- buggy function:
                              push {lr}
0x080001a0
               00b5
                              sub sp, 0xc
0x080001a2
               83b0
           0278
                              ldrb r2, [r0]
0x080001a4
               411c
                              adds r1, r0, 1
0x080001a6
0x080001a8
               6846
                              mov r0, sp
                              ldr r3, [0x080001c6]
               064b
0x080001aa
0x080001ac
               9847
                              blx r3
0x080001c0
               03b0
                              add sp, 0xc
0x080001c2
               00bd
                              foc} and
```

```
void buggy_function(const uint8_t* packet) {
   char buffer[8];
   const uint8_t length = packet[0];
   std::memcpy(buffer, packet + 1, length);
}
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```
return addr
;-- buggy function:
                                push {lr}
0x080001a0
                00b5
                                                               [0x0c]
                                sub sp, 0xc
0x080001a2
                83b0
0x080001a4
                0278
                                ldrb r2, [r0]
                411c
                                adds r1, r0, 1
0x080001a6
                                                               [0x0b]
0x080001a8
                6846
                                mov r0, sp
                                ldr r3, [0x080001c6]
                064b
0x080001aa
0x080001ac
                9847
                                blx r3
                                                                 . . . .
0x080001c0
                03b0
                                add sp, 0xc
                                                               [0x00]
0x080001c2
                00bd
```

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                0278
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                                                               [0x0b]
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                6846
                                mov r0, sp
                                ldr r3, [0x080001c6]
                064b
0x080001aa
0x080001ac
                9847
                                blx r3
                                                                 . . . .
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                03b0
                                add sp, 0xc
                                                               [0x00]
0x080001c2
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                00b5
                                                               [0x0c]
                                sub sp, 0xc
0x080001a2
                83b0
0x080001a4
                0278
                                ldrb r2, [r0]
                411c
                                adds r1, r0, 1
0x080001a6
                                                               [0x0b]
0x080001a8
                6846
                                mov r0, sp
                                ldr r3, [0x080001c6]
                064b
0x080001aa
0x080001ac
                9847
                                blx r3
                                                                 . . . .
0x080001c0
                03b0
                                add sp, 0xc
                                                               [0x00]
0x080001c2
                00bd
```

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                00b5
                                                              [0x0c]
                               sub sp, 0xc
0x080001a2
                83b0
                               ldrb r2, [r0]
0x080001a4
                0278
                411c
                               adds r1, r0, 1
0x080001a6
                                                              [0x0b]
                               mov r0, sp
0x080001a8
                6846
                               ldr (0x080001c6)
                064b
0x080001aa
                               blx
0x080001ac
                9847
                                                                . . . .
0x080001c0
                03b0
                               add
                                        0xc
                                                              [0x00]
0x080001c2
                00bd
```

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   char buffer[8];
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                     83b0
        0x080001a4
                     0278
                                 ldrb r2, [r0]
                     411c
                                 adds r1, r0, 1
        0x080001a6
                                                          [0x0b]
        0x080001a8
                     6846
                                 mov r0, sp
                                 ldr r3, [0x080001c6]
                     064b
        0x080001aa
                                 blx r3
        0x080001ac
                     9847
                                                            . . . .
        0x080001c0
                     03b0
                                 add sp, 0xc
                                                          [0x00]
                                                                       stack ptr
        0x080001c2
                     00bd
void buggy function(const
std::memcpy
                                                      packet) {
    char buffer[8];
    const uint8 t length = packet[0];
    std::memcpy(buffer, packet + 1, length);
```

```
return addr
;-- buggy function:
                                push {lr}
0x080001a0
                00b5
                                                              [0x0c]
                                sub sp, 0xc
0x080001a2
                83b0
0x080001a4
                0278
                                ldrb r2, [r0]
                411c
                                adds r1, r0, 1
0x080001a6
                                                              [0x0b]
0x080001a8
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                                mov r0, sp
                               ldr r3, [0x080001c6]
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0x080001aa
0x080001ac
                9847
                                blx r3
                                                                 . . . .
                                add sp, 0xc
0x080001c0
                03b0
                                                              [0x00]
                00bd
0x080001c2
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                                    adds r1, r0, 1
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                                    mov r0, sp
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                       064b
        0x080001aa
        0x080001ac
                       9847
                                    blx r3
                                                                 . . . .
        0x080001c0
                       03b0
                                    add sp, 0xc
                                                               [0x00]
        0x080001c2
                       00bd
void buggy function(const uint8 t* packet) {
    char buffer[8];
```

std::memcpy(buffer, packet + 1, length);

const uint8 t length = packet[0];

```
return_addr_locs.=.[]

def.on_store(addr,.value,.src_reg,.pc,.instr_count):

→ if.addr.in.return_addr_locs:

→ → raise.Exception("Return.address.overwriten.with.0x{:08x}.@.pc=0x{:08x}".format(value,.pc))

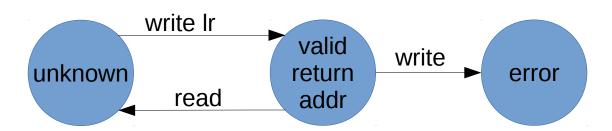
→ elif.src_reg.==.14:

→ → return_addr_locs.append(addr)

def.on_load(addr,.value,.dst_reg,.pc,.instr_count):

→ if.addr.in.return_addr_locs:

→ → return_addr_locs.remove(addr)
```



### Demo Time:

\$ wc -l pc

\$./sim.sh

#### **Outlook**

- Scale up: add support for more instructions, interrupts and fix bugs
- Automate trace analysis: this is required for automated fuzzing
- Collect branch metrics
- Implement more error detectors: stack overflow detector, detect out of bounds accesses, etc.