Bibliography

- [1] Intel. Intel CilkPlus; 2013. URL: http://cilkplus.org.
- [2] Microsoft. Microsoft Parallel Patterns Library; 2015. URL: https://msdn.microsoft.com/en-us/library/ dd492418.aspx.
- [3] MIT. M.I.T. Cilk; 2013. URL: http://supertech.csail.mit.edu.
- [4] Dennard R, Gaensslen F, Yu HN, Rideout L, Bassous E, LeBlanc A. Design of ion-implanted MOSFET's with very small physical dimensions; 1974. URL: www.ece.ucsb.edu/courses/ECE225/225_WOBanerjee/reference/Dennard.pdf.
- [5] Kirk D, Hwu WM. Programming massively parallel processors. Waltham, MA: Morgan Kaufmann; 2013. ISBN 978-0-12-415992-1.
- [6] Wilt N. The CUDA handbook. Waltham, MA: Addison-Wesley; 2012. ISBN 978-0-321-80946-9.
- [7] Sanders J, Kandrot E. CUDA by example. Waltham, MA: Addison-Wesley; 2011. ISBN 978-0-13-138768-3.
- [8] Jeffers J, Reinders J. Intel Xeon Phi coprocessor. Waltham, MA: O'Reilly; 2012. ISBN 978-0-12-410414-3.
- [9] Intel. Intel Xeon Phi documentation; 2013. URL: http://intel.com/software/mic.
- [10] Gove D. Multicore application programming. Boston, MA: Addison-Wesley; 2011. ISBN 978-0-321-71137-3.
- [11] Butenhof D. Programming with POSIX threads. Reading, MA: Addison-Wesley; 1997. ISBN 978-2-7440-7182-9.
- [12] Nichols B, Buttlar D, Proulx Farrel J. Pthreads programming. Sebastopol, CA: O'Reilly; 1998. ISBN 1-56592-115-1.
- [13] Microsoft. Windows API home page; 2015. URL: https://msdn.microsoft.com/en-us/library/ff818516(v=vs. 85).aspx.
- [14] Williams A. C++ concurrency in action. Shelter Island, NY: Manning Publications; 2012. ISBN 978-1-933988771.
- [15] GNU compiler collection site; 2015. URL: http://gcc.gnu.org.
- [16] Press W, Teukolsky S, Vettering W, Flannery B. Numerical recipes in C. 2nd ed. New York, NY: Cambridge University Press; 1992. ISBN 0-521-43108-5.
- [17] Intel Threading Building Blocks. TBB reference manual; 2015. URL: http://software.intel.com/en-us/node/ 506130.
- [18] Intel Threading Building Blocks. Download and documentation TBB site; 2015. URL: http://www.threadingbuildingblocks.org.
- [19] Boehm HJ. Threads cannot be implemented as a library; 2004. URL: http://www.hpl.hp.com/2004/HPL-2004-209.html.
- [20] Adve S, Boehm HJ. Memory models: a case for rethinking parallel languages and hardware. Commun ACM 2010; 53(8):91–101.
- [21] Adve S, Boehm HJ. Memory models: a case for rethinking parallel languages and hardware; 2002-2001. URL: http://rsim.cs.uuic.edu/Pubs/10-cacm-memory-models.pdf.
- [22] Boehm HJ. Threads basics; 2009. URL: http://www.hpl.hp.com/2009/HPL-2009-259.html.
- [23] Lamport L. How to make a multiprocessor computer that correctly execute multiprocess programs. IEEE Trans Comput 1979; C-3(9):241–248.
- [24] Patterson D, Hennessy J. Computer organization and design: the hardware/software interface. 4th ed. Burlington, MA: Morgan Kaufmann; 2009. ISBN 978-2-7440-7182-9.
- [25] Herlijy M, Shavit N. The art of multiprocessor programming. Waltham, MA: Morgan Kaufmann; 2012. ISBN 978-0-12-397337-5.

- [26] Chapman B, Yost G, van der Paas R. Using OpenMP: portable shared memory parallel programming. Cambridge, MA: The MIT Press; 2007. ISBN 978-2-7440-7182-9.
- [27] ARB. Official Architecture Review Board (ARB) OpenMP site; 2013. URL: http://www.openmp.org.
- [28] ARB. OpenMP application programming interface; 2013. URL: http://www.openmp.org/mp-documents/ OpenMP4.0.pdf.
- [29] ARB. OpenMP application programming interface–examples; 2014. URL: http://www.openmp.org/mp-documents/OpenMP_Examples_4.0.1.pdf.
- [30] de Supinski B, Klemm M, Stotzer E, Terboven C, van der Paas R. Advanced OpenMP: performance and OpenMP4.0 features; 2014. URL: sc14.supercomputing.org/program/tutorials.
- [31] Terboven C. SC14: a short stroll through OpenMP4.0; 2014. URL: http://terboven.com.
- [32] Intel Threading Building Blocks. TBB getting started (tutorial); 2015. URL: https://www.threadingbuildongblocks.org/intel-tbb-tutorial.
- [33] Intel Threading Building Blocks. TBB user guide and flow graph; 2015. URL: http://software.intel.com/en-us/node/506045.
- [34] Reinders J. Intel Threading Building Blocks. Waltham, MA: O'Reilly; 2007. ISBN 978-0-596-52480-8.
- [35] McCool M, Robison A, Reinders J. Structured parallel programming. Waltham, MA: Morgan Kaufmann; 2012. ISBN 978-0-12-415993-8.
- [36] Intel. Guide to vectorization with Intel C++ compilers; 2013. URL: http://tinyurl.com/intelautovec.
- [37] Lamport L. The parallel execution of DO loops. Commun ACM 1974; 17(2):83–93.