a2Spark designed for Digispark

| Name | 15 | | Bit . | | | | | | | | |
|------------------------------------|--------|--------|-------|------|------|------|------|------|------|------|--------|
| | ID | Offset | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | Access |
| I2C Address Register | ADDR | 0x00 | ADR7 | ADR6 | ADR5 | ADR4 | ADR3 | ADR2 | ADR1 | ADR0 | RO |
| Firmware Version Register | FWVR | 0x01 | FWV7 | FWV6 | FWV5 | FWV4 | FWV3 | FWV2 | FWV1 | FWV0 | RO |
| Reserved Register | - | 0x02 | - | - | - | - | - | _ | - | - | RO |
| Reserved Register | - | 0x03 | - | - | - | - | - | - | - | - | RO |
| Analog1 Registers | | | | | | | | | | | |
| Analog1 Configuration Register | AN1CR | 0x04 | - | - | - | - | - | - | LPEN | ANEN | RW |
| Low-Pass Filter1 Register | LPF1R | 0x05 | - | - | - | - | LP3 | LP2 | LP1 | LP0 | RW |
| Analog1 Data Register Low | AN1DRL | 0x06 | AN7 | AN6 | AN5 | AN4 | AN3 | AN2 | AN1 | AN0 | RO |
| Analog1 Data Register High | AN1DRH | 0x07 | - | - | - | - | - | - | AN9 | AN8 | RO |
| Alarm1 Registers | | | | | | | | | | | |
| Alarm1 Configuration Register | AL1CR | 0x08 | ALM2 | ALM1 | ALM0 | ALME | - | - | ALO | ALEN | RW |
| Alarm1 Set Value Register Low | AL1SVL | 0x09 | ALS7 | ALS6 | ALS5 | ALS4 | ALS3 | ALS2 | ALS1 | ALS0 | RW |
| Alarm1 Set Value Register High | AL1SVH | 0x0A | - | - | - | - | - | - | ALS9 | ALS8 | RW |
| Alarm1 Clear Value Register Low | AL1CVL | 0x0B | ALC7 | ALC6 | ALC5 | ALC4 | ALC3 | ALC2 | ALC1 | ALC0 | RW |
| Alarm1 Clear Value Register High | AL1CVH | 0x0C | - | - | - | - | - | - | ALC9 | ALC8 | RW |
| Reserved Register | - | 0x0D | - | - | - | - | - | - | - | - | RO |
| Reserved Register | - | 0x0E | - | - | - | - | - | - | - | - | RO |
| Reserved Register | - | 0x0F | - | - | - | - | - | - | - | - | RO |
| Temperature Configuration Register | TCR | 0x10 | - | - | - | - | - | TUS1 | TUS0 | TEN | RW |
| Temperature Data Register Low | TDRL | 0x11 | TP7 | TP6 | TP5 | TP4 | TP3 | TP2 | TP1 | TP0 | RO |
| Temperature Data Register High | TDRH | 0x12 | - | - | - | - | - | - | TP9 | TP8 | RO |
| Reserved Register | - | 0x13 | - | - | - | - | - | - | - | - | RO |
| Analog2 Registers | | | | | | | | | | | |
| Analog2 Configuration Register | AN2CR | 0x14 | - | - | - | - | - | - | LPEN | ANEN | RW |
| Low-Pass Filter2 Register | LPF2R | 0x15 | - | - | - | - | LP3 | LP2 | LP1 | LP0 | RW |
| Analog2 Data Register Low | AN2DRL | 0x16 | AN7 | AN6 | AN5 | AN4 | AN3 | AN2 | AN1 | AN0 | RO |
| Analog2 Data Register High | AN2DRH | 0x17 | - | - | - | - | - | - | AN9 | AN8 | RO |
| Alarm2 Registers | | | | | | | | | | | |
| Alarm2 Configuration Register | AL2CR | 0x18 | ALM2 | ALM1 | ALM0 | ALME | - | - | ALO | ALEN | RW |
| Alarm2 Set Value Register Low | AL2SVL | 0x19 | ALS7 | ALS6 | ALS5 | ALS4 | ALS3 | ALS2 | ALS1 | ALS0 | RW |
| Alarm2 Set Value Register High | AL2SVH | 0x1A | - | - | - | - | - | - | ALS9 | ALS8 | RW |
| Alarm2 Clear Value Register Low | AL2CVL | 0x1B | ALC7 | ALC6 | ALC5 | ALC4 | ALC3 | ALC2 | ALC1 | ALC0 | RW |
| Alarm2 Clear Value Register High | AL2CVH | 0x1C | - | - | - | - | - | - | ALC9 | ALC8 | RW |
| Reserved Register | _ | 0x1D | _ | _ | _ | _ | _ | _ | _ | - | RO |
| Reserved Register | - | 0x1E | - | - | - | - | - | - | - | - | RO |
| Reserved Register | - | 0x1F | - | - | - | - | - | - | - | - | RO |