Here, we describe a PCM-based program logic for reasoning about concurrent programs, with a built-in notion of "PCM refinement" to provide modularity that is both useful in its own right and particularly useful for reasoning about "read-only" state

## Monoid definitions

We start by defining what it means to be a PCM with a transition relation  $\mapsto$ . We call this a *Transitional PCM* (TPCM).

Let 
$$\widehat{M} = (M, \cdot, \mapsto)$$
 where where  $\cdot : M \times M \to M^?$  and  $\mapsto : M \times M \to bool$ . (We use  $M^? \triangleq M \cup \{\bot\}$ .)

We say that  $\mapsto$  is *monotonic* if

$$\forall a, b, c. (a \mapsto b) \land (a \cdot c \neq \bot) \implies (b \cdot c \neq \bot) \land (a \cdot c \mapsto b \cdot c)$$

This allows us to define what it means to be a TPCM,

$$\widehat{M} = (M, \cdot, \mapsto)$$

$$\cdot : M \times M \to M^?, \text{ comm., assoc.}$$

$$\mapsto : M \times M \to \text{bool, trans., refl., monotonic}$$

$$\overline{\text{TPCM}(\widehat{M})}$$
 (TPCM)

Note that we can always construct a TPCM by picking  $\cdot$  to be commutative and associative, and then by definition setting  $\mapsto$  to be,

$$a \mapsto b = \forall c. \ a \cdot c \neq \bot \implies b \cdot c \neq \bot$$

However, we can also set  $\mapsto$  to be more restrictive, which will be useful later.

## **TPCM Example**

Consider a Sharded State Machine (SSM). (This is similar to the STS common in the literature but framed a little differently.)

An SSM is given by (i) some PCM state  $S, \cdot, (ii)$  an invariant  $inv : S \rightarrow bool$  and a sharded transition  $\Rightarrow : S \times S \rightarrow bool$ . For a sharded state machine, the invariant inductivity predicate is given by,

$$inv(a \cdot c) \land (a \leadsto b) \implies inv(b \cdot c)$$

(Note:  $\rightsquigarrow$  is neither transitive nor required to be monotonic here. However, an alternative definition would require  $\rightsquigarrow$  to be monotonic, in which case we could simplify the inductivity predicate to  $inv(a) \land (a \rightsquigarrow b) \implies inv(b)$ .)

We can define a TPCM to correspond to this SSM by,

$$M \triangleq \{m \in S : \exists c. inv(m \cdot c)\}$$

$$a \cdot_M b \triangleq \begin{cases} a \cdot b & \text{if } a \cdot b \in M \\ \bot, & \text{otherwise} \end{cases}$$

and finally we can define  $\mapsto$  as the transitive closure of  $\mapsto_{pre}$  defined as,

$$a \mapsto_{\mathrm{pre}} b \triangleq \exists a_0, b_0, c. \ (a = a_0 \cdot c) \land (b = b_0 \cdot c) \land a_0 \rightsquigarrow b_0$$

## **TPCM refinements (motivation)**

There are a handful of reasons to want to modularize PCM definitions:

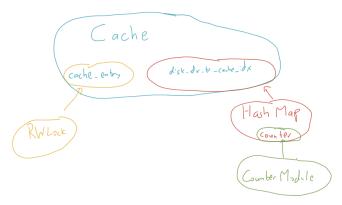
- The "base language" may provide some "points to" assertion  $\ell \hookrightarrow v$ , and then the user wants to build something else on top of that (like a fractional permission logic or similar) without needing to inject it into the native definition.
- You have some PCM N (custom built for your application), and you want to build a reader-writer lock to manage access to the state of N, without encoding the reader-writer lock protocol into N.
- You have some PCM N, and you want to manage part of it via some hash table implementation. You want to verify the hash table implementation independently (e.g., via some SSM strategy) and then use it modularly to manage some part of the N-state.

• You have some PCM N with a counter field, and you want to write the counter-manipulation code as its own independent module.

These are all things that have come up in VeriBetrKV.

We'll develop the concept of a TPCM refinement, denoted  $\widehat{M} \gg \widehat{N}$ . In a modular design,  $\widehat{M}$  will be the "sub-component", while  $\widehat{N}$  is the client.

For example, a hypothetical cache architecture might have a "main" ghost state Cache represented as an SSM. We might have a sub-component RwLock  $\Rightarrow$  Cache, another sub-component HashTable  $\Rightarrow$  Cache, and even further a sub-component Counter  $\Rightarrow$  HashTable.



#### **TPCM** refinements

We'll use the symbol → to denote refinement. Yeah, you can tell I scoured the LATEX documentation for this thing, but I actually like it: it suggests that the thing on the left is more "broken up" (e.g., like in a fractional logic).

We use  $\widehat{M} \gg_{\mathrm{rel}} \widehat{N}$  to denote that  $\widehat{M}$  refines  $\widehat{N}$  via a relation rel :  $M \times N \to bool$ . Of course, it must satisfy some conditions (and this is why we allow for flexibility in the definition of  $\mapsto$  above).

$$\begin{split} & \operatorname{TPCM}(\widehat{M}) \quad \operatorname{TPCM}(\widehat{N}) \\ & \operatorname{rel}: M \times N \to \operatorname{bool} \\ & \operatorname{rel}(\varepsilon, \varepsilon) \\ & (\forall b, b', q. \ b \mapsto b' \wedge \operatorname{rel}(b, q) \implies \exists q'. \ \operatorname{rel}(b', q') \wedge (q \mapsto q')) \\ & \frac{\forall b, a, a'. \ \operatorname{rel}(b, a) \wedge \operatorname{rel}(b, a') \implies a \mapsto a'}{\widehat{M} \gg_{\operatorname{rel}} \widehat{N}} \end{split} \tag{TPCM-Refinement}$$

#### Using ghost state in programs

We instantiate instances of monoidal ghost state at *locations*. The notation  $\begin{bmatrix} a \end{bmatrix}^{\gamma}$  means that state a is at location  $\gamma$ . Typically, locations are considered arbitrary values; here, we have to do a little more to handle the relationships between different ghost states.

For example, suppose we have a refinement  $\widehat{M} \gg_{\text{rel}} \widehat{N}$ . Perhaps  $\widehat{M}$  represents a reader-writer protocol or something similar. Furthermore, let's say we're tracking ghost state  $n \in N$  at location  $\gamma$ : we will use the notation  $\lceil n \rceil^{\gamma}$ .

Now, we want to use the  $\widehat{M}$  protocol to help manage that state. We'll need some rules for "exchanging" the  $\widehat{M}$ -state with  $\widehat{N}$ -state. Furthermore, when we manage the  $\widehat{M}$ -state, we'll need to somehow "remember" that it corresponds to the  $\widehat{N}$ -state at  $\gamma$ , via the refinement rel. We will track this information in the location value.

Thus, we'll define the location values recursively, reusing the  $\Rightarrow$  notation:

$$\gamma := \alpha \mid \alpha \Rightarrow_{\text{rel}} \gamma.$$

Here,  $\alpha$  denotes any "base location." We will define rules that let us convert  $[n]^{\gamma}$  state to and from  $[n]^{\gamma}$  state.

Here are our rules for manipulating TPCM ghost state, which are mostly standard except the ones involving ⇒.

### Justification.

To justify these rules, we can roll up all the  $\begin{bmatrix} \vec{n} \end{bmatrix}^{\gamma}$  values into a mega-monoid (or resource algebra) one defined as a mapping from locations to monoid elements. Then we show that all those rules above follow from frame-preserving updates.

# Notes about abstraction.

When using a refinement  $\widehat{M} \Rightarrow \widehat{N}$  to abstract out some subcomponent, we observe that  $\widehat{M}$  (which corresponds to the subcomponent) needs to be defined with respect to the base state  $\widehat{N}$ , which initially seems a little strange. Shouldn't we define the subcomponent *first*, and then build the larger components out of those?

And yes, if for example  $\widehat{M}$  is a reader-writer lock, then  $\widehat{M}$  and the refinement rel will need to be defined in terms of  $\widehat{N}$ . However, this is not really so different than using a templated type, e.g., writing RwLock<T> to create a mutex storing type T.

# **Borrows and read-only state.**

I think we can use a fairly simple system for representing (shared) borrows. We use  $\kappa$  to represent a lifetime variable.  $\&^{\kappa}A$  represents some borrowed proposition; active( $\kappa$ ) indicates that lifetime  $\kappa$  is active, and borrowed( $\kappa$ , A) indicates you can get back exclusive access to A once the lifetime  $\kappa$  expires.

We use  $\kappa_1 \sqcap \kappa_2$  to represent lifetime intersection and  $\kappa_1 \sqsubseteq \kappa_2$  to represent lifetime inclusion.

Of course, the intent is that the verification framework will use some sort of borrow-checker to identify the lifetimes and perform these automatically.

(Note: RustBelt uses a much more complex system - need to understand why; am I missing something or is it for some feature I don't need?)

$$A \Rightarrow \exists \kappa. \ \operatorname{active}(\kappa) * \operatorname{borrowed}(\kappa, A) * \&^{\kappa} A$$
 (Borrow-BEGIN)

$$\overline{\operatorname{active}(\kappa) * \operatorname{borrowed}(\kappa, A) \Rrightarrow A} \quad \text{(Borrow-END)}$$

$$\overline{\&^{\kappa} A \Rrightarrow \&^{\kappa} A * \&^{\kappa} A} \quad \text{(Borrow-DUPE)}$$

$$\frac{\kappa_1 \sqsupseteq \kappa_2}{\&^{\kappa_1} A \Rrightarrow \&^{\kappa_2} A} \quad \text{(Reborrow)}$$

$$\operatorname{active}(\kappa_1 \sqcap \kappa_2) \iff \operatorname{active}(\kappa_1) * \operatorname{active}(\kappa_2)$$

Next, let's add some rules for making use of borrowed TPCM state.

$$\operatorname{True} \implies \&^{\kappa} [\varepsilon]^{\gamma}$$

$$\operatorname{active}(\kappa) * \&^{\kappa} [\bot]^{\gamma} \implies \operatorname{False}$$

$$\frac{\forall r. \ m \leq r \wedge n \leq r \implies k \leq r}{\&^{\kappa} [m]^{\gamma} * \&^{\kappa} [n]^{\gamma} \Rightarrow \&^{\kappa} [k]^{\gamma}} \text{ (Borrow-Combine)}$$

$$\frac{c \cdot a \mapsto c \cdot b}{\operatorname{active}(\kappa) * \&^{\kappa} [c]^{\gamma} * [a]^{\gamma} \Rightarrow \operatorname{active}(\kappa) * [b]^{\gamma}} \text{ (Borrow-Ghost-ViewShift)}$$

Finally, rules to allow borrows to interact with TPCM refinements:

$$\frac{\widehat{M} \gg_{\text{rel}} \widehat{N}}{\forall p, b. \ \text{rel}(m \cdot p, b) \implies a \leq b}$$

$$\frac{\forall p, b. \ \text{rel}(m \cdot p, b) \implies a \leq b}{\&^{\kappa} \left[ m \right]^{\delta \gg \gamma}} \implies \&^{\kappa} \left[ a \right]^{\gamma}$$
(BORROW-BACK)

Note that Borrow-Ghost-Refinement-Exchange allows us to utilize a borrow in the extension world  $\delta \Rightarrow \gamma$  but *not* a borrow in the base world  $\gamma$ . This is important as we would have an unsoundness otherwise. In general, we can't have any rule that allows a borrow in the  $\gamma$  world to flow back to the  $\delta \Rightarrow \gamma$  world, as then that borrow could "interfere" with the  $\delta \Rightarrow \gamma$ -world's state which is being held "in reserve" for that borrow.

## **Justification**

As before, our justification will be to roll all the ghost state, the  $\&^{\kappa} [a]^{\gamma}$  tokens, the active tokens, and the borrowed tokens into a single mega-monoid, and derive the above rules as frame-preserving updates.

There's one trick worth pointing out. Intuitively, we're going to have some invariant that says the following:

Suppose we have  $\operatorname{active}(\kappa) * \&^{\kappa} [a]^{\forall}$  with  $a \in M$ . Then, consider:

• All "reserved"  $\gamma$ -state: borrowed( $\kappa'$ ,  $\lceil b_{\perp}^{\neg \gamma}$ )

• All state in an extension  $\delta$  of  $\gamma$ , both *borrowed and non-borrowed*. By "extension"  $\delta$ , we mean any location  $\alpha \Rightarrow \gamma$ ,  $\alpha \Rightarrow (\beta \Rightarrow \gamma)$ , and so on, (and *not* including  $\gamma$  itself). Project all this state down to M via the refinement relations.

*Borrow state invariant*: Then the monoidal sum of all this state gives some value t where  $a \le t$ .

Essentially, this says that for active borrow state &  $\stackrel{\Gamma}{\sqsubseteq} a_{\perp}^{\gamma}$ , that state is actually "backed" by some state which is either "borrowed out" or in some "refinement world."

Note again in the second bullet that within the "refinement worlds" we allow both *borrowed and non-borrowed* state. This is the "trick" that lets the TPCM refinement be so effective for dealing with borrowed state.

For example, suppose we derive the fractional logic  $\operatorname{Frac}(\hat{N}) \Rightarrow \hat{N}$ .

Now, as a user of the fractional logic, we can derive  $a \Rightarrow \operatorname{Frac}(1,a)$ , and then  $\operatorname{Frac}(1,a) \Rightarrow \operatorname{Frac}(1/2,a) * \operatorname{Frac}(1/2,a)$ . Then, we could perform a borrow of one of the halves and obtain  $\&^{\kappa} \operatorname{Frac}(1/2,a)$ , then obtain  $\&^{\kappa} a$ .

We are able to do this, even though we have "only" borrowed half, e.g., we have borrowed( $\kappa$ , Frac(1/2, a)) \* Frac(1/2, a), rather than borrowed( $\kappa$ , Frac(1, a)).

### **Justification (more formally)**

We can define a resource algebra (RA) as follows. Let  $\Gamma$  be the set of locations  $\gamma$  and K the set of lifetimes  $\kappa$ .

- *live* state at each location  $\gamma$  given by *live* :  $\Gamma \rightarrow m$
- reserved state at each location  $\gamma$  and lifetime  $\kappa$ , given by reserved :  $K \to \Gamma \to m$ .
- A set of *borrows* where *borrows*  $\subseteq K \times \Gamma \times M$ .
- An active set of lifetimes, given by active: K.

The live state is additive (that is,  $live(\gamma \to m) \cdot live(\gamma \to p) \triangleq live(\gamma \to p \cdot q)$ . Reserved state is similarly additive.

Finally, borrows are freely duplicable, and add *freely* (i.e., take the union of the set of borrow objects).

We'll describe the invariants on this resource algebra below, but first, I'll explain how all the above concepts are embedded into this resource algebra:

Now we define some functions on this state, defined recursively. First, for any refinement location  $\delta \gg_{\rm rel} \gamma$ , where rel:  $M \times N \to {\rm bool}$ , we let  ${\it Project}_{\delta}: M \to N$  be a "functionalization" of rel. In particular,

$$\forall m. m \in domain(rel) \implies rel(m, Project_{\delta}(m)).$$

where  $domain(rel) \triangleq \{m : \exists n. \ rel(m, n)\}.$ 

Now we define  $Total_{\kappa}(\gamma)$  to be the "total" state of a location  $\gamma$ : that is, all the live and reserved state (for the active livetime  $\kappa$ ) plus all the total state of locations which depend on  $\gamma$ . Thus we use this recursive definition,

$$Total_{\kappa}(\gamma) \triangleq live(\gamma) \cdot \left( \bigcirc_{\kappa' \supseteq \kappa} reserved(\kappa', \gamma) \right) \cdot \left( \bigcirc_{\delta} Project_{\delta} \left( Total_{\kappa}(\delta \gg_{rel} \gamma) \right) \right)$$

Now, this definition is only sensible when  $Total_{\kappa}(\delta \Rightarrow_{\text{rel}} \gamma)$  is in the "domain" of rel.

Thus we will have an invariant, only applying to the active lifetime active,

$$Total_{active}(\delta \gg_{rel} \gamma) \in domain(rel) \tag{1}$$

Next, we define a similar recursive function called View, which will be defined only in terms of the *reserved* state. However, it will be used to explain what the "possible" values of the Total (minus the live state). A view will be a function  $v: M \to bool$ . These views will also be closed *upwards*, i.e.,  $v(a) \Longrightarrow v(a \cdot b)$ .

Again, we define a "projection" function from a view on a refinement TPCM to a base TPCM.

$$ProjectView_{\delta}(v) \triangleq \lambda a. \ \exists x, y. \ v(x) \land y = Project_{\delta}(x) \land (x \in domain(rel)) \land y \leq a.$$

For a given m, we define ViewOver(m) to be the view of all elements that are at least m:

$$ViewOver(m) \triangleq \lambda n. n > m$$

And we can add two views in the natural way:

$$v \otimes w \triangleq \lambda m. \ \exists x, y. \ v(x) \land w(y) \land x \cdot y = m$$

Finally, we're ready to define the  $View_{\kappa}$  function for a given location, again recursively, like the  $Total_{\kappa}$  function:

$$View_{\kappa}(\gamma) \triangleq \left(\bigotimes_{\kappa' \supseteq \kappa} ViewOver(reserved(\kappa', \gamma))\right) \otimes \left(\bigotimes_{\delta} ProjectView_{\delta}\left(View_{\kappa}(\delta \Rightarrow_{\mathsf{rel}} \gamma)\right)\right)$$

Now the  $View_{\kappa}$  denotes the "possible" states that the total unlive state can be in for a given  $\gamma$ , without actually depending on any live state even in the refining locations. The  $View_{\kappa}$  is in some sense used to justify the borrows at  $\kappa$ , so we will have an invariant,

$$\forall \kappa, \gamma, m. \ (\kappa, \gamma, m) \in borrows \implies \forall y. \ View_{\kappa}(\gamma)(y) \implies m \le y$$
 (2)

(We don't need a condition that  $\kappa \supseteq active$ , although we won't be able to make use of the invariant otherwise.)

Now, we define Inv to be both (1) and (2) together, and define an RA with a validity predicate  $\mathcal{V}(x) \triangleq \exists y. \ Inv(x \cdot y)$ . Then we can derive all the rules claimed above, by simply showing that they are frame-preserving within this RA.

First, the borrowing rules: we can check that they preserve (2). Also, all of the borrowing rules will preserve  $Total_{active}$ , so they preserve (1) trivially.

Now, for the frame update rules (Borrow-Ghost-ViewShift and Borrow-Ghost-Refinement-Exchange). First, these don't affect  $View_{\kappa}$  at all, so it is easy to show that they preserve (2).

We can also check that (1) is preserved. There are two key points here. First, the rules about TPCM refinements imply that a frame-preserving update on a  $Total_{\kappa}(\delta \gg_{rel} \gamma)$  implies a frame-preserving update on the projection,  $Project_{\delta}(Total_{\kappa}(\delta \gg_{rel} \gamma))$ .

Second, we have to show why it is allowed to use borrowed state to justify the transitions. Luckily, from (1) we can derive this law,

$$\kappa \sqsupseteq active \implies View_{\kappa}(\gamma) \left( \left( \bigcirc_{\kappa' \supseteq active} reserved(\kappa, \gamma) \right) \cdot \left( \bigcirc_{\delta} Project_{\delta} \left( Total_{active}(\delta \gg_{\mathrm{rel}} \gamma) \right) \right) \right).$$

This large expression inside the  $View_{\kappa}(\gamma)$  is just the part of  $Total_{active}(\gamma)$  except the part which is live at  $\gamma$ . Thus, combined with (2), we can see that any borrow will be covered by the reserved state at  $\gamma$  plus all state in the refined locations.

Just to reiterate the "trick":  $View_{\kappa}(\gamma)$  depends only on reserved state: however we are able to show that the value

$$\left( \bigcirc_{\kappa' \supseteq active} reserved(\kappa', \gamma) \right) \cdot \left( \bigcirc_{\delta} Project_{\delta} \left( Total_{active}(\delta \gg_{\mathrm{rel}} \gamma) \right) \right)$$

is in the active view, even though it includes the *total* state at locations refining  $\gamma$ .

# Fractional logic

Given a TPCM M, we can construct a TPCM  $Frac_M$  with the following laws.

And,

$$\&^{\kappa} \operatorname{Frac}(x,r) \right]^{\alpha \Rightarrow \gamma} \Rightarrow \&^{\kappa} \left[ x \right]^{\gamma}$$

# **Counting logic**

And,

& 
$$\kappa \subseteq \text{SharedRef}(x) \subseteq \text{SharedRef}(x)$$

## **RwLock Logic**

Given a TPCM M, we can construct a TPCM  $RwLock_M$  with the following laws. (Here, we have exc: bool,  $rc \in \mathbb{N}$ , and  $x \in M$ .

These laws are for exclusive locks:

The first two are for acquisition, while the third is for release. Meanwhile, the first one is derived from Ghost-ViewShift, while the last two are derived from Ghost-Refinement-Exchange.

These laws are for shared locks:

All of these are derived from Ghost-ViewShift.

We don't use Ghost-Refinement-Exchange, because in the 'shared' case, our mechanism for getting back M-state is to use the Borrow-Back rule. In particular, we use it to derive this:

$$\&^{\kappa} [\mathsf{SharedGuard}(x)]^{\alpha \Rightarrow \gamma} \Rightarrow \&^{\kappa} [x]^{\gamma}$$

This logic is all we need to verify a reader-writer lock.

## **RwLock Verified Example**

Let's use a struct rwlock: {exc: Pointer, rc: Pointer}. Our implementation will be straightforward:

We would like to prove the following specifications, for some predicate  $Inv(rwlock, \alpha, \gamma, inv)$ . (Here, inv is the user-defined predicate on the ghost state stored by the rwlock.)

AcquireExc:

```
\{Inv(rwlock,\alpha,\gamma,inv)\}
AcquireExc(rwlock)
\{Inv(rwlock,\alpha,\gamma,inv)*[\texttt{ExcGuard}]^{\alpha \Rightarrow \gamma}*\exists x.[x]^{\gamma} \land inv(x)\}
ReleaseExc:
\{Inv(rwlock,\alpha,\gamma,inv)*[\texttt{ExcGuard}]^{\alpha \Rightarrow \gamma}*[x]^{\gamma} \land inv(x)\}
ReleaseExc(rwlock)
\{Inv(rwlock,\alpha,\gamma,inv)\}
AcquireShared:
\{Inv(rwlock,\alpha,\gamma,inv)\}
AcquireShared(rwlock)
\{Inv(rwlock,\alpha,\gamma,inv)*\exists x.[SharedGuard(x)]^{\alpha \Rightarrow \gamma} \land inv(x)\}
ReleaseShared:
\{Inv(rwlock,\alpha,\gamma,inv)*[SharedGuard(x)]^{\alpha \Rightarrow \gamma} \land inv(x)\}
ReleaseShared(rwlock)
\{Inv(rwlock,\alpha,\gamma,inv)\}
```

To prove these specifications, we will first define *Inv*:

```
Now, we can work out how to verify the above programs. AcquireExc will follow from the below:
```

```
\{Inv(rwlock, \alpha, \gamma, inv)\}
       CAS(rwlock.exc, 0, 1)
       \left\{v.\ Inv(rwlock, \alpha, \gamma, inv) * \left(v \implies \left[\mathsf{ExcPending}\right]^{\alpha \Rightarrow \gamma}\right)\right\}
       \left\{Inv(rwlock, \alpha, \gamma, inv) * \begin{bmatrix} ExcPending \end{bmatrix}^{\alpha \Rightarrow \gamma} \right\}
       \left\{v.\ Inv(rwlock,\alpha,\gamma,inv)*\left(v\neq 0\right.\Longrightarrow \left[\mathsf{ExcPending}\right]^{\alpha\Rightarrow\gamma}\right)*\left(v=0\right.\Longrightarrow \left[\mathsf{ExcGuard}\right]^{\alpha\Rightarrow\gamma}*\exists x.\left[x\right]^{\gamma}\wedge inv(x)\right)\right\}
AcquireShared will follow from:
          \{Inv(rwlock, \alpha, \gamma, inv)\}
          AtomicAdd(rwlock.rc, 1)
                                                                                                                                                                                          (3)
          \{Inv(rwlock, \alpha, \gamma, inv) * SharedPending\}
          \{Inv(rwlock, \alpha, \gamma, inv) * SharedPending\}
          (!rwlock.exc)
          \left\{v.\ Inv(rwlock,\alpha,\gamma,inv)*(v\neq 0\implies \mathsf{SharedPending})*\left(v=0\implies \exists x.\ \mathsf{SharedGuard}(x)\right) \land inv(x)\right\}
          \{Inv(rwlock, \alpha, \gamma, inv) * SharedPending\}
          AtomicAdd(rwlock.rc, -1)
          \{Inv(rwlock, \alpha, \gamma, inv)\}
ReleaseExc will follow from:
                                                  \left\{ Inv(rwlock, \alpha, \gamma, inv) * \left[ \text{ExcGuard} \right]^{\alpha \Rightarrow \gamma} * \left[ x \right]^{\gamma} \wedge inv(x) \right\}
                                                  rwlock.exc \leftarrow 0
                                                  \{Inv(rwlock, \alpha, \gamma, inv)\}
And finally, ReleaseShared will follow from:
                                                   \left\{ Inv(rwlock, \alpha, \gamma, inv) * \left[ SharedGuard(x) \right] \wedge inv(x) \right\}
                                                   AtomicAdd(rwlock.rc. -1)
                                                   \{Inv(rwlock, \alpha, \gamma, inv)\}
Let's work out (3) explicitly:
                          \{Inv(rwlock, \alpha, \gamma, inv)\}
                         AtomicAdd(rwlock.rc, 1)
                          \left\{\exists exc, rc, x. \left[ \overrightarrow{\mathsf{Central}}(exc, rc, x) \right] \right\} * (rwlock.exc \hookrightarrow exc) * (rwlock.rc \hookrightarrow rc + 1) \right\}
\left\{\exists exc, rc, x. \left[\mathsf{Central}(exc, rc+1, x)\right] \right\} * \mathsf{SharedPending} * (rwlock.exc \hookrightarrow exc) * (rwlock.rc \hookrightarrow rc+1) \right\}
(rc' = rc + 1) \quad \left\{ \exists exc, rc', x. \middle[ \mathsf{Central}(exc, rc', x) \middle] \quad * \mathsf{SharedPending} * (rwlock.exc \hookrightarrow exc) * (rwlock.rc \hookrightarrow rc') \right\}
      (close Inv) \{Inv(rwlock, \alpha, \gamma, inv) * SharedPending\}
```

#### A hash-table logic

We'll want to support the following specs for a linear-probing hash table.

$$\left\{ HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow v_0 \end{bmatrix} \right\} \text{ Query}(ht,k) \left\{ v. HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow v_0 \end{bmatrix} \land v = v_0 \right\}$$

$$\left\{ HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow v_0 \end{bmatrix} \right\} \text{ Update}(ht,k,v) \left\{ HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow Some v \end{bmatrix} \right\}$$

$$\left\{ HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow v_0 \end{bmatrix} \right\} \text{ Remove}(ht,k) \left\{ HTInv(ht,\gamma) * \begin{bmatrix} hash \\ k \hookrightarrow None \end{bmatrix} \right\}$$

We assume a function  $hash: Keys \to \mathbb{N}$ . We'll let  $ht: \{rwlocks: rwlock\ list, table: ((Key, Value)\ option)\ list\}$ .

```
Query(ht, k) \triangleq QueryIter(ht, k, hash(k))
    QueryIter(ht, k, i) \triangleq AcquireShared(ht.rwlocks[i]);
                                  let r = (match ! ht.table[i] with
                                     | \text{None} \Rightarrow \text{None}
                                     | Some (k_0, v_0) \Rightarrow \mathbf{if} \ k = k_0 \ \mathbf{then} \ Some(v_0) \ \mathbf{else} \ \mathrm{QueryIter}(ht, k, i+1)
                                  end) in
                                  ReleaseShared(ht.rwlocks[i]);
                                  r
       Update(ht, k, v) \triangleq UpdateIter(ht, k, v, hash(k))
UpdateIter(ht, k, v, i) \triangleq AcquireExc(ht.rwlocks[i]);
                                  let r = (match ! ht.table[i] with
                                     | None \Rightarrow ht.table[i] \leftarrow (k, v)
                                     | Some (k_0, v_0) \Rightarrow \mathbf{if} \ k = k_0 \ \mathbf{then} \ ht.table[i] \leftarrow (k, v) \ \mathbf{else} \ \mathrm{UpdateIter}(ht, k, v, i+1)
                                  end) in
                                  ReleaseExc(ht.rwlocks[i]);
                                  r
```

We'll define a TPCM resource for our hash table (that is, a "normal" TPCM, not a refinement). It will have two kinds of elements,  $k \hookrightarrow v$  (where v : V alue option) and  $i \hookrightarrow c$ , where c : (Key, V alue) option.

Allowable operations will be encoded into this logic. For example, we will have all the following: *Update*.

$$\begin{aligned} hash(k) &\leq u \ \land \ \left( \forall i.\ hash(k) \leq i < u \implies i \in entries \land \exists k_1, v_1.\ entries[i] = \operatorname{Some}(k_1, v_1) \land k_1 \neq k \right) \\ &\land \ \left( entries[u] = \operatorname{None} \lor \exists v_1.\ entries[u] = \operatorname{Some}(k, v) \right) \\ &\implies entries \cdot (k \overset{\text{hash}}{\hookrightarrow} v_0) \ \mapsto \ entries[u \leftarrow \operatorname{Some}(k, v)] \cdot (k \overset{\text{hash}}{\hookrightarrow} v) \end{aligned}$$

Query (found).

The found case is easy: a single entry (k, v) proves that the hash table maps k to v.

$$\left((i \overset{\text{entry}}{\hookrightarrow} (k, v_0)) \cdot (k \overset{\text{hash}}{\hookrightarrow} v)\right) \neq \bot \implies v = \operatorname{Some}(v_0)$$

The not-found case requires another quantifed predicate:

We'll define HTInv as follows:

$$HTInv(ht, \gamma) \triangleq \exists \alpha. \quad *Inv(ht.rwlocks[i], \alpha, \gamma \times Heap, htinv(i, ht.entries[i]))$$

$$htinv(i, \ell) \triangleq \lambda x. \exists c. \ x = (i \stackrel{\text{entry}}{\hookrightarrow} c, \ell \hookrightarrow c)$$

In other words, the RwLocks will be arranged so that we can use the lock at index i to obtain some ghost state ( $i \stackrel{\text{entry}}{\hookrightarrow} c$ ,  $ht.entries[i] \hookrightarrow c$ ).

Now, we prove QueryIter. (To avoid clutter, we silently skolemize existential variables as they are introduced.)

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\begin{cases} \operatorname{active}(\kappa) * \&^{\kappa}[ht.entries] * * [hash \\ \hookrightarrow v_0] \land KeyNotInRange(ht.entries, k, i) \end{cases} \\ \operatorname{AcquireShared}(ht.rwlocks[i]); \\ \left\{ \operatorname{active}(\kappa) * \&^{\kappa}[ht.entries] * [hash \\ \hookrightarrow v_0] \land KeyNotInRange(ht.entries, k, i) * SharedGuard (i \\ \hookrightarrow c, ht.entries[i] \hookrightarrow c) \end{cases} \right\} \\ \left\{ \operatorname{active}(\kappa) * \&^{\kappa}[ht.entries] * [hash \\ \hookrightarrow v_0] \land KeyNotInRange(ht.entries, k, i) * SharedGuard (i \\ \hookrightarrow c, ht.entries[i] \hookrightarrow c) \end{cases} \right\} \\ \left\{ \operatorname{active}(\kappa_0) * \operatorname{borrowed} \left( \kappa_0, \operatorname{SharedGuard} \left( (i \\ \hookrightarrow c, ht.entries[i] \hookrightarrow c) \right) \right) * \&^{\kappa_0} \operatorname{SharedGuard} \left( (i \\ \hookrightarrow c, ht.entries[i] \hookrightarrow c) \right) \right\} \\ \operatorname{let} r = (\operatorname{match} !ht.table[i] \operatorname{with} \\ |\operatorname{None} \Rightarrow \operatorname{None} \\ |\operatorname{Some}(k_0, v_0) \Rightarrow \operatorname{if} k = k_0 \operatorname{then} Some(v_0) \operatorname{else} \operatorname{QueryIter}(ht, k, i + 1) \\ \operatorname{end}) \operatorname{in} \\ \operatorname{ReleaseShared}(ht.rwlocks[i]); \\ r \\ \left\{ \operatorname{active}(\kappa) * (k \\ \hookrightarrow v_0) \land v = v_0 \right\} \end{aligned}
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