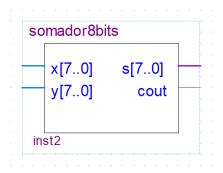
Somador de 8 bits

Projeto: somador8bits

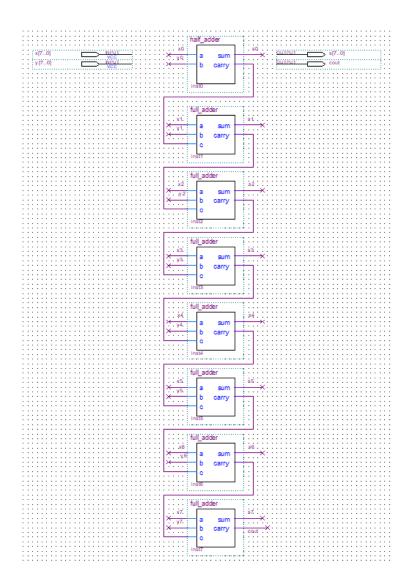
Descrição: Implementação de um somador de 8 bits com uso de um

meio somador e 7 somadores completos

Símbolo:



Circuito:



Formas de onda:

		Name	Value at 0 ps	.0 ns	60.0	ns	80.0	ns	100.) ns	120.	0 ns	140.	0 ns	160	0 ns	18	0.0 ns	21	00.0 n		220 ₋ 0 n	s	240.0 n	s 26	i0.0 ns	28	0.0 ns	300).0 ns	320	0. <mark>0 ns</mark>	340	0 ns	36	0.0 ns	38	90,0 ns	40	0.0 ns	421	1,0 ns	440.	0 ns	460	0.0 ns	481	0.0 ns	50	0.0 ns	52	0.0 ns
5	>	×	UO	X4)	5	6 X	7 X	8	9	10 X	11	12	13	14	15	16	17	18	X 19	X 2	X	1 X 2	2 X i	23 🔾 2	4 25	26	X 27	X 28	29	30	31	32	33	34	35	X 36	37	38	39	X 40	X 41	42	(43	44	45	X 46	47	X 48	X 49	50	X 51	(52)
-	>	у	U O	X 2		30	=X	40		50		61			0		0	$X \subseteq$	90	X	100	\supset	110	=X $=$	120	X	130	$X \subseteq$	140	X_1	50	X 1	60		70	$X \subseteq$	180	Х	190	X	200	X 2	10	<u>2</u>	20	Χ.	230	X	240	χ_:	250	X
94	>	s	U O	(24)	25	36 X	37 X	48	49	60 X	61	72	73	84	85	96	97	108	X 109) X 12	0 X 1	21 (1	32 🛚 1	33 🛚 14	14 (14	156	157	168	169	180	181	192	193	204	205	216	21	7 (228	229	240	241	252	253	8	9	20	21	32	(33	X 44	X 45	X 56
out		cout	80																																										Т			т				