

Elahe Jalalpour

Curriculum Vitae

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Research Interests

- Computer Networks
- Software Defined Networking
- Internet of Things
- Computer and System Architectures
- FPGA

Education

2012–2016 **B.Sc. Computer Engineering**, Amirkabir University of Technology, Tehran.
GPA – 17.31 out of 20

2008–2012 **High School, Diploma of Math & physics**, Manzoumeh Kherad Institute, Tehran.
GPA – 19.80 out of 20

Patents

Title *Intelligent Control System of Steering Wheel*
Issued 03 / 2011
IR Patent 69363

Experience

2015 **Teacher Assistant**, COMPUTER ARCHITECTURE, Amirkabir University of technology.

Awards

- 2012 Ranked in top 1.6% of Nation-wide University Entrance Exam among all Iranian Students of Math. & Physics
- 2012 Ranked in top 0.1% of Nation-wide University Entrance Exam among all Iranian Students of Foreign Languages (English)
- 2013–2015 Ranked in top 4 Computer Hardware Engineering Students

Computer skills

- **Programing & Hardware Design Languages.**
Java (Proficient), C/C++, Python (familiar), Verilog, VHDL, 8086 Assembly

- **Typesetting.**
L^AT_EX, Microsoft Word, LibreOffice, Pages, Vim
- **Hardware Simulators.**
Xilinx ISE Design Suite, P-Spice, H-Spice, Proteus
- **Web Development.**
HTML
- **Other.**
Netbeans, Eclipse, Wireshark
- **Operating Systems.**
Windows, OS X, Ubuntu, Fedora

Publications

- 2015 **E.Jalalpour**, *Consistent Update in SDN*, Tehran.
technical report for Research Method and Technical Report Writings course (In Persian)
- 2015 **E.Jalalpour**, *Real Time Cache Performance Analyzing for Multi-core Parallel Programs*, Tehran.
project report for Computer Architecture course (In Persian)

Research Experiences

- 2015 **Consistent update in SDN**, Tehran.
This research was done for Research Method and Technical Report Writings course (Technical Report and Poster)

Languages

- Persian **Mothertongue**
(Farsi)
- English **Professional working proficiency**
- French **Elementary proficiency**

Projects

- 2012 **Phone Book**, C.
Implementation of Phone Book with file
- 2014 **HTTP Proxy**, JAVA.
Implementation of a HTTP and HTTPS proxy server
- 2013 **JUMONG (Java Ultimate Maze Obstacle Neutralizer Game)**, JAVA.
Implementation of a graphical game with multiplayer support
- 2014 **FAT Parser**, C.
Implementing FAT-16 and FAT-32 parser with delete, list and retrieve support
- 2014 **Disassembling DOS Executable File.**
- 2015 **FPGA Co-Design**, C & VHDL.
Implementing hardware modules in VHDL and using them for having a cryptographic algorithm in a software environment

- 2015 **Transistor Level Ripple Adder**, HSPICE.
Designing and implementing transistor level ripple adder in three families: Static CMOS, Dynamic CMOS and DCVSL
- 2013 **Simon Game**, *Proteus*.
Design and implementation of Simon game's logic circuit

References

Professor Bahador Bakhshi, *Assistant Professor*.

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Professor Hamid Reza Zarandi, *Assistant Professor*.

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