Today, Nov. 4th.

Chapter 5 Combinational Logic

1.
    Section 4.3 ANALYSIS PROCEDURE, page 125
    Section 4.4 DESIGN PROCEDURE, page 126
2. Code Conversion Example on page 131~133
3. Half Adder, Full Adder, Look-ahead-Carry Adder. Understand why **Carry Look Ahead Adder** is faster than **Ripple Carry Adder**
4. **BCD Adder** on page 144~146
5. Binary multiplier on page 146~148
6. MAGNITUDE COMPARATOR page 148~150
7. Encoder   (page 150~ 153) and Decoder (page 155~158)
8. Multiplexer (page 160) and De-multiplexer
    Demultiplexer: it distributes input data on different channel, depending on channel selection, it can be thought as an inverse direction of multiplexer.

Home works：

In the 5th Edition text book, go to **page 246~254**, please finish the following questions:

5.3
5.6
5.8
5.9
5.10