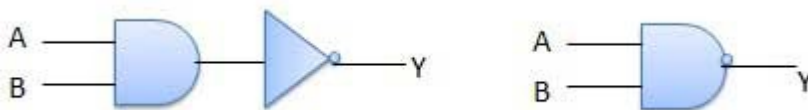


NAND Gate

A NOT-AND operation is known as NAND operation. It has n input ($n \geq 2$) and one output.

$$\begin{aligned} Y &= A \text{ NOT AND } B \text{ NOT AND } C \dots\dots N \\ Y &= A \text{ NAND } B \text{ NAND } C \dots\dots N \end{aligned}$$

Logic diagram



Truth Table

Inputs		Output
A	B	\overline{AB}
0	0	1
0	1	1
1	0	1
1	1	0