

## **Xilinx Imapct**

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One important tool that enables flashing an FPGA or its attached Flash memory, Xilinx Impact which we will show briefly how to use it for downloading bitstream into an FPGA.

Open Xilinx Impact.

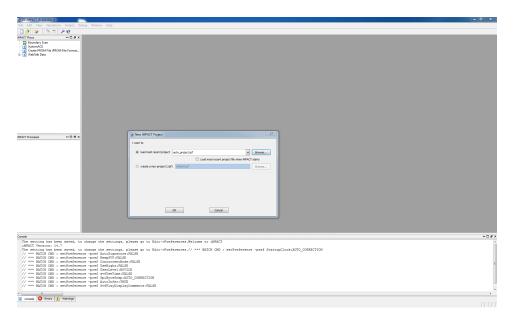


Figure 1: Start Impact

## Click Cancel. Click Edit>>Launch Wizard

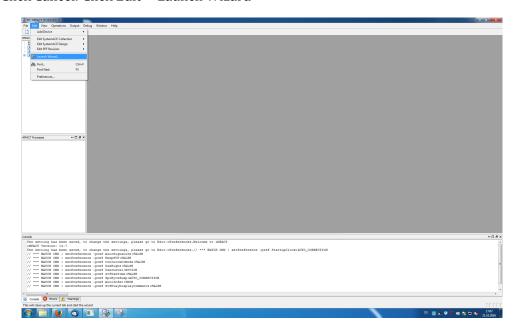


Figure 2: Start Download Wizard

Make sure that your FPGA is connected. Then Click Ok

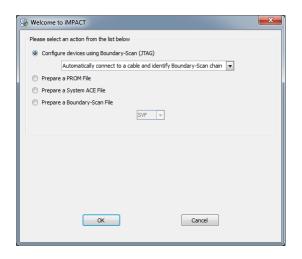


Figure 3: Connect Over JTAG

The following message will appear.

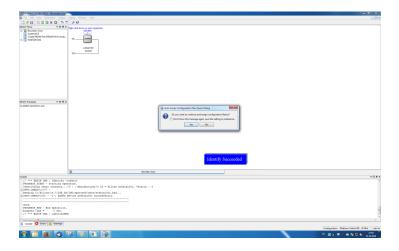


Figure 4: Prompt for Assigning Config File

Click No for the moment. The following Prompt will appear. Click Cancel.

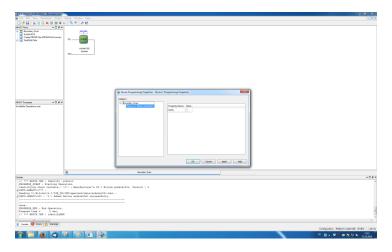


Figure 5: Boundary Scan Prompt

You can see all scanned devices which in our case are the FPGA chip and attached Flash

## memory.

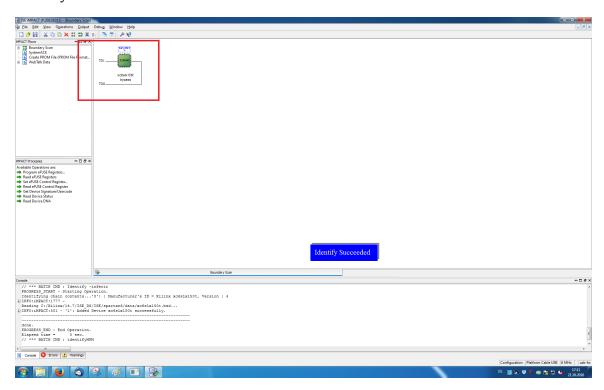


Figure 6: All Scaned Devices

You can assign the file that you want to download into the FPGA by right click then choose "Assign New Configuration File"  $^{\prime\prime}$ 

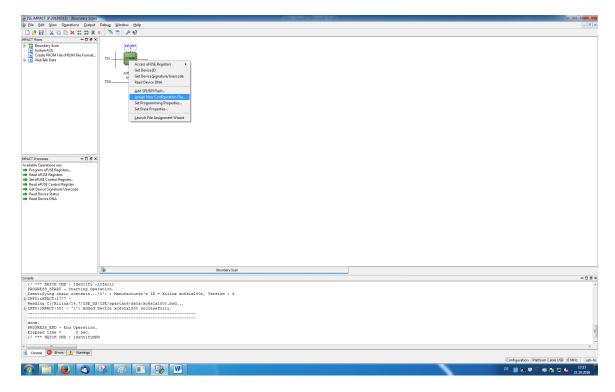


Figure 7: Assign Configuration File

Browse to the target file. When you add it, click on the FPGA icon to see available options

in the left pane.

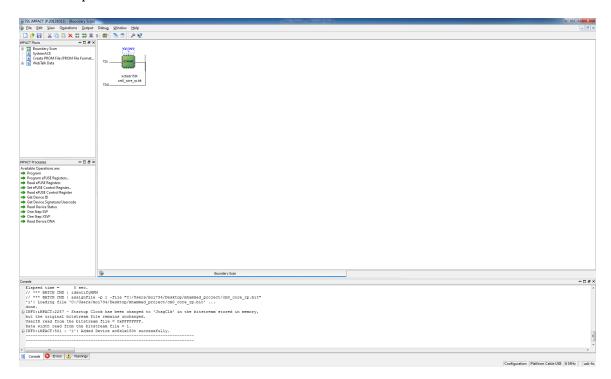


Figure 8: After Assigning Configuration File

Double click on program to download the bit file into the FPGA. Click ok or Apply

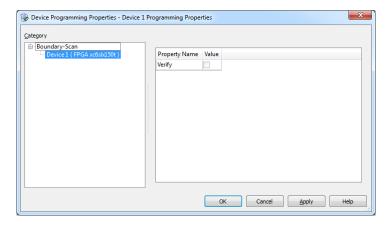


Figure 9: Apply Programming

The FPGA shall be programmed successfully.