

- Interrupts, Revisited
  - Power management
  - Multiple interrupts
  - Data transfer between ISR, callback fn and the main code
  - Important issues in interrupt handling.  
Example on the actual board
  - Extra topics
- Timers, Introduction
  - Clock signals in the MC
    - Oscillators
  - Timers in Embedded systems
    - What is a timer
    - Timers in the STM32L0 MC, Mbed and its simulator.

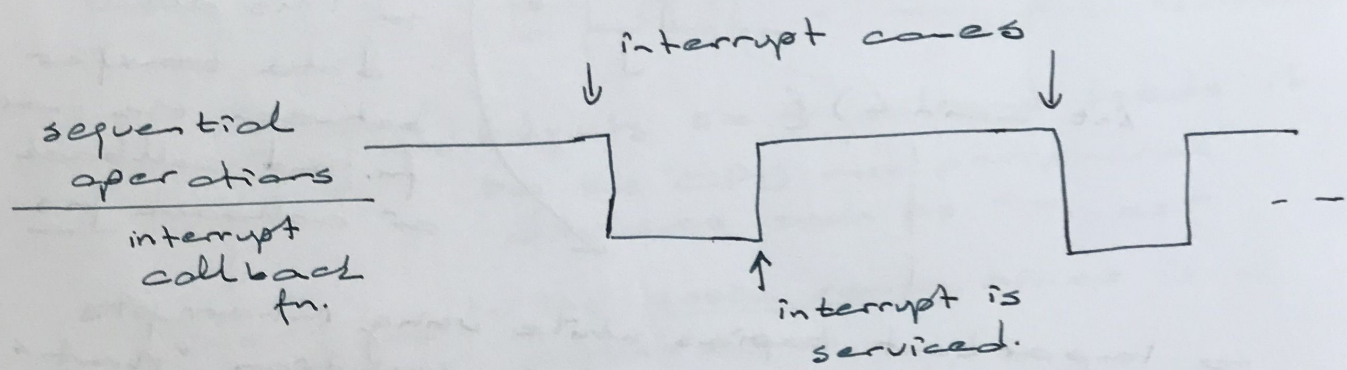


# Interrupts

- sleep mode
- sequential op

Asynchronous event

interrupt  
↓  
serve the interrupt.



What happens

- i-) more than one interrupt comes?
- ii-) successive interrupts come?

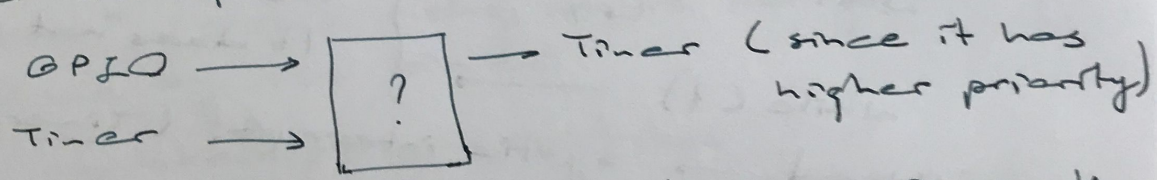
## → Interrupt priority concept.

- we have several interrupt sources
- source 1 (GPIO)
- source 2 (timer)
- source 3 (ADC)

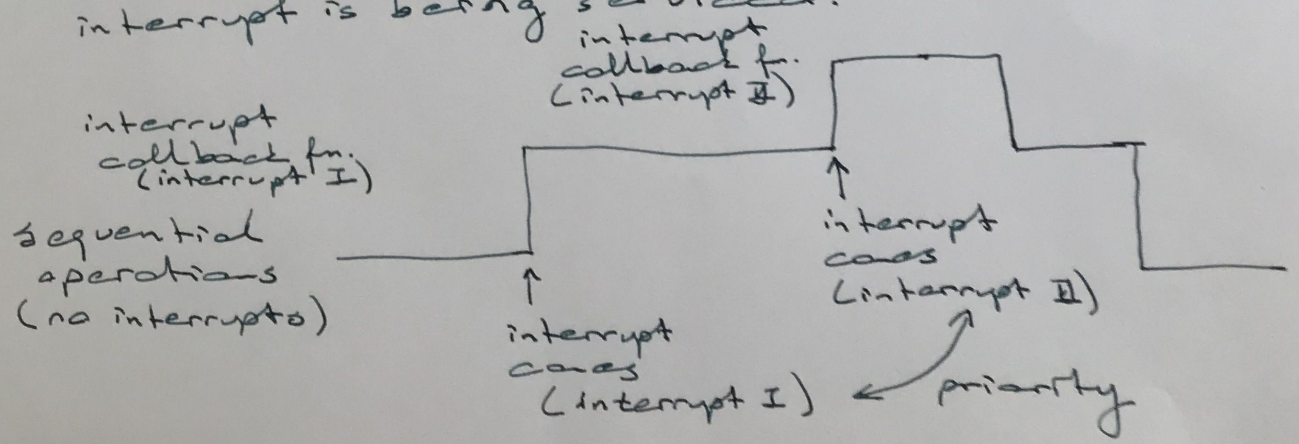
→ prioritized beforehand.

→ we can change priority by software.

i-) Two interrupts come at the same time



ii-) A new interrupt comes while there is another interrupt is being serviced.





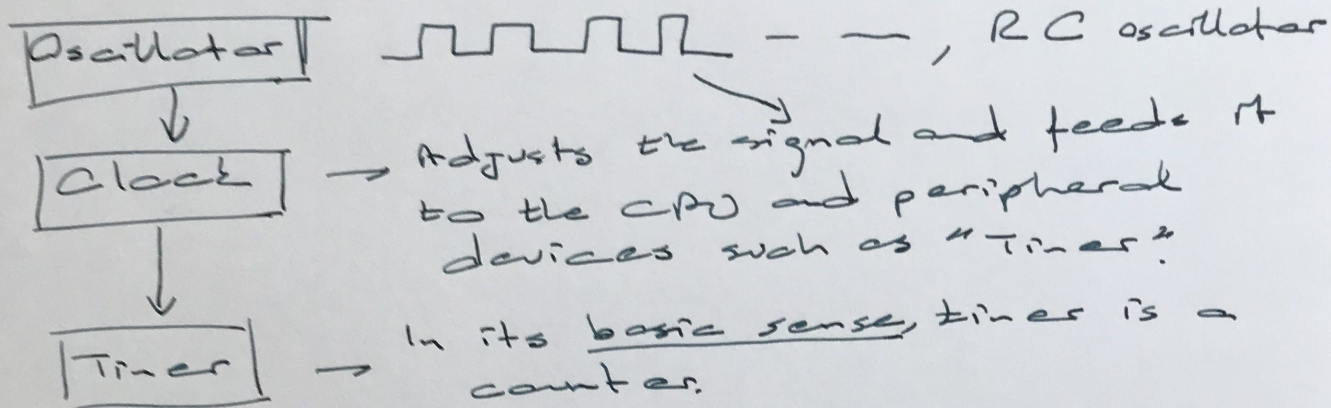




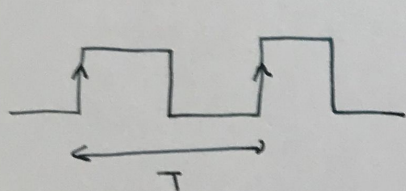
## Introduction to Timers

- Time based operations. Or when periodic operations are executed.

(inside the MC)



- How do I perform time based operations in the MC using osc, clock, timer?



$T \rightarrow$  period

$f \rightarrow$  frequency

Example 1 kHz. frequency  
 $10^{-3}$  sec. period

- Every millisecond, rising edge of the clock signal comes
- If I want to toggle the LED every sec.  
 "count" 1000 rising clock edges.  
 ⇒ Done by the timer.  
 ↳ Interrupt generation.  
 ↳ callback fn. can be executed.