- clack signals
- what is a clock signal? - ascillators

- Tiness

- Bosics

- Mbed bosed tiring sperations

-, ticker

- tireart

- red time clack

- systice

- wotchdag ther - Mbed Examples

- clack in embedded systems. -synder anous! - ALALALA - At every rising edge, we perfor on apardia. 1 Lalack, square wave OV 33.3V "clock ticks, 0-31
pulses 1-10

clack =) square wave (pariodic) 1 MHz. frequency => T=10 sec.

W12,2 Generating Clack Signal - Oscillator "electronic circuit 11 1-1-- Resistive Capacitive (RC) ascillator > Implemented within the microsatroller. - is not precise, ~ 1,10% - cheap - available for garerd use. - Crystal -III- - clock pulses, digital watch -precise, nimm es possible - not available on the MC - add extra arouttry to the board. => Mbed "simplifies" ascillator usage. Timer is a simple " conter" clack - Timer - generated interrupts
signal - Timer - generated interrupts
capture / capture / capture aparations

w12,3 TITIST - court

O 1 2 3 --- 1

reset

count + clock period => the actual time. 3 periods 3×10 sec. > Mbed as simplifies these adaptars. - Interrupt generation. - generate periodically.

- axecute canado periodically. - Capture aparation.

- "capture" time of a specific aparation. - "capace" the captured time, generate
interrupt afterwards. - Capare aparation. -> There are "several thers" in the SIMBLLO - bose timers

- 2321, 21-1

Is was independably = overflow - reset. - System times (systick) - warzs by itself - RTOS, - 1 sec, water up the CPD - Red-Ti-e clack LRTC) - set up - get the actual time 4 - Watchdag timer