# Personal information

Name / Surname
Telephone
Personal Email
Date of birth
Github profile

### Paolo Scaramuzza

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paoscr@gmail.com

16 December, 1992

https://github.com/electricant



# Working Experience

Oct. 2019 - present

Hardware RF application engineer at u-blox Italia S.p.A.

My role involves designing and releasing demo boards for cellular modules, writing documentation, reviewing and improving customers' designs (both schematic and PCB) and keeping in touch with business partners from a technical point of view.

I help customers by testing their products and by suggesting improvements in various RF-related topics such as: PCB layout, RF matching and filter design, antenna design or selection, improving signal integrity and reducing spurious emissions.

Oct. 2017 - May 2018

## Internship at Infineon Technologies Villach, Austria

The internship was focused on the design and measurement of integrated 28 GHz power amplifiers for 5G cellular networks.

## Education

Oct. 2016 - Oct. 2019

**Ph.D.** in Information Engineering, University of Padova

Topic: Fully-integrated Power Amplifiers for 5G Communications (supervisor: Andrea Neviani, Andrea Bevilacqua)

Nov. 2018 During the Ph.D., was awarded

During the Ph.D., was awarded at the **Huawei Italy University Challenge** in Pavia, aimed at inspiring research in Silicon Integrated Circuits for Analog, RF, mmWave and Optical transceivers

July 2017

Qualification test (esame di stato) for being a professional Engineer

2014 - 2016

Master's degree in Electronics Engineering (110/110 cum laude) University of Padova, thesis title: "Project of a Class J Power Amplifier in Integrated Bipolar Technology"

June 2015

While studying for his M.Sc, received the Roberto Rocca Education Program scolarship for academic merit.

2011 - 2014

#### Bachelor's degree in Information Engineering (110/110)

University of Padova, thesis title: "A Study on the Efficiency of Integrated LC Oscillators for Ultra Wideband Impulse Radio Transmission"

2006 - 2011

Liceo Scientifico "Duca degli Abruzzi" in Gorizia

### **Publications**

TCAS-I 2018

P. Scaramuzza, C. Rubino, M. Caruso, M. Tiebout, A. Bevilacqua, and A. Neviani

Class-J SiGe X-Band Power Amplifier Using a Ladder Filter-Based AM-PM Distortion Reduction Technique

ESSCIRC 2017

P. Scaramuzza, C. Rubino, M. Tiebout, M. Caruso, M. Ortner,

A. Neviani, and A. Bevilacqua

Class-AB and Class-J 22dBm SiGe HBT PAs for X-Band Radar Systems

### Personal skills

**Electronics** 

Interested in electronics since childhood, I realized and I'm still working on many hobby projects.

Practical experience in designing and fabricating circuits and PCBs.

Analog/RF IC design & layout.

Knowledge on VHDL hardware description language.

Altium, Cadence Allegro, Cadence Virtuoso, ADS, KiCAD, LTSPICE.

Electromagnetic simulations with HFSS, ADS Momentum and Sonnet.

Bench testing | Analog/RF/mixed-signal circuits measurement and characterization.

Radiated & conducted RF characterization of cellular devices according to ETSI/3GPP/FCC standards.

Programming languages

C, C++, Perl, Python, Java, MATLAB, lisp and other functional programming languages.

Other

CAD

Web programming (HTML, CSS, PHP) & Database knowledge (SQL). Competent in networking and sysadmin tasks with Linux and BSD operating systems.

Document preparation with LATEX and Microsoft Office.

 $Self-assessment \\ European \ level^{(*)}$ 

English

German

Slovene

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C1 Proficient		C1 Proficient		C1 Proficient		C1 Proficient		C1 Proficient	
	user		user		user		user		user
A2	Basic	A2	Basic	A2	Basic	A2	Basic	A2	Basic
	user		user		user		user		user
A1	Basic	A1	Basic	A1	Basic	A1	Basic	A1	Basic
	user		user		user		user		user
(*) ~				7 6 7	D (	ODD)			

<sup>(\*)</sup> Common European Framework of Reference (CEF) level

 $March\ 2011$ 

Certificate in Advanced English (CAE)

#### Other Activities

Sept. 2015 - Sept. 2017

Member in the electronic subsystem design team for the MORPHEUS project, University of Padova. The aim of the project was the design of a rover for the European Rover Challenge, an international robotics competition with tasks analogous to those performed by rovers on the surface of Mars and the Moon.