

Europass Curriculum Vitae



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Personal information

Surname / First name

Address

Scaramuzza Paolo

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Personal Email

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Nationality Italian

Date of birth

16 December, 1992

Education and academic activities

2006-2011 2011-2014 Liceo Scientifico "Duca degli Abruzzi" in Gorizia

Bachelor's degree

Information Engineering, University of Padova

Thesis: A Study on the Efficiency of Integrated LC Oscillators for

Ultra Wideband Impulse Radio Transmission

2014-2016

Master's degree

Electronics Engineering, University of Padova

Thesis: Project of a Class J Power Amplifier in Integrated Bipolar

Technology

June 2015

Roberto Rocca scholarship winner

October 2016 - present

Ph.D.

Information Engineering, University of Padova

Topic: integrated circuits for radio-frequency applications

Publications

ESSCIRC 2017

P. Scaramuzza, C. Rubino, M. Tiebout, M. Caruso, M. Ortner, A.

Neviani, and A. Bevilacqua

Class-AB and Class-J 22dBm SiGe HBT PAs for X-Band Radar Sys-

tems

TCAS-I 2018

P. Scaramuzza, C. Rubino, M. Caruso, M. Tiebout, A. Bevilacqua,

and A. Neviani

Class-J SiGe X-Band Power Amplifier Using a Ladder Filter-Based

AM-PM Distortion Reduction Technique

Personal skills

Mother tongue Self-assessment European level^(*)

English

German

Italian

Understanding		Speaking		Writing
Listening	Reading	Spoken interaction	Spoken production	
C1 Proficient	C1 Proficient	C1 Proficient	C1 Proficient	C1 Proficient
user	user	user	user	user
B1 Independer user	t B1 Independer user	nt A2 Basic user	A2 Basic user	A2 Basic user

^(*) Common European Framework of Reference (CEF) level

Institut für Interkulturelle Komunikation (Jena, Germany)

Vacation studies abroad:

Euroinstitute(Insbruck, Austria)

ActiLingua Academy (Vienna, Austria)

Certificate in Advanced English (CAE)

July 2008

July 2009

August 2010

March 2011

Computer skills

Competent with several programming languages such as: C/C++, D, Python, Java, Javascript, HTML, CSS, PHP, MATLAB and functional programming languages.

Database knowledge (SQL), competent in networking and sysadmin tasks with Linux and BSD operating systems.

Electronics skills

Interested in electronics since childhood, with practical experience in designing and fabricating circuits and PCBs.

Knowledge on VHDL hardware description language.

Github profile

https://github.com/electricant

Working Experience

October 2017 - May 2018

Internship at Infineon Technologies Villach, Austria

The internship was focused on design and measurement of integrated power amplifiers for 5G cellular networks operating at 28GHz.

Other Activities

February 2014-present

Member of the nonprofit organization *Faber Libertatis* in Padova. The organization recycles and repairs obsolete computers using free and open source software. The computers are then given away for charity.

October - December 2015

Teacher of beginner computer classes for elderly people at the Public Library in Saonara (PD). The course was organized thanks to APS Faber Libertatis.

Sept. 2015 - Sept. 2017

MORPHEUS project, University of Padova. The aim of the project was the design of a rover for the European Rover Challenge, an international robotics competition in which rovers constructing teams compete in the tasks analogous to those performed by rovers on the surface of Mars and the Moon.

March 2017 - June 2017

Academic tutoring at the University of Padova in Analog Integrated Circuits Design (A. Bevilacqua)

October 2018 - February 2019

Academic tutoring at the University of Padova in Integrated Circuits for Signal Processing (A. Gerosa)