

Personal information

Name / Surname

Address

Telephone

Personal Email

Date of birth

Github profile

Scaramuzza Paolo

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34170 Gorizia (GO), Italy

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paoscr@gmail.com

16 December, 1992

<https://github.com/electricant>



Education

July 2017

Qualification test (*esame di stato*) for being a professional Engineer

Oct. 2016 - Oct. 2019

Ph.D. in Information Engineering, University of Padova

Topic: *Fully-integrated Power Amplifiers for 5G Communications*
(supervisor: Andrea Neviani, Andrea Bevilacqua)

Nov. 2018

During the Ph.D., was awarded at the **Huawei Italy University Challenge** in Pavia, aimed at inspiring research in Silicon Integrated Circuits for Analog, RF, mmWave and Optical transceivers

2014 - 2016

Master's degree in Electronics Engineering (110/110 cum laude)

University of Padova, thesis title: "*Project of a Class J Power Amplifier in Integrated Bipolar Technology*"

June 2015

While studying for his M.Sc, received the **Roberto Rocca Education Program scholarship** for academic merit.

2011 - 2014

Bachelor's degree in Information Engineering (110/110)

University of Padova, thesis title: "*A Study on the Efficiency of Integrated LC Oscillators for Ultra Wideband Impulse Radio Transmission*"

2006 - 2011

Liceo Scientifico "Duca degli Abruzzi" in Gorizia

Publications

TCAS-I 2018

P. Scaramuzza, C. Rubino, M. Caruso, M. Tiebout, A. Bevilacqua, and A. Neviani

Class-J SiGe X-Band Power Amplifier Using a Ladder Filter-Based AM-PM Distortion Reduction Technique

ESSCIRC 2017

P. Scaramuzza, C. Rubino, M. Tiebout, M. Caruso, M. Ortner, A. Neviani, and A. Bevilacqua

Class-AB and Class-J 22dBm SiGe HBT PAs for X-Band Radar Systems

Working Experience

Oct. 2019 - present

Hardware RF application engineer for cellular products at **u-blox Italia S.p.A.**

Oct. 2017 - May 2018

Internship at Infineon Technologies Villach, Austria

The internship was focused on the design and measurement of integrated 28 GHz power amplifiers for 5G cellular networks.

Other Activities

Oct. 2018 - Feb. 2019

Academic tutoring at the University of Padova in Integrated Circuits for Signal Processing (A. Gerosa)

Mar. 2017 - June 2017

Academic tutoring at the University of Padova in Analog Integrated Circuits Design (A. Bevilacqua)

Sept. 2015 - Sept. 2017

Member in the electronic subsystem design team for the MORPHEUS project, University of Padova. The aim of the project was the design of a rover for the European Rover Challenge, an international robotics competition with tasks analogous to those performed by rovers on the surface of Mars and the Moon.

2014 - 2018

Member of the nonprofit organization *Faber Libertatis* in Padova. The organization recycles and repairs obsolete computers using free and open source software. The computers are then given away for charity.

Oct. - Dec. 2015

While volunteering at *Faber Libertatis* he taught beginner computer classes for elderly people at the Public Library in Saonara (PD).

Personal skills

Electronics

Interested in electronics since childhood.

Practical experience in designing and fabricating circuits and PCBs.

Analog RF IC design & layout.

Knowledge on VHDL hardware description language.

CAD

Altium, Cadence Allegro, Cadence Virtuoso, ADS, KiCAD, LTSPICE.

Electromagnetic simulations with HFSS, ADS Momentum and Sonnet.

Bench testing

RF circuits measurement and characterization.

Radiated & conducted characterization of cellular devices according to ETSI/3GPP/FCC standards.

Programming languages

C, C++, Perl, Python, Java, MATLAB, lisp and other functional programming languages.

Other

Web programming (HTML, CSS, PHP) & Database knowledge (SQL).

Competent in networking and sysadmin tasks with Linux and BSD operating systems.

Document preparation with L^AT_EX and Microsoft Office.

Self-assessment European level^(*)

English

German

Understanding		Speaking		Writing
Listening	Reading	Spoken interaction	Spoken production	
C1 Proficient user	C1 Proficient user	C1 Proficient user	C1 Proficient user	C1 Proficient user
B1 Independent user	B1 Independent user	A2 Basic user	A2 Basic user	A2 Basic user

^(*) Common European Framework of Reference (CEF) level

March 2011

Certificate in Advanced English (CAE)