

Personal information

Surname / First name

Address

Telephone

Personal Email

Date of birth

Github profile

Scaramuzza Paolo

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scaramu1@dei.unipd.it

16 December, 1992

<https://github.com/electricant>



Education

2006-2011

2011-2014

2014-2016

June 2015

October 2016 - present

Liceo Scientifico "Duca degli Abruzzi" in Gorizia

Bachelor's degree in Information Engineering (110/110)

University of Padova, thesis title: "*A Study on the Efficiency of Integrated LC Oscillators for Ultra Wideband Impulse Radio Transmission*" (supervisor: Andrea Neviani)

Master's degree in Electronics Engineering (110/110 cum laude)

University of Padova, thesis title: "*Project of a Class J Power Amplifier in Integrated Bipolar Technology*" (supervisor: Andrea Neviani)

Roberto Rocca scholarship winner

Ph.D. in Information Engineering, University of Padova

Topic: *analog integrated circuits for radio-frequency applications* (supervisor: Andrea Neviani)

Publications

ESSCIRC 2017

TCAS-I 2018

TCAS-II 2019 (accepted)

P. Scaramuzza, C. Rubino, M. Tiebout, M. Caruso, M. Ortner, A. Neviani, and A. Bevilacqua

Class-AB and Class-J 22dBm SiGe HBT PAs for X-Band Radar Systems

P. Scaramuzza, C. Rubino, M. Caruso, M. Tiebout, A. Bevilacqua, and A. Neviani

Class-J SiGe X-Band Power Amplifier Using a Ladder Filter-Based AM-PM Distortion Reduction Technique

P. Scaramuzza, C. Rubino, M. Caruso, M. Tiebout, A. Bevilacqua, and A. Neviani

A Fully-Integrated SiGe BiCMOS Power Amplifier for 5G with Minimum AM-PM Distortion

Working Experience

October 2017 - May 2018

Internship at Infineon Technologies Villach, Austria

The internship was focused on the design and measurement of integrated 28 GHz power amplifiers for 5G cellular networks.

Other Activities

Sept. 2015 - Sept. 2017

Member in the electronic subsystem design team for the MORPHEUS project, University of Padova. The aim of the project was the design of a rover for the European Rover Challenge, an international robotics competition with tasks analogous to those performed by rovers on the surface of Mars and the Moon.

March 2017 - June 2017
October 2018 - February 2019

Academic tutoring at the University of Padova in Analog Integrated Circuits Design (A. Bevilacqua)
Academic tutoring at the University of Padova in Integrated Circuits for Signal Processing (A. Gerosa)

Personal skills

Electronics skills

Interested in electronics since childhood, with practical experience in designing and fabricating circuits and PCBs.
Analog RF IC design & layout with Cadence Virtuoso and ADS.
Electromagnetic simulations with HFSS, ADS Momentum and Sonnet.
RF circuits measurement and characterization.
Knowledge on VHDL hardware description language.

Computer skills

Competent with several programming languages such as: C/C++, Perl, Python, Java, Javascript, HTML, CSS, PHP, MATLAB and functional programming languages.
Database knowledge (SQL), competent in networking and sysadmin tasks with Linux and BSD operating systems.

Mother tongue

Italian

*Self-assessment
European level^(*)*

English

German

Understanding		Speaking		Writing
Listening	Reading	Spoken interaction	Spoken production	
C1 Proficient user	C1 Proficient user	C1 Proficient user	C1 Proficient user	C1 Proficient user
B1 Independent user	B1 Independent user	A2 Basic user	A2 Basic user	A2 Basic user

^(*) Common European Framework of Reference (CEF) level

March 2011

Certificate in Advanced English (CAE)

Vacation studies abroad

July 2008
July 2009
August 2010

Euroinstitute(Innsbruck, Austria)
ActiLingua Academy (Vienna, Austria)
Institut für Interkulturelle Kommunikation (Jena, Germany)