

# PCIe to 2-Port Gigabit Ethernet Switch

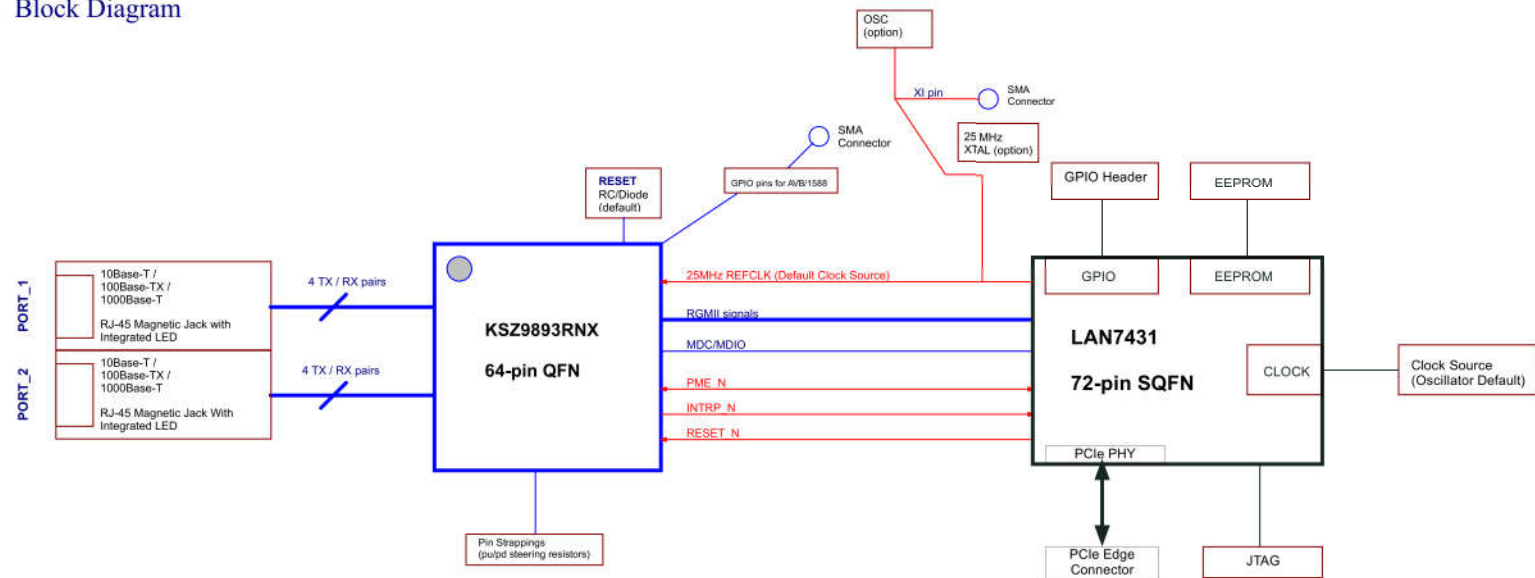
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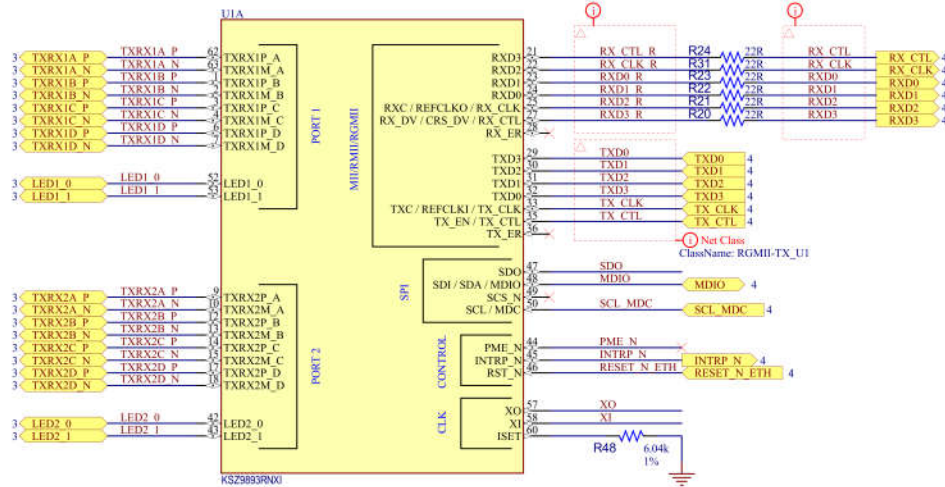
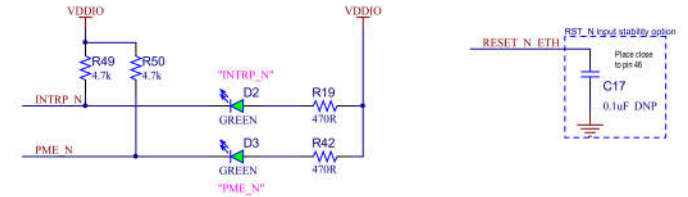
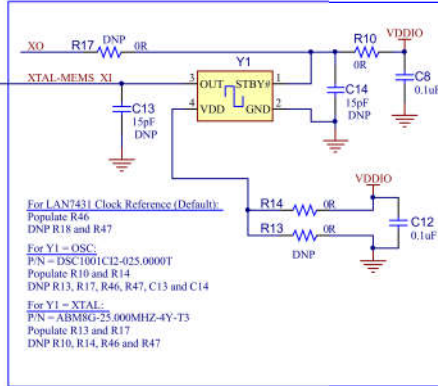
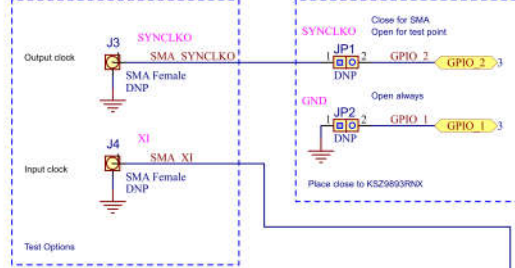
## Revision History

Revision	Date	Revision Summary	Author
A0	8/14/2018	Initial Release	John MacKay

## Block Diagram



# External Reference Clocks



## Strapping Pins

Refer to KSZ9893RNX Datasheet for detailed descriptions.

Strapping pins have internal pull-ups/pull-downs (default values); external pull-ups/pull-downs are used to override internal values.

RXD3, RXD2	Port_3 xMII Mode	RXD1, RXD0	Management Mode
0, 0	MII (default)	0, 0	MDIO/MDIO (default)
0, 1	RMII	0, 1	I2C Slave
1, 0	<reserved>	1, x	SPS Slave
1, 1	RGMI		

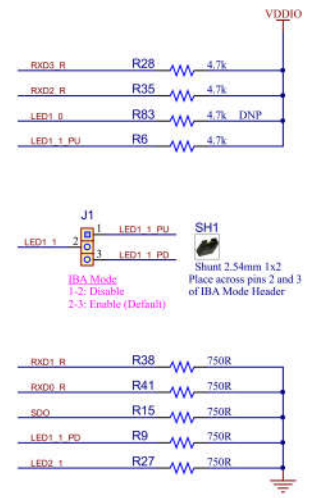
LED2_1	Port_3 Speed	LED1_1	IBA Mode
0	1000Mbps	0	Enable (default)
1	10/100Mbps (default)	1	Disable

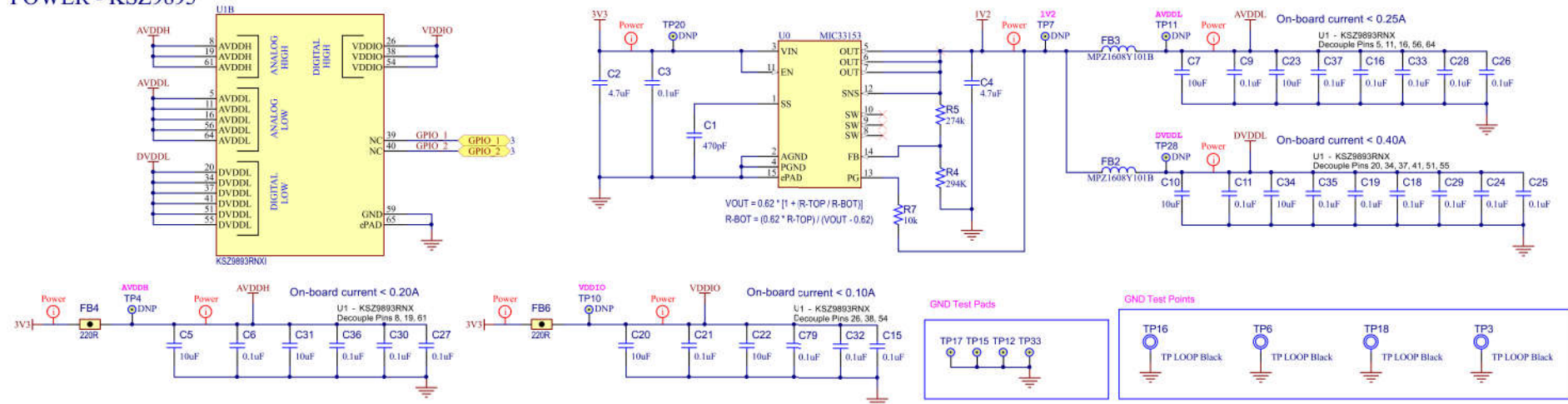
LED1_0	Start Switch Configuration
0	Start Switch disabled Switch will not forward packets until the Start bit is set in Register 0x0_3_00.
1	Start Switch enabled (default) Switch will forward packets immediately after reset.

RX_ER, PME_N	PHY Ports 1 and 2 Configuration
0, 0	<reserved>
0, 1	Auto-Negotiation enable with EEE enable (default)
1, 0	Auto-Negotiation disable (Force 100Mbps, Full-duplex mode), and Auto-MDIX disable (set to MDI-X mode)
1, 1	Auto-Negotiation disable (Force 100Mbps, Full-duplex mode), and Auto-MDIX disable (set to MDI mode)



## POWER - KSZ9893



## ETHERNET

