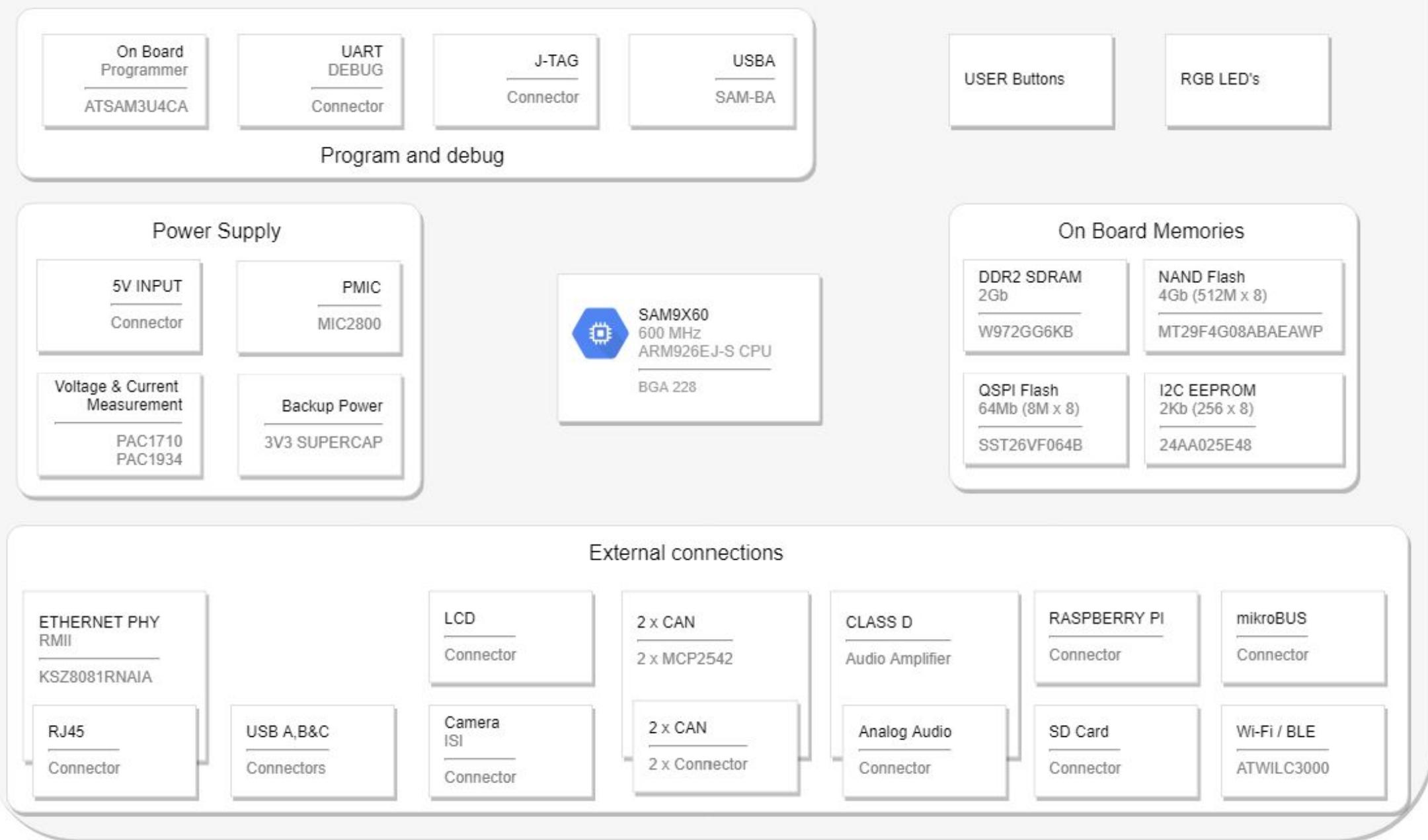
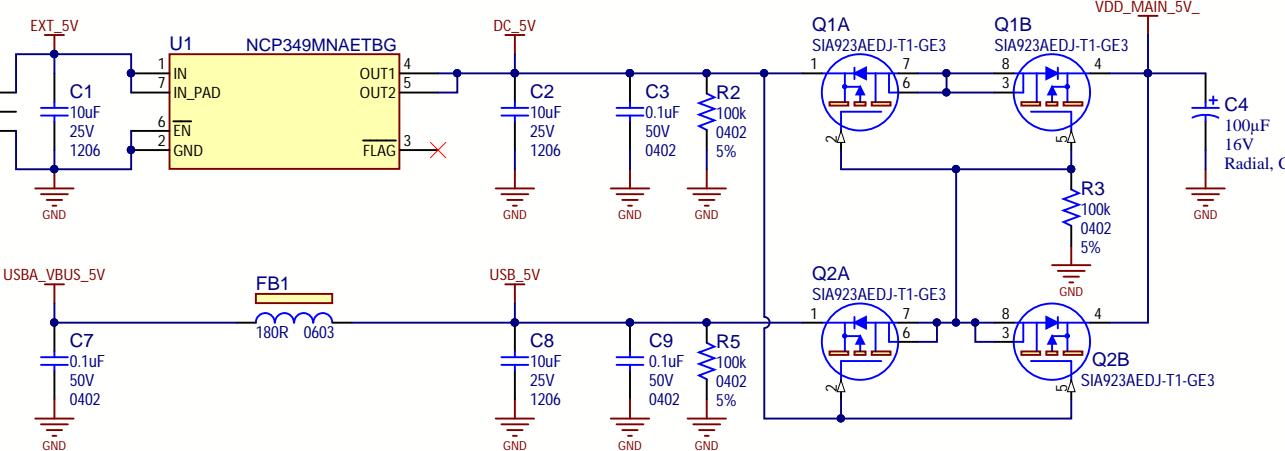
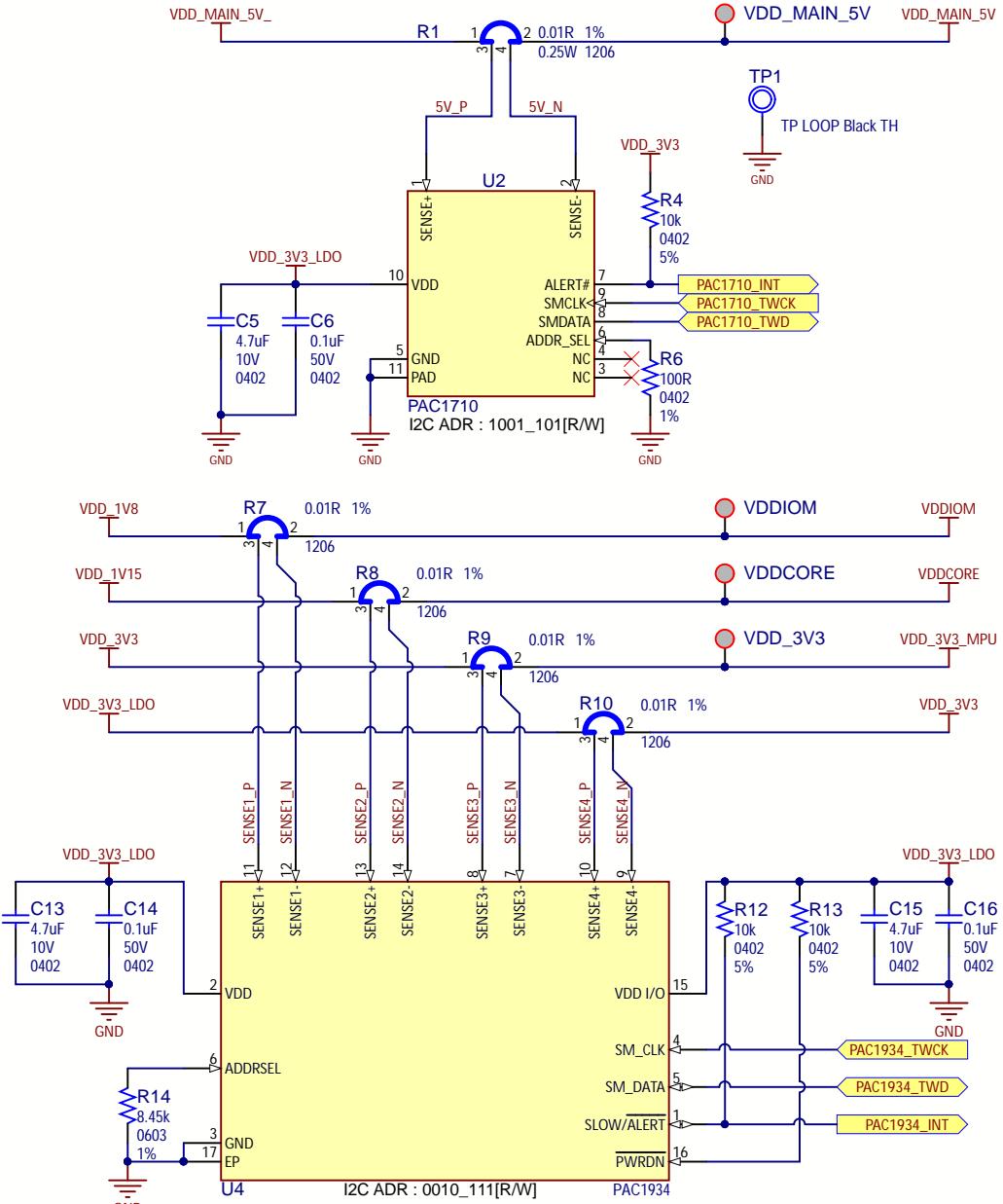
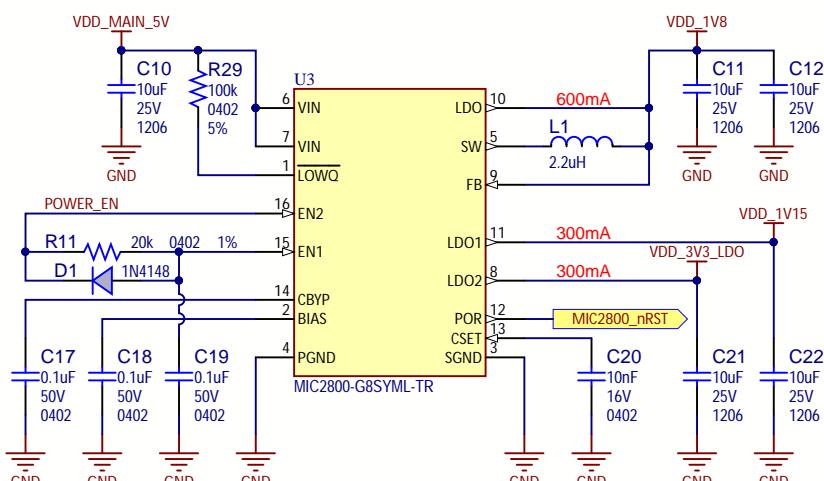
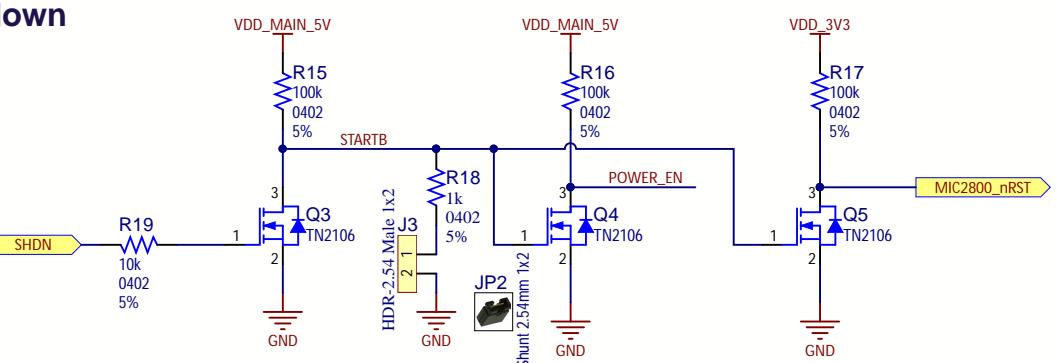
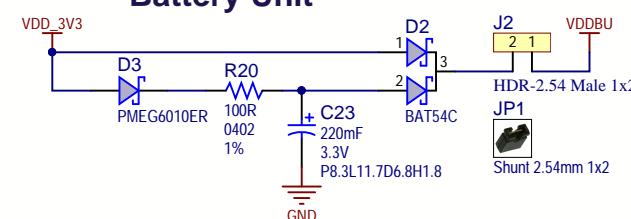




SAM9X60 - EVALUATION KIT

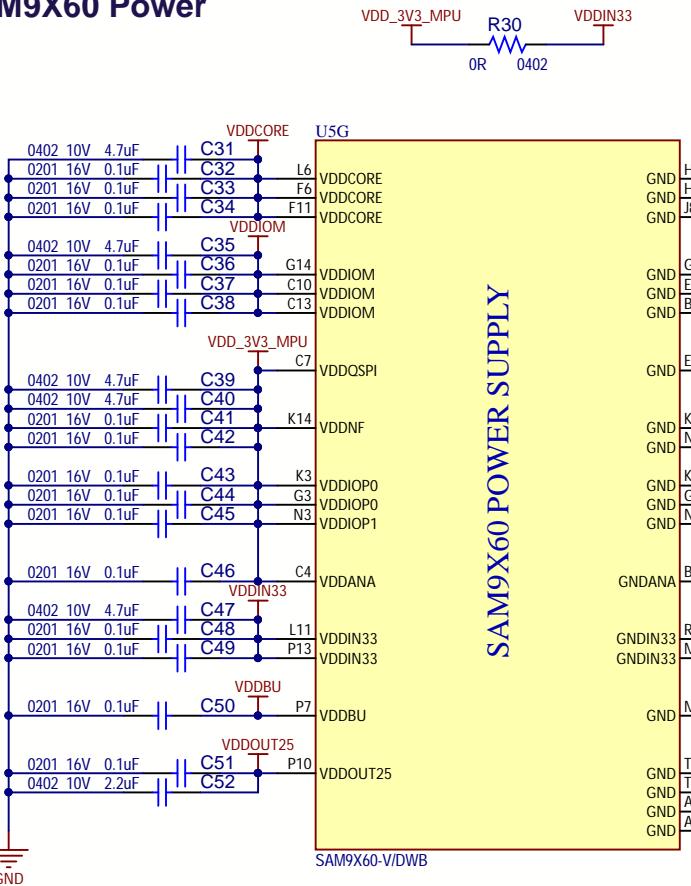


Drawn By: IGn	Engineer: IGn	MICROCHIP	
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>		
Sheet Title Block Diagram	Variant Name Standard_WILC3000_DNP	Designed with Altium Altium.com	
Size B	Sch #: 03-11039 Revision:2	Date: 9/20/2019 10:27:44 AM	Sheet 1 of 13
File: 01 - Block Diagram.SchDoc			

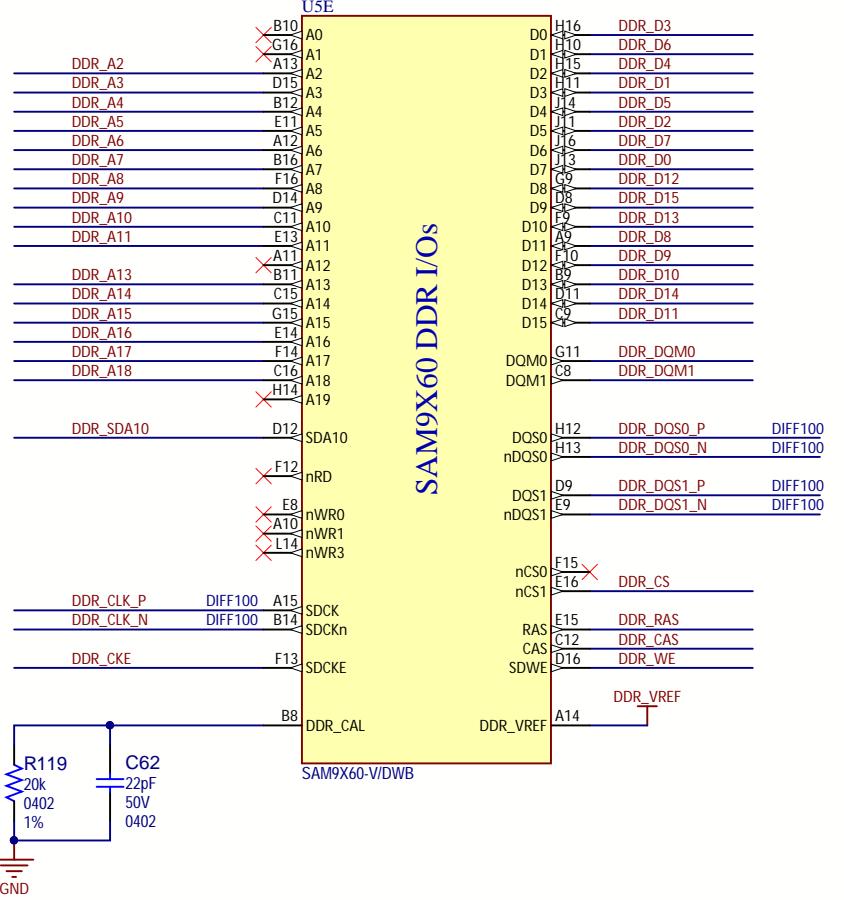
Input Power Options**Power Measurement****PMIC****Shut-down****Battery Unit**

Drawn By: IGn	
Engineer: IGn	
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit
Sheet Title Power supply	Variant Name Standard_WILC3000_DNP
Size B Sch #: 03-11039 Revision:2	Date: 9/20/2019 10:27:44 AM Sheet 2 of 13
File: 02 - Power supply.SchDoc	

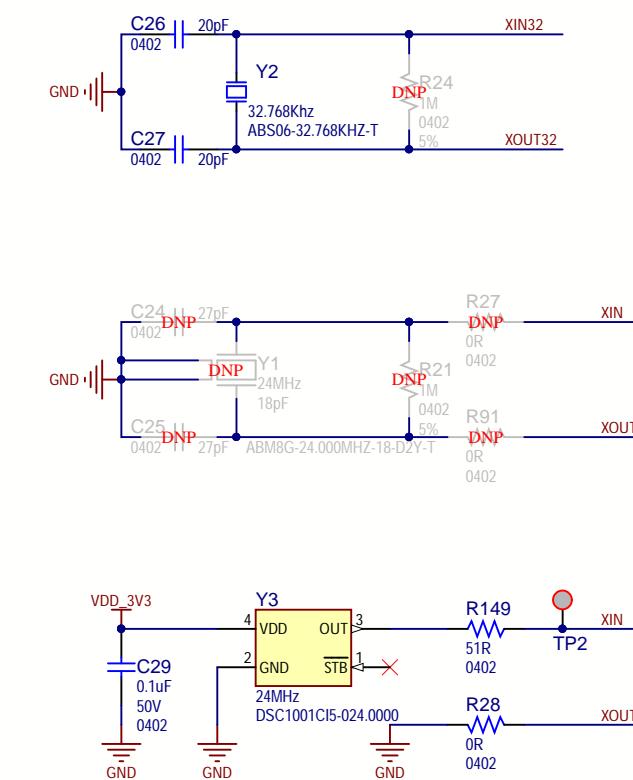
SAM9X60 Power



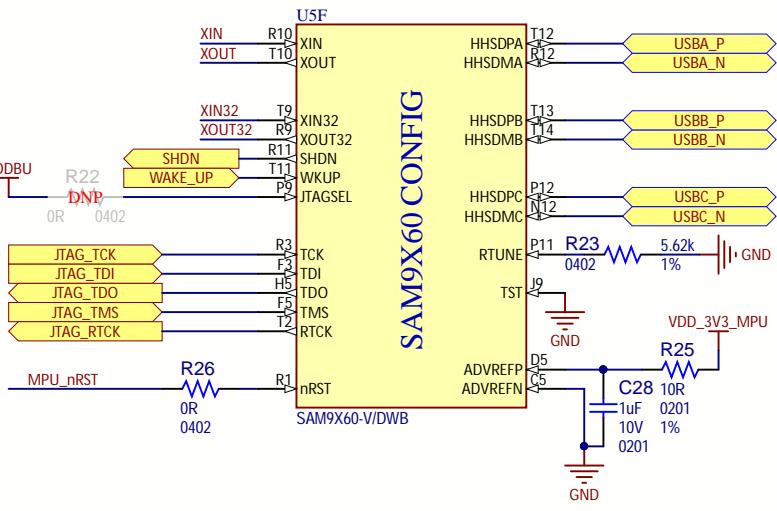
SAM9X60 DDR Controller



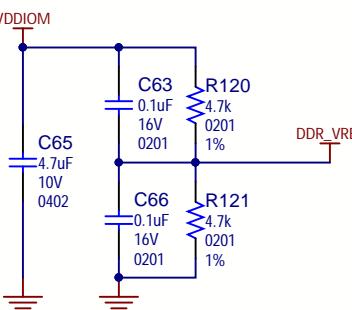
SAM9X60 Clocks



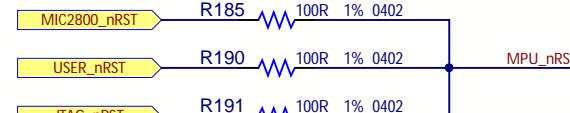
SAM9X60 Core



DDR_VREF

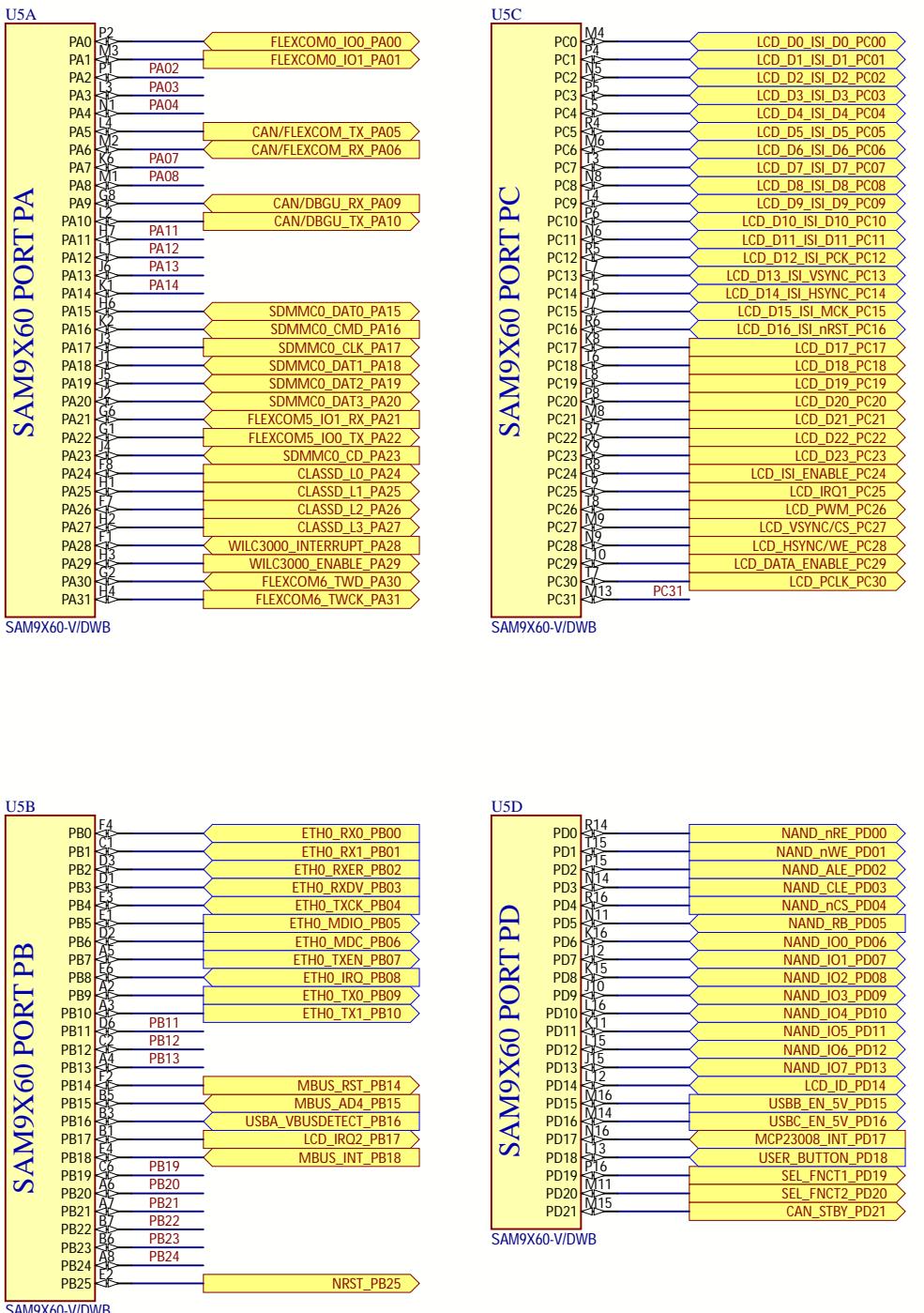


SAM9X60 Reset

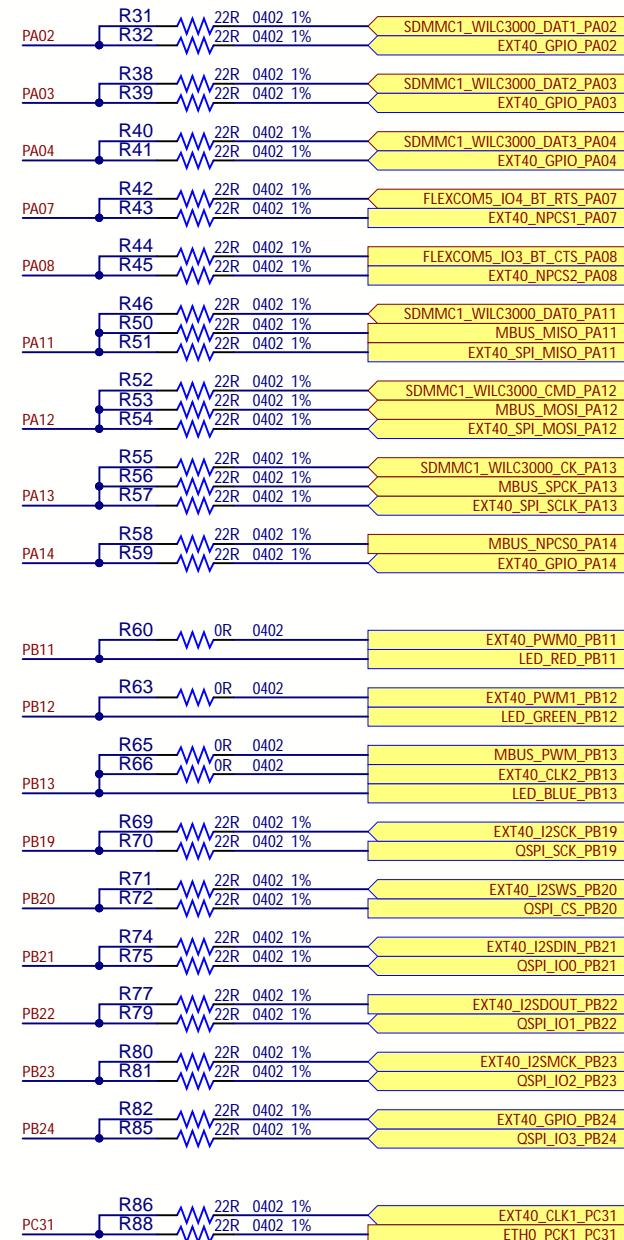


Drawn By: IGn		MICROCHIP
Engineer: IGn		
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit	
Sheet Title Processor SAM9X60	Variant Name Standard_WILC3000_DNP	Designed with Altium Altium.com
Size B	Sch #: 03-11039 Revision: 2	Date: 9/20/2019 10:27:44 AM Sheet 3 of 13
File: 03 - Processor SAM9X60.SchDoc		

SAM9X60 PIOs



SAM9X60 PIO Muxing



Drawn By:
IGn



Engineer:
IGn

PartNumber:
DT100126

Project Title:
SAM9X60 Evaluation Kit

Sheet Title:
Processor IOs

Variant Name:
Standard_WILC3000_DNP

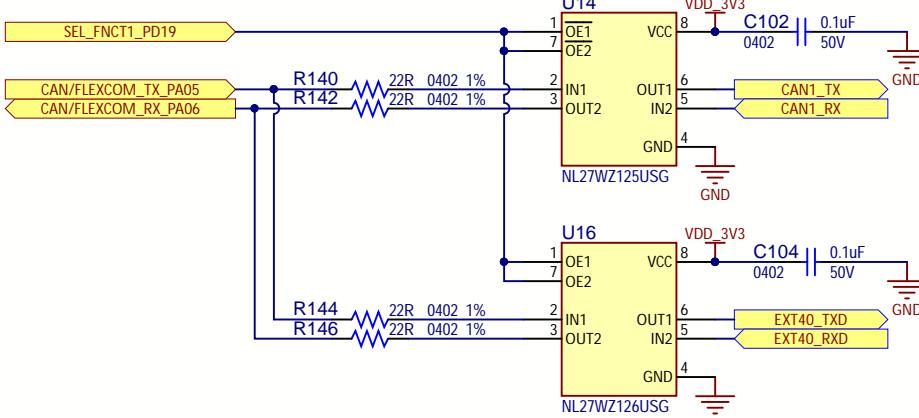
Size: B

Date: 9/20/2019 10:27:45 AM
Revision: 2

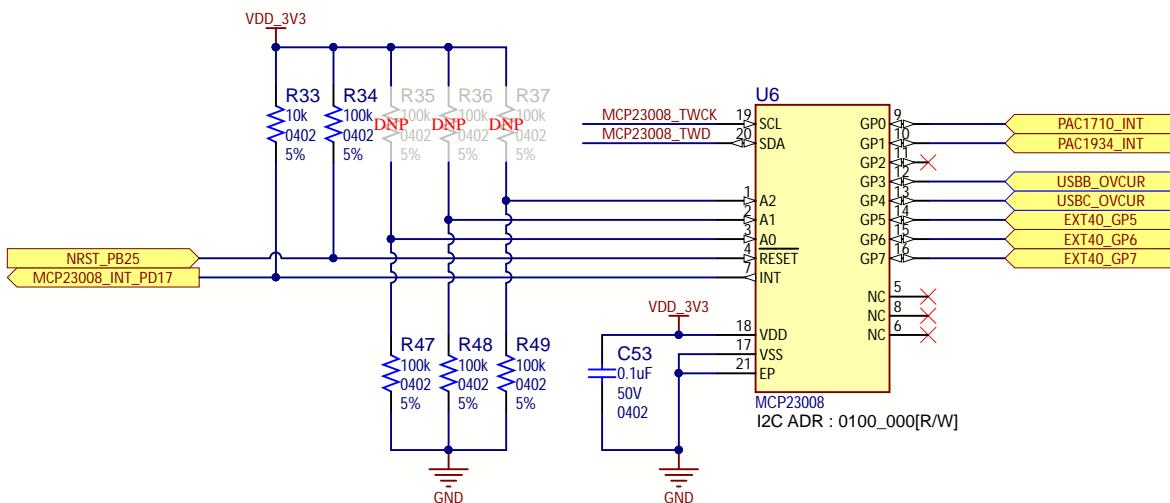
File: 04 - Processor IOs.SchDoc



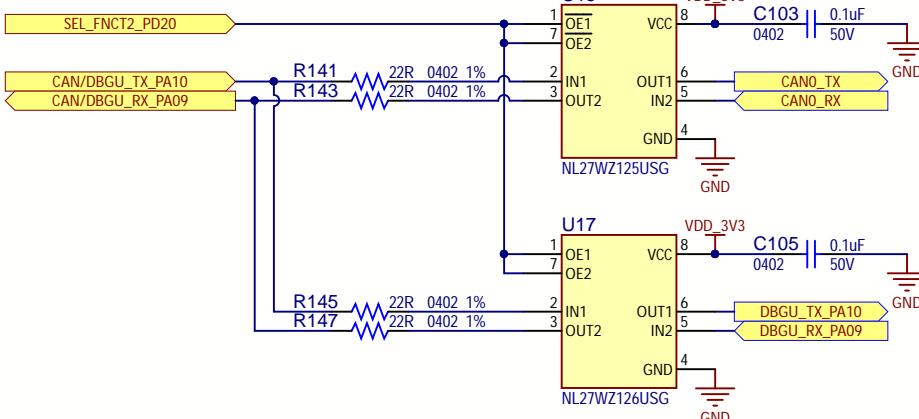
CAN1 / EXT40 UART Selection



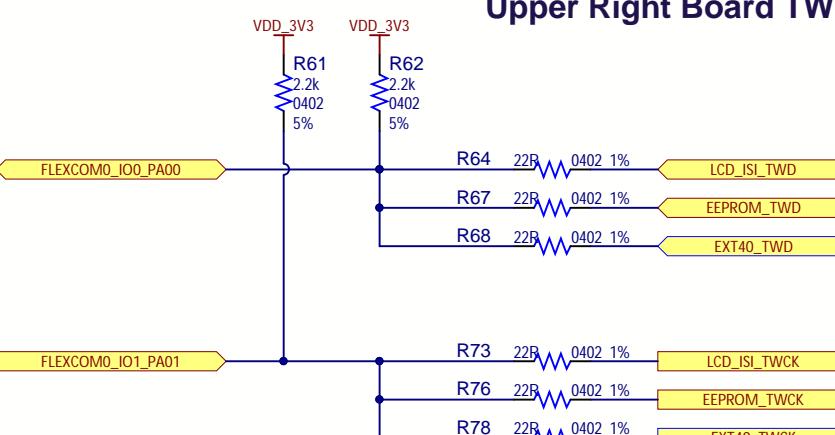
Port Expander



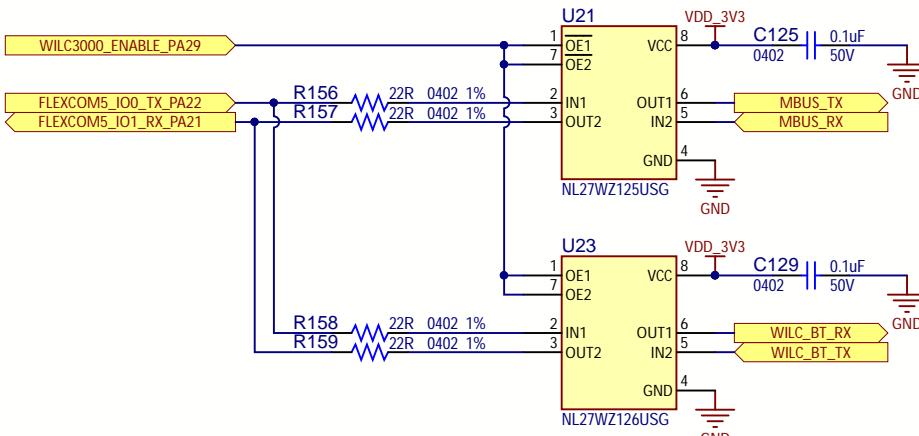
CAN0 / DEBUG UART Selection



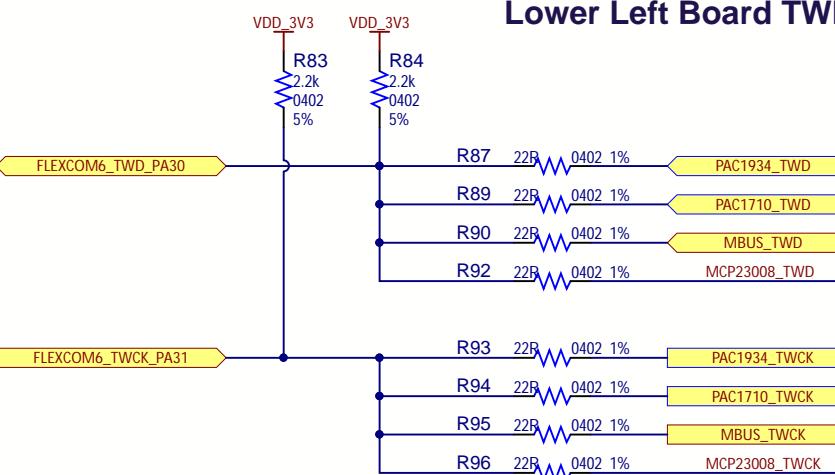
Upper Right Board TWI



MikroBUS UART / WILC BT UART Selection



Lower Left Board TWI



Drawn By:
IGn

Engineer:
IGn

PartNumber:
DT100126

Project Title:
SAM9X60 Evaluation Kit

Sheet Title
Processor IO Expansions

Variant Name
Standard_WILC3000_DNP

Size **B**

Date: **9/20/2019 10:27:45 AM**

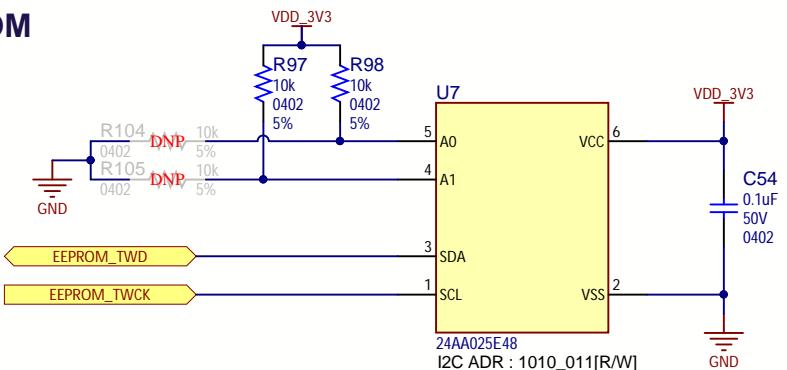
Revision: **2**

Sheet **5** of 13

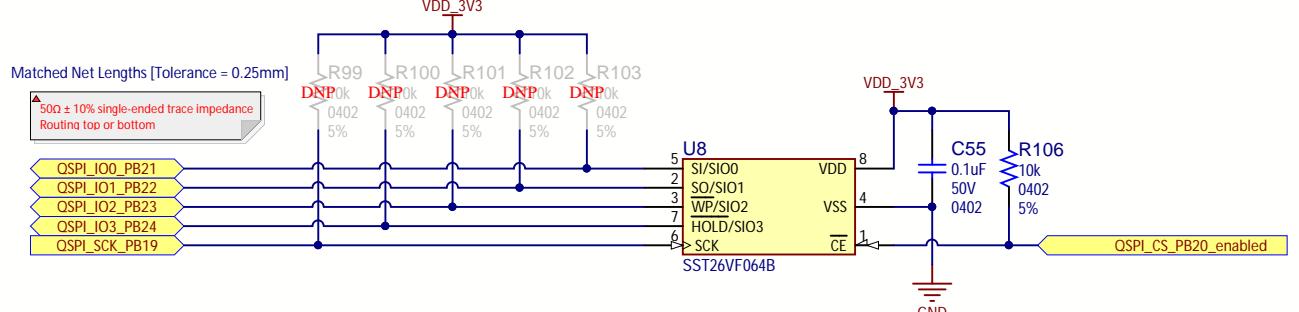
File: **05 - Processor IO Expansions.SchDoc**



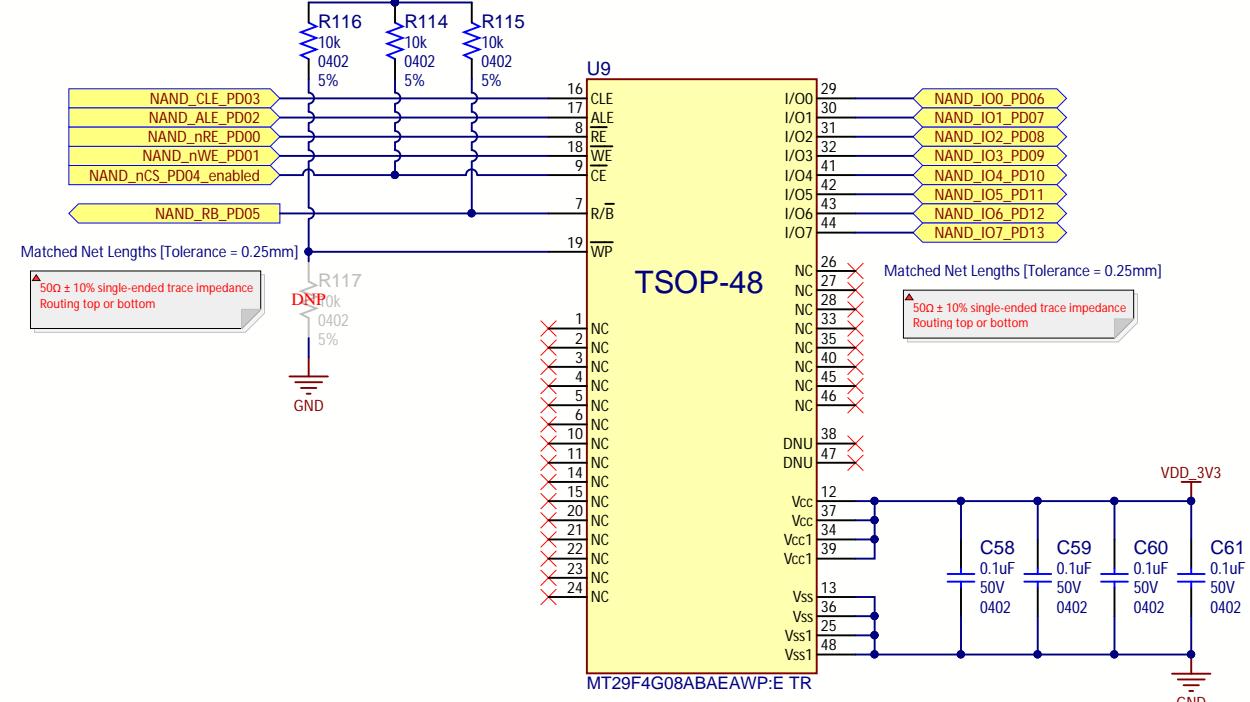
TWI EEPROM



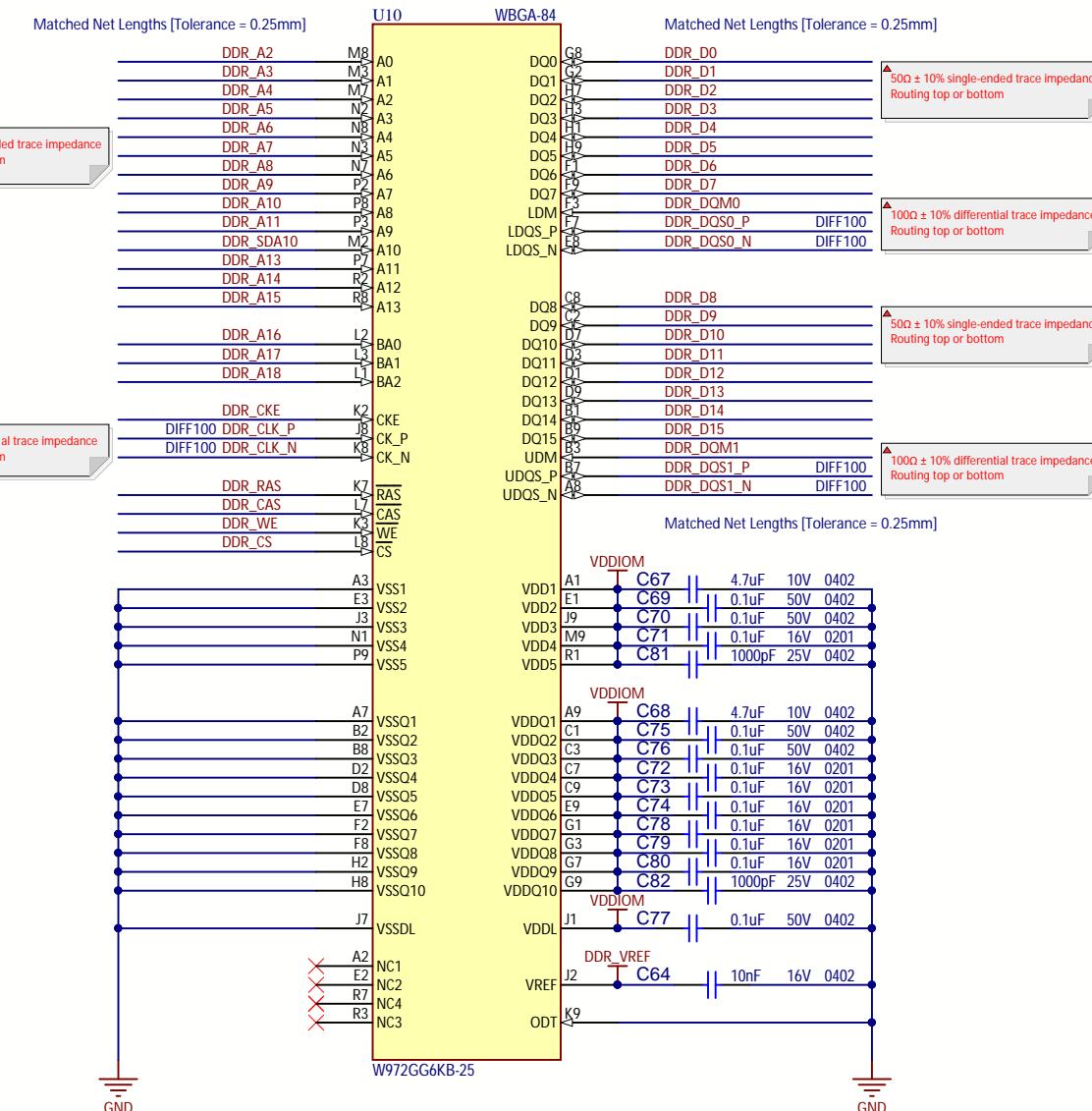
Serial Quad I/O Flash



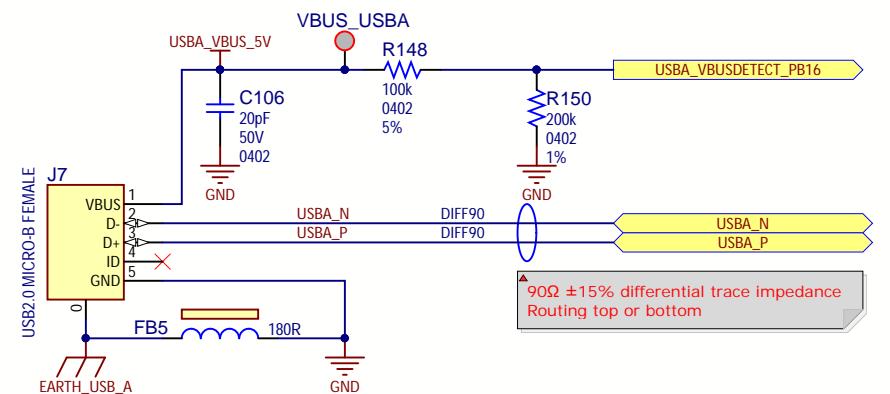
4Gb NAND Flash



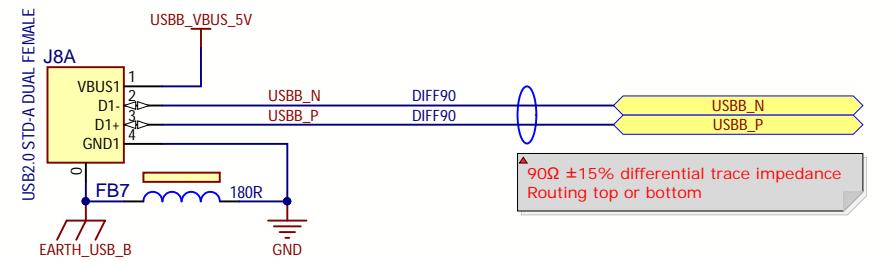
2Gb DDR2-800 SDRAM 16bit



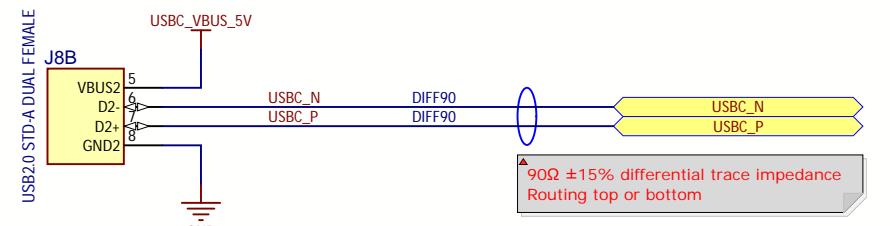
USB-A port



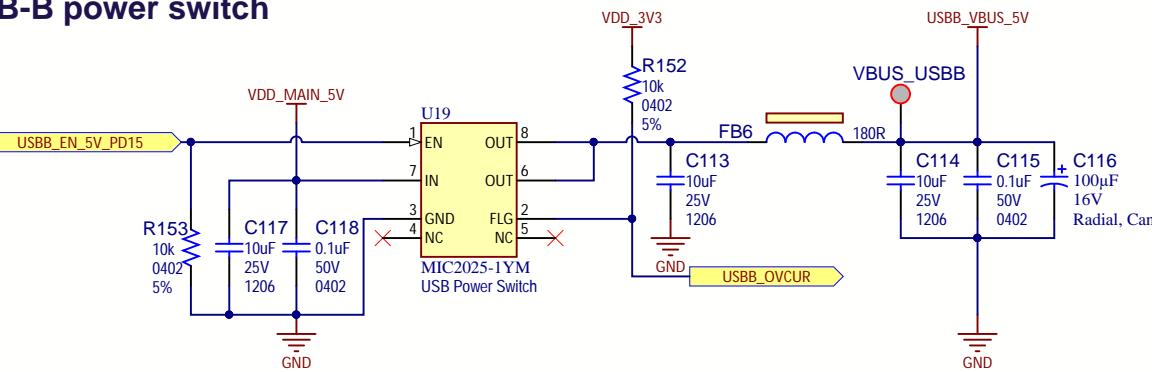
USB-B port



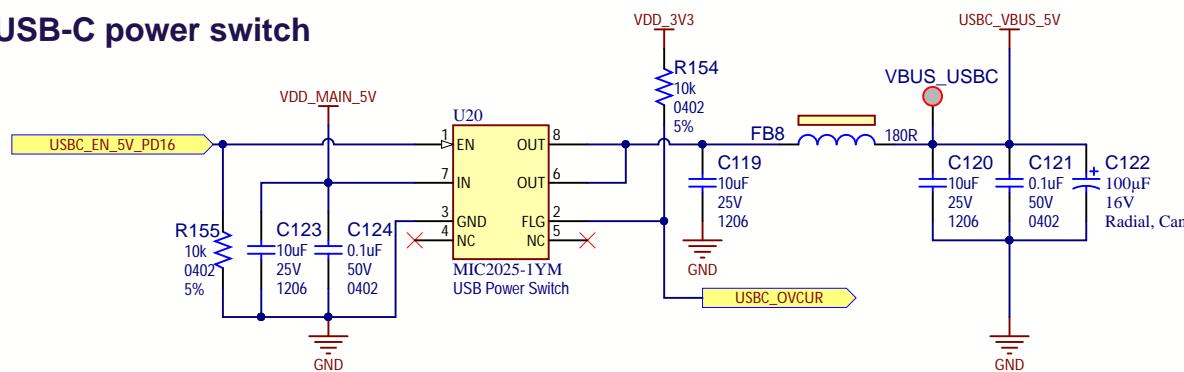
USB-C port



USB-B power switch



USB-C power switch



Drawn By:
IGn



Engineer:
IGn

PartNumber:
DT100126

Project Title:
SAM9X60 Evaluation Kit

Sheet Title:
USB interfaces

Variant Name:
Standard_WILC3000_DNP

Size:
B

Date:
9/20/2019 10:27:46 AM

Revision:
2

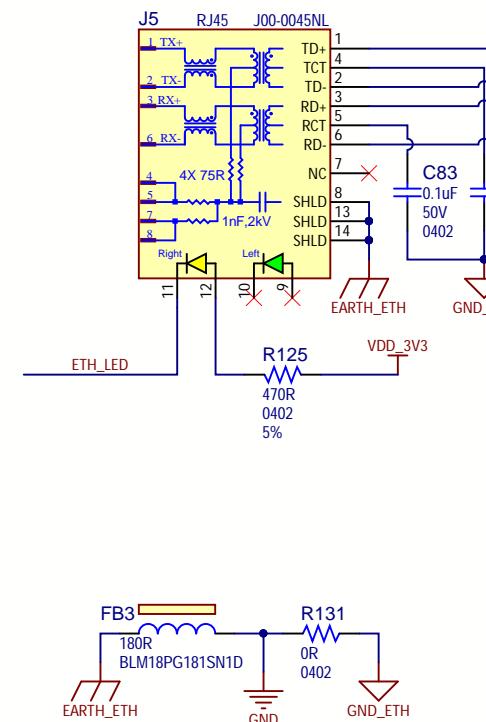
Sheet:
7 of 13

File:
07 - USB ports.SchDoc

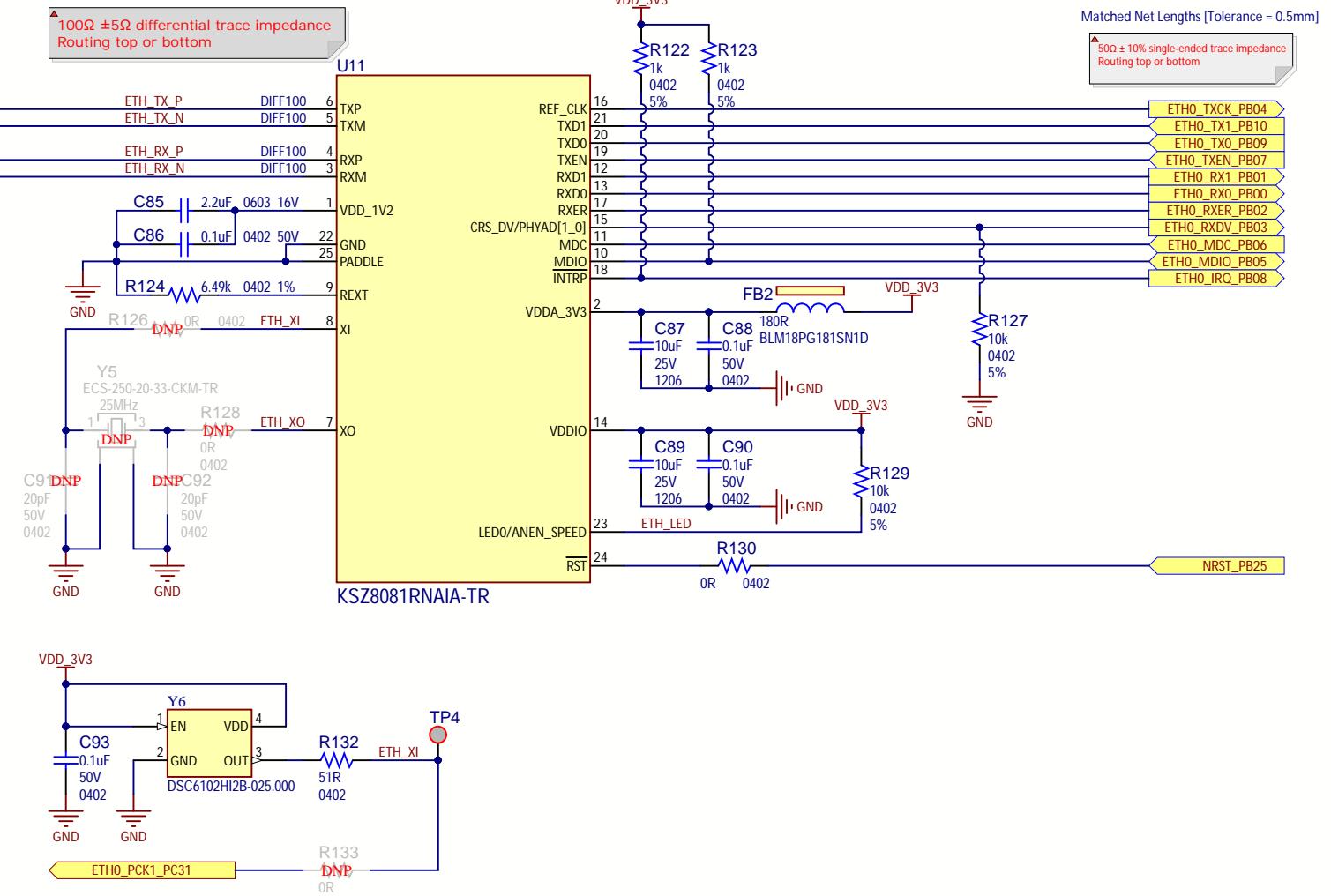
Designed with
Altium

Altium.com

Ethernet Connector



Ethernet PHY



Drawn By:
IGn



Engineer:
IGn

PartNumber:
DT100126

Project Title:
SAM9X60 Evaluation Kit

Sheet Title:
Ethernet MAC

Variant Name:
Standard_WILC3000_DNP

Designed with



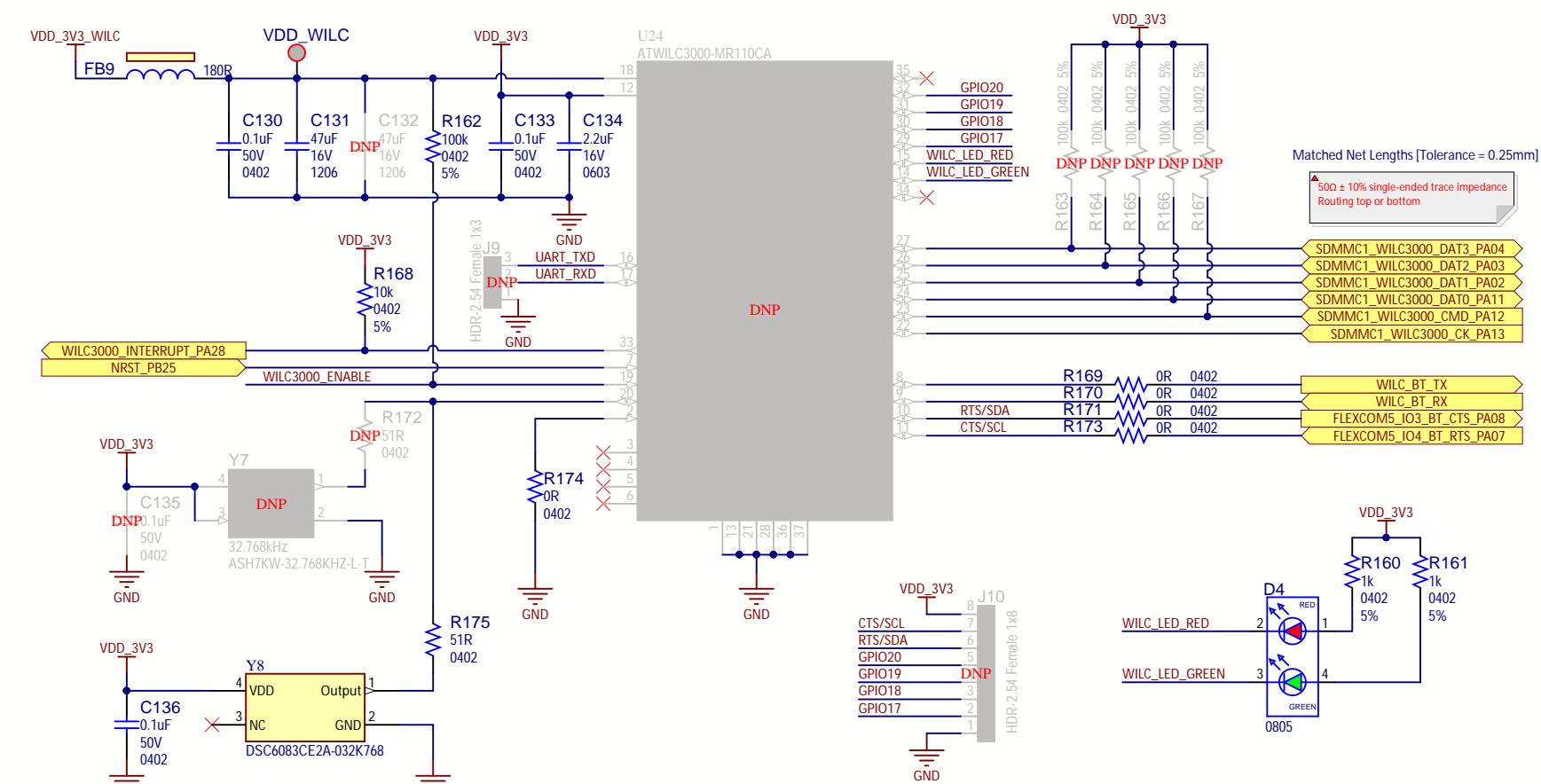
Date: 9/20/2019 10:27:46 AM

Revision: 2

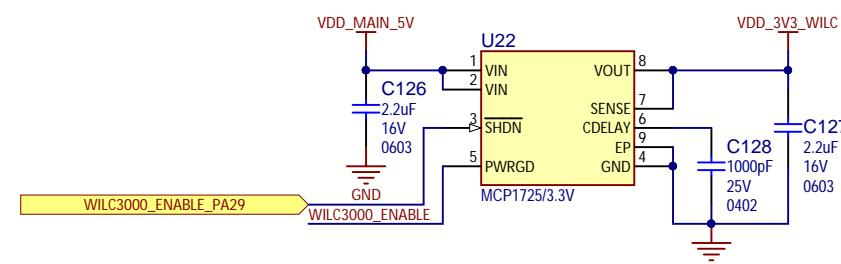
Sheet 8 of 13

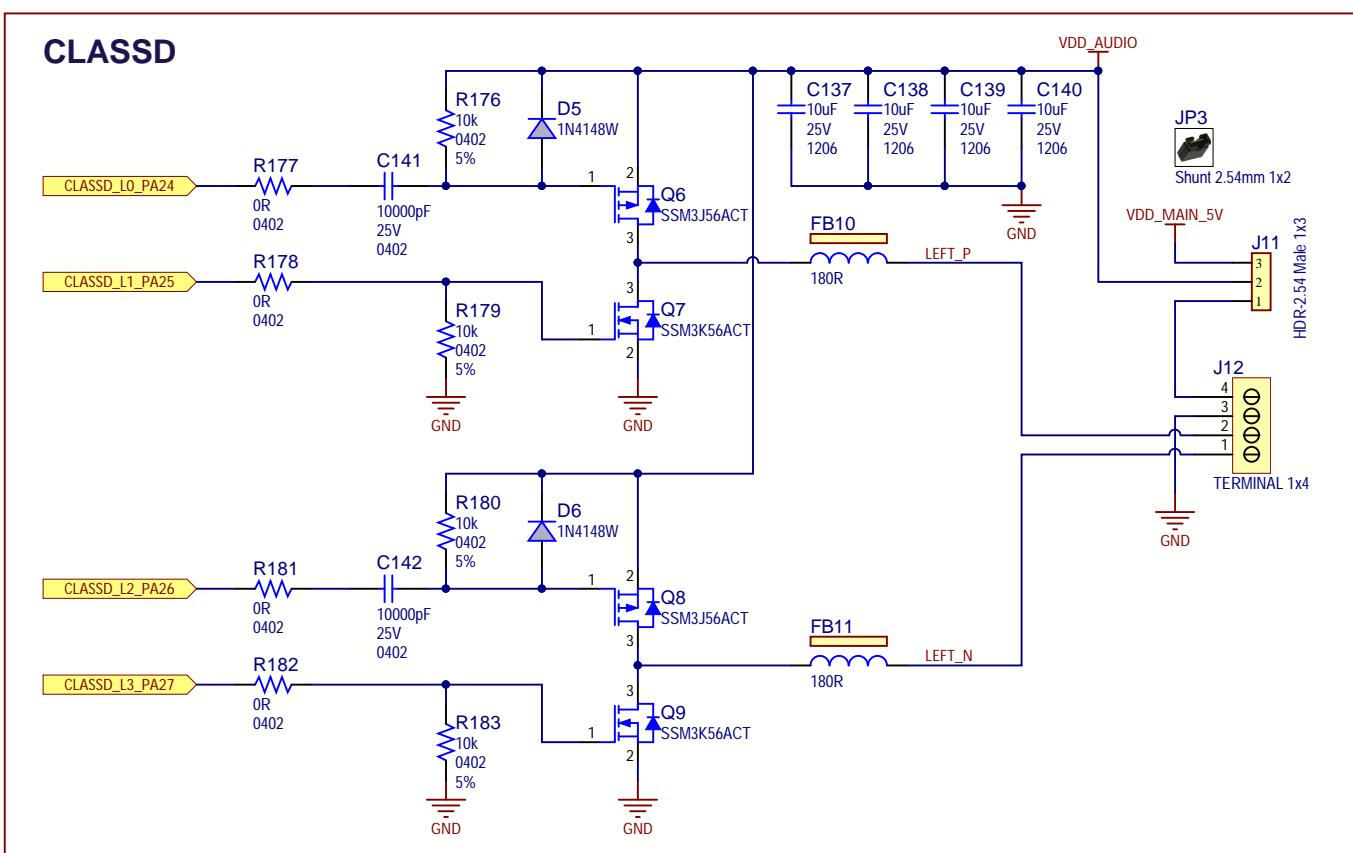
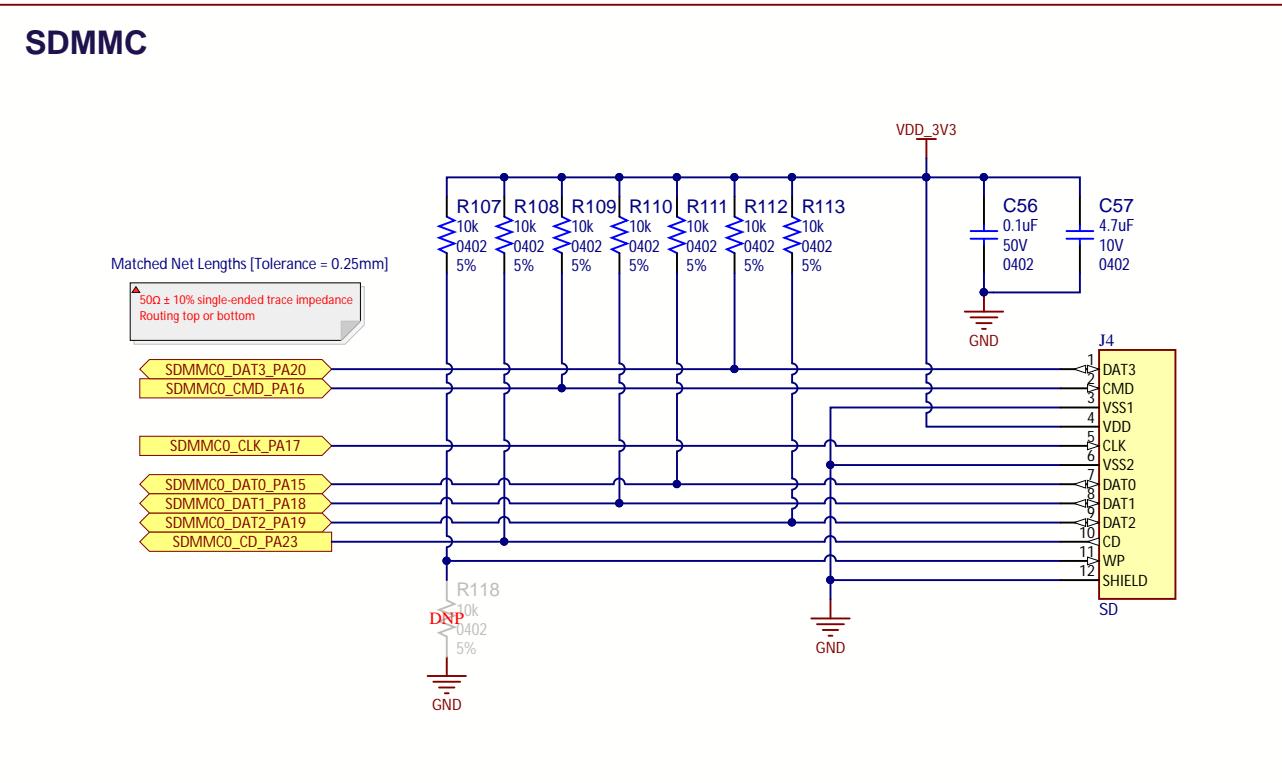
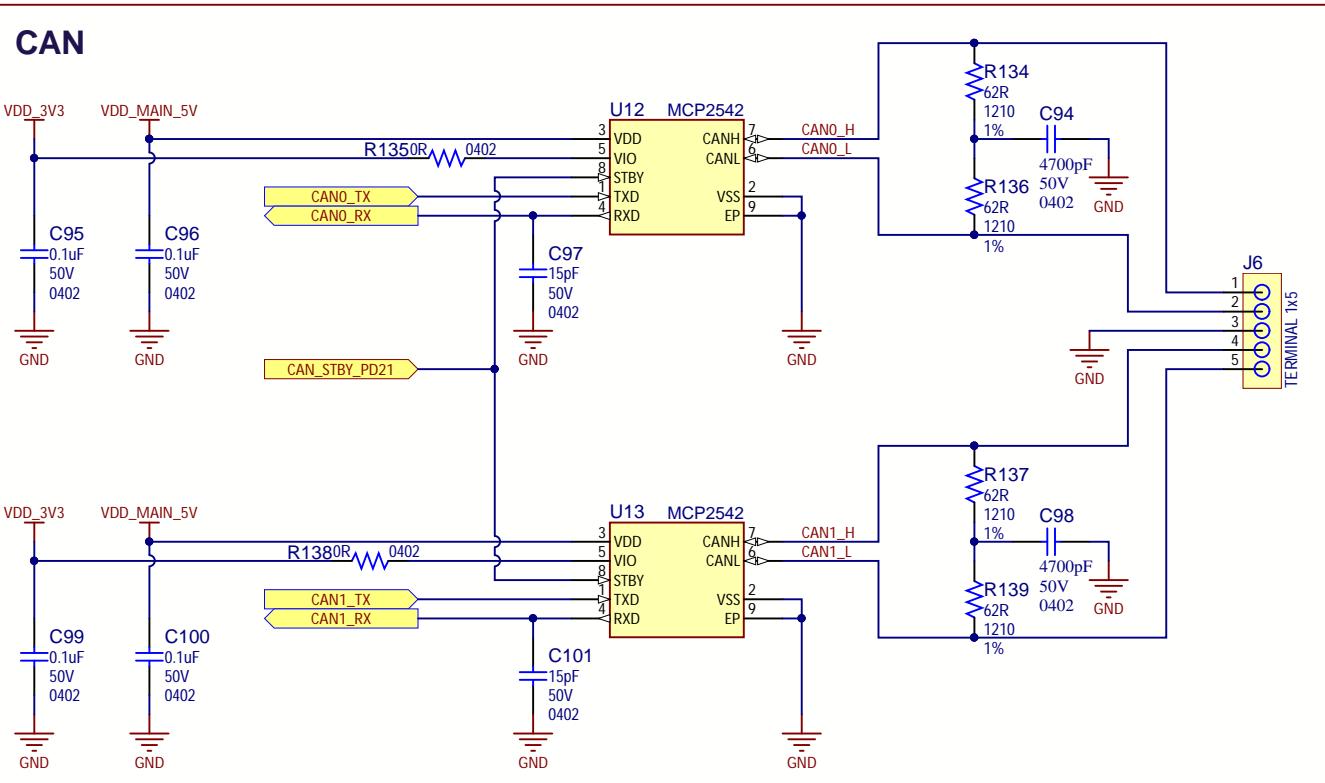
File: 08 - Ethernet MAC.SchDoc

WiFi / BT MODULE

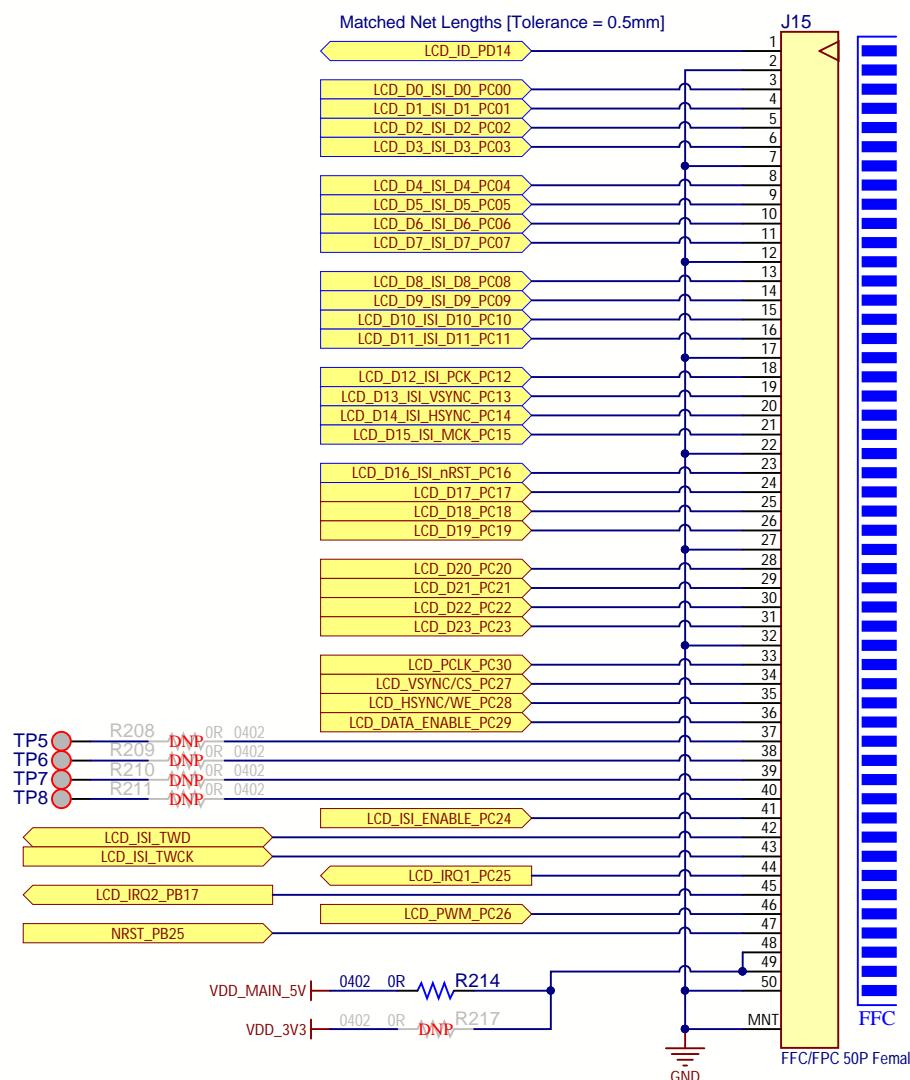
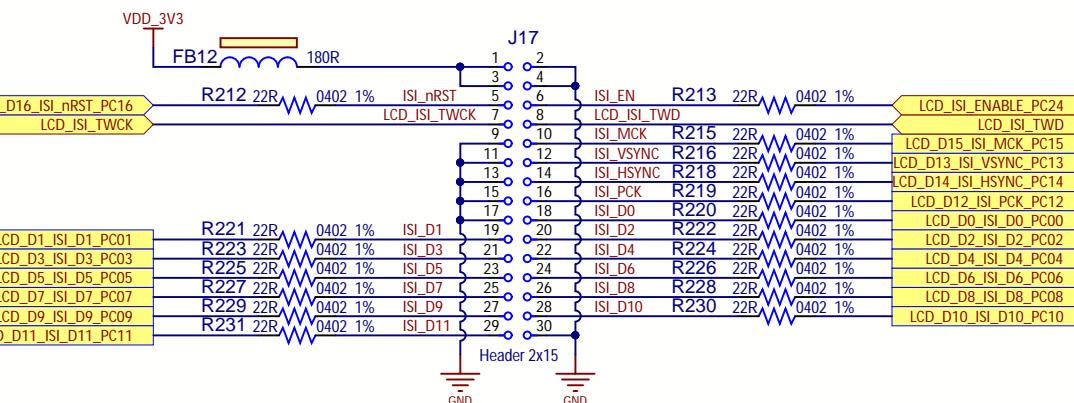
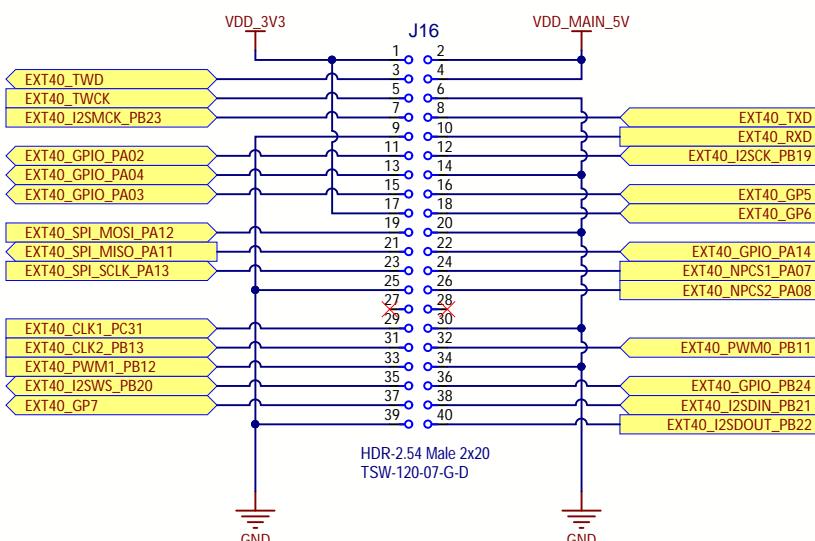
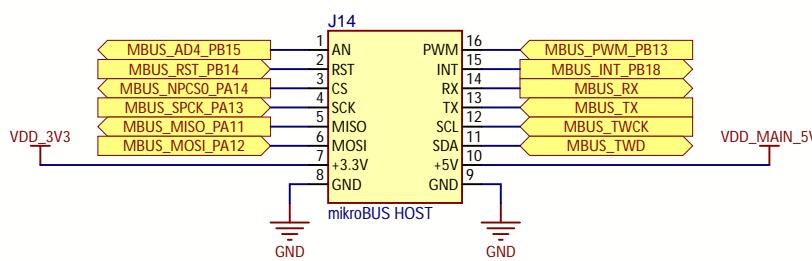


WiFi / BT ENABLE

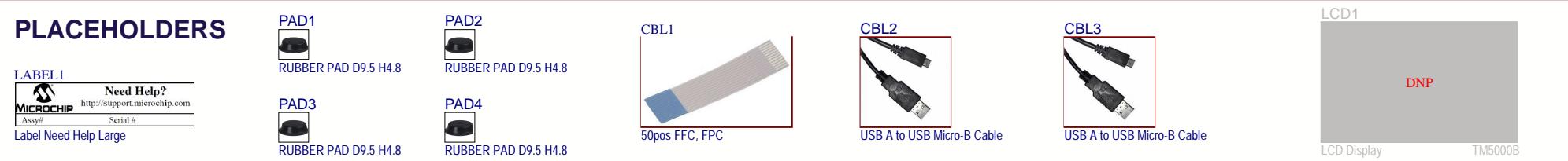
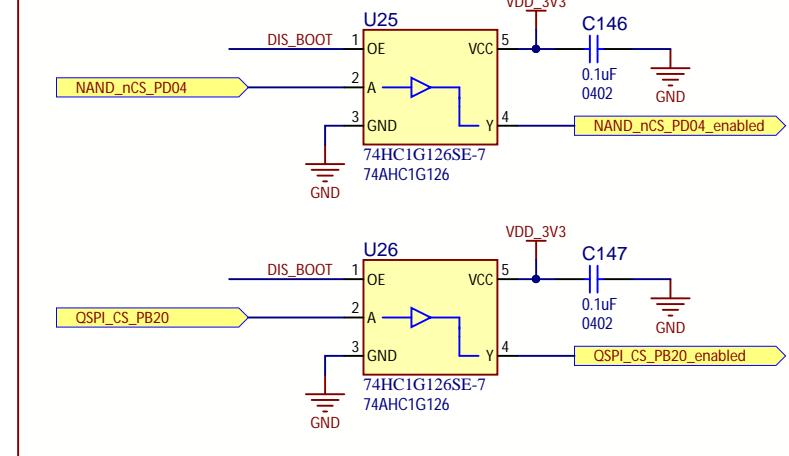
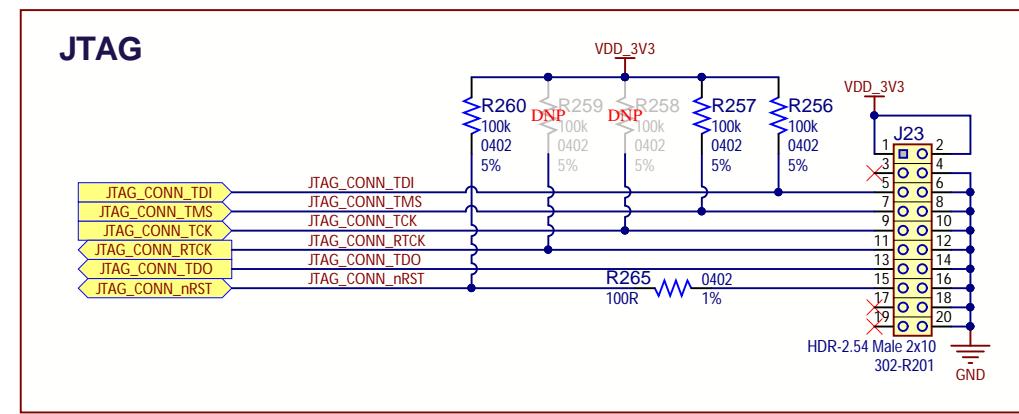
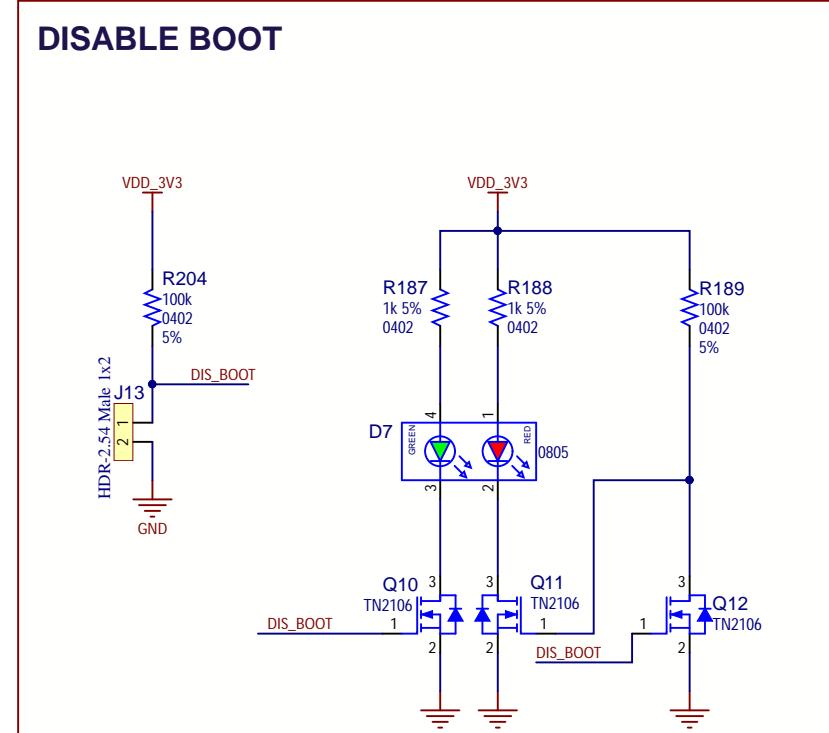
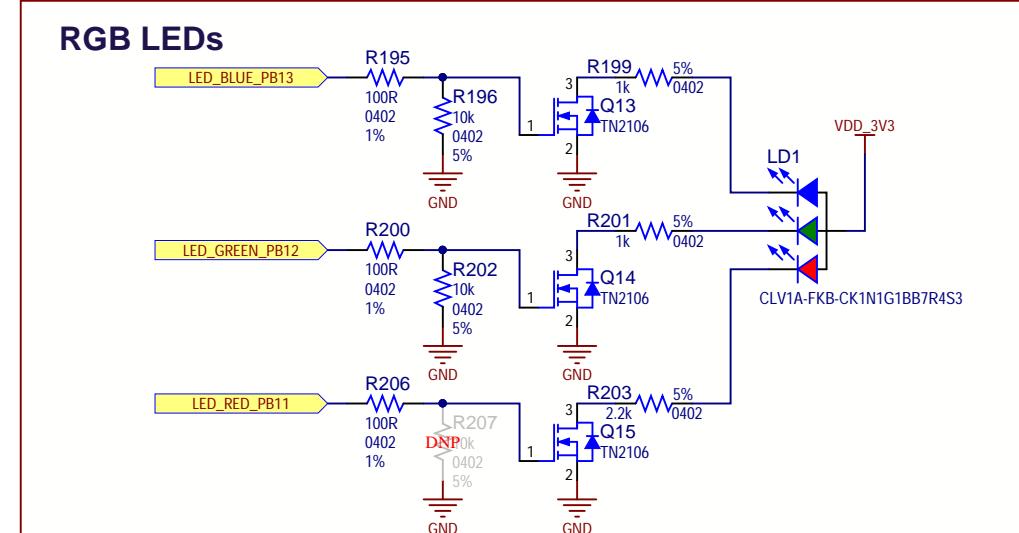
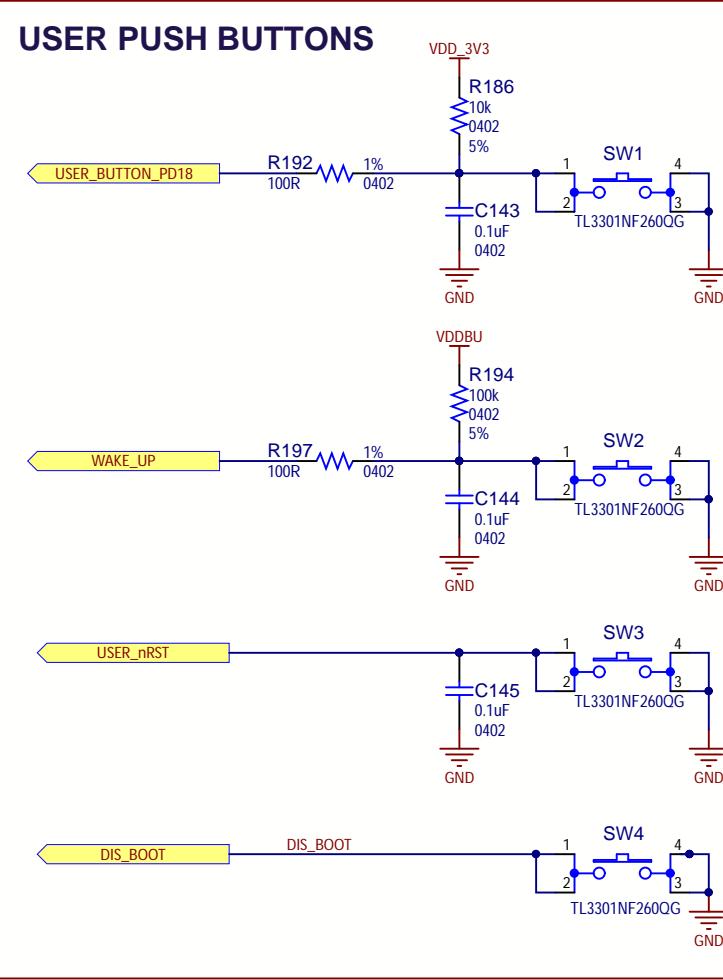
Drawn By:
IGnEngineer:
IGnPartNumber:
DT100126Project Title:
SAM9X60 Evaluation KitSheet Title:
Wi-Fi BTSize: **B**Revision: **2**Date: **9/20/2019 10:27:46 AM**Page: **9 of 13**File: **09 - Wi-Fi BT module.SchDoc**Variant Name:
Standard_WILC3000_DNPDesigned with:
AltiumAltium.com



Drawn By: IGn	Engineer: IGn	MICROCHIP	
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit		
Sheet Title SDMMC, CAN & CLASSD	Variant Name Standard_WILC3000_DNP		Designed with Altium Altium.com
Size B	Sch #: 03-11039	Date: 9/20/2019 10:27:46 AM	
Revision: 2	Sheet 10 of 13		
File: 10 - SDMMC, CAN & CLASSD.SchDoc			

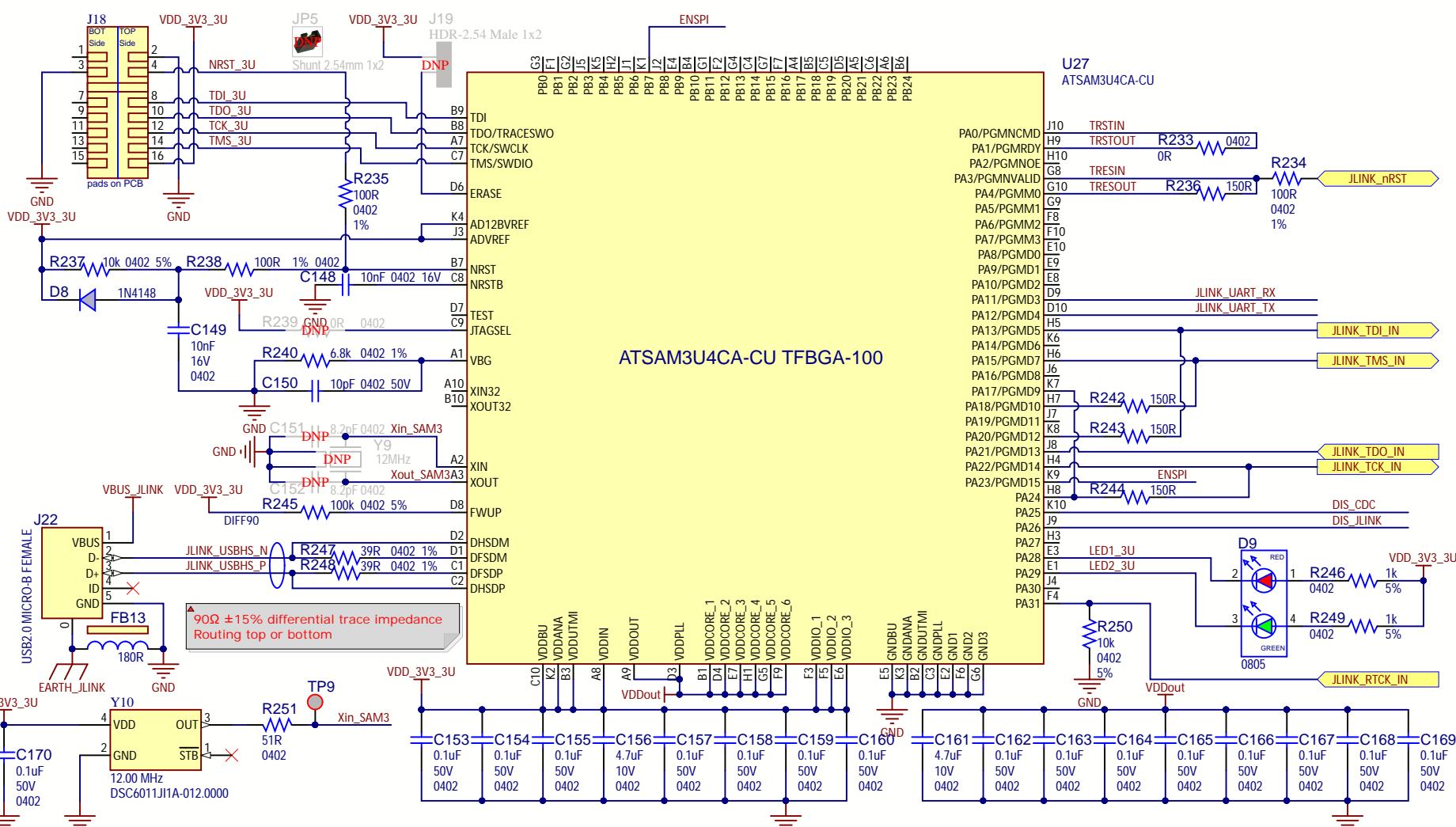
LCD**ISI Camera****40-pin GPIO****MikroBUS**

Drawn By: IGn	 MICROCHIP
Engineer: IGn	
PartNumber: DT100126	Project Title SAM9X60 Evaluation Kit
Sheet Title Expansion connectors	Variant Name Standard_WILC3000_DNP
Size B	Date: 9/20/2019 10:27:47 AM
Revision: 2	Sheet 11 of 13
File: 11 - Expansion connectors.SchDoc	Designed with Altium Altium.com

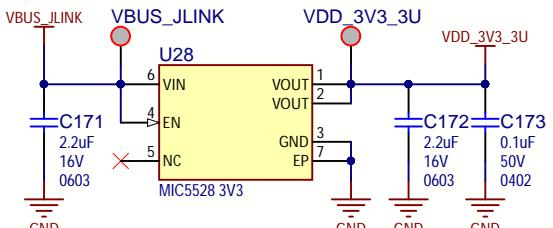


Drawn By: IGN	 MICROCHIP
Engineer: IGN	
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>
Sheet Title User Interaction	Variant Name Standard_WILC3000_DNP
Size B	Sch #: 03-11039
	Revision: 2
	Date: 9/20/2019 10:27:47 AM
	Sheet 12 of 13
	<i>Designed with</i> Altium Altium.com

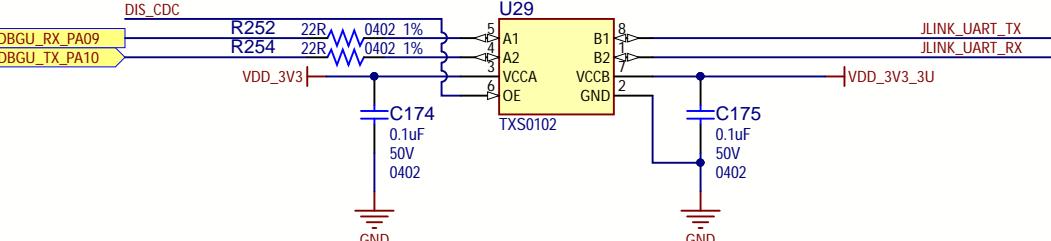
JLINK-OB INTERFACE



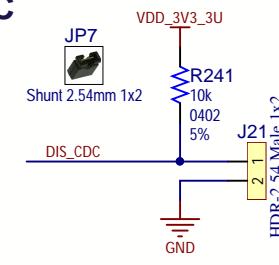
JLINK-OB POWER SUPPLY



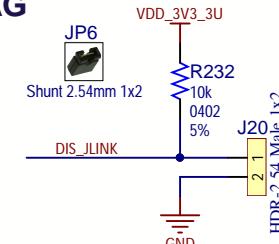
LEVEL SHIFTING UART - JLINK-OB



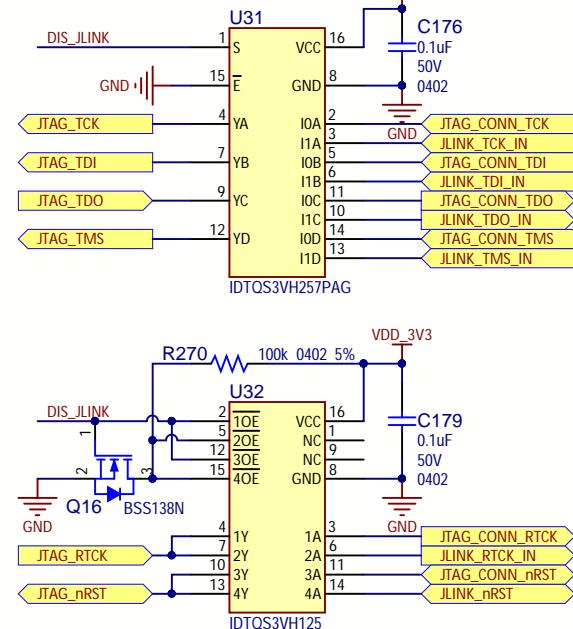
DIS JLINK CDC



DIS JLINK JTAG



JTAG Switch



Drawn By:
IGn

Engineer:
IGn

PartNumber:
DT100126

Project Title:
SAM9X60 Evaluation Kit

Sheet Title:
On Board JLINK

Variant Name:
Standard_WILC3000_DNP

Size: B

Sch #: 03-11039

Date: 9/20/2019 10:27:47 AM

Revision: 2

Sheet: 13 of 13

File: 13 - On Board JLINK.SchDoc

