2SA2039/2SC5706



ON Semiconductor®

http://onsemi.com

Bipolar Transistor

(-)50V, (-)5A, Low VCE(sat), (PNP)NPN Single TP/TP-FA

Applications

· DC / DC converter, relay drivers, lamp drivers, motor drivers, flash

Features

- · Adoption of FBET and MBIT processes
- · Low collector-to-emitter saturation voltage
- · Large current capacitance
- · High-speed switching · High allowable power dissipation

Specifications (): 2SA2039

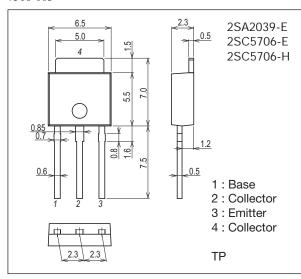
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		(-50)100	V
Collector-to-Emitter Voltage	VCES		(-50)100	V
Collector-to-Emitter Voltage	VCEO		(-)50	V
Emitter-to-Base Voltage	VEBO		(-)6	V
Collector Current	IC		(-)5	А
Collector Current (Pulse)	ICP		(-)7.5	А

Continued on next page.

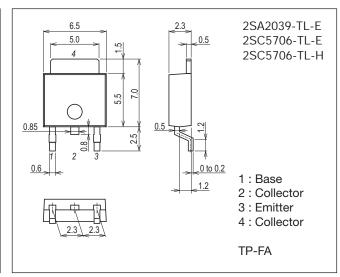
Package Dimensions unit: mm (typ)

7518-003



Package Dimensions unit: mm (typ)

7003-003

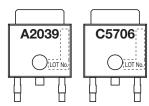


Product & Package Information

• Package: TP

• JEITA, JEDEC: SC-64, TO-251 • Minimum Packing Quantity: 500 pcs./bag

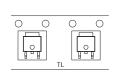
Marking (TP, TP-FA)

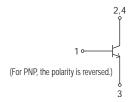


• Package : TP-FA

• JEITA, JEDEC: SC-63, TO-252 • Minimum Packing Quantity: 700 pcs./reel

Packing Type (TP-FA): TL Electrical Connection





Semiconductor Components Industries, LLC, 2013

September, 2013

Continued from preceding page.

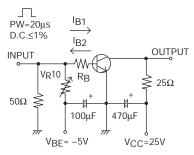
Parameter	Symbol	Conditions	Ratings	Unit
Base Current	IB		(-)1.2	А
C-ll-st-pDi-si-sti-s	De		0.8	W
Collector Dissipation	PC	Tc=25°C	15	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit	
Farantetei	Syllibol	Conditions	min	min typ ma		- Offit	
Collector Cutoff Current	ICBO	V _{CB} =(-)40V, I _E =0A			(-)1	μΑ	
Emitter Cutoff Current	IEBO	V _{EB} =(-)4V, I _C =0A			(-)1	μΑ	
DC Current Gain	hFE	V _{CE} =(-)2V, I _C =(-)500mA	200		560		
Gain-Bandwidth Product	fŢ	VCE=(-)10V, IC=(-)500mA		(360)400		MHz	
Output Capacitance	Cob	V _{CB} =(-)10V, f=1MHz		(24)15		pF	
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)1	I _C =(-)1A, I _B =(-)50mA		(-115)90	(-195)135	mV	
	V _{CE} (sat)2	I _C =(-)2A, I _B =(-)100mA		(-255)160	(-430)240	mV	
Base-to-Emitter Saturation Voltage	VBE(sat)	VCE=(-)2V, IB=(-)100mA		(-)0.89	(-)1.2	V	
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μA, I _E =0A	(-50)100			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =(-)100μA, R _{BE} =0Ω	(-50)100			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(-)1mA, R _{BE} =∞	(-)50			V	
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=(-)10μA, IC=0A	(-)6			V	
Turn-On Time	ton			(30)35		ns	
		See specified Test Circuit.		(230)300		ns	
				(15)20		ns	

Switching Time Test Circuit

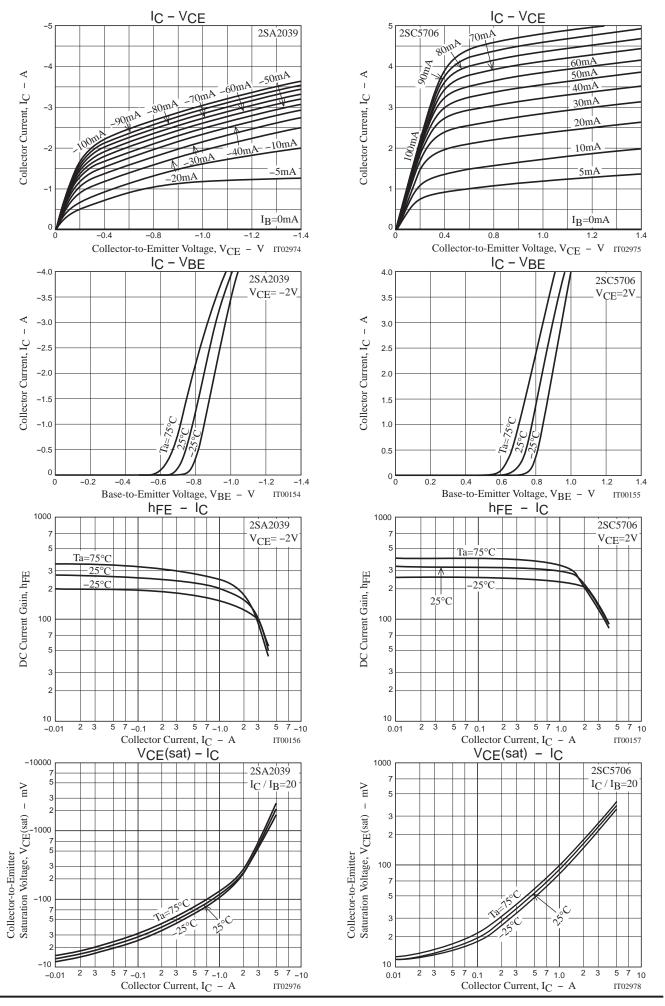


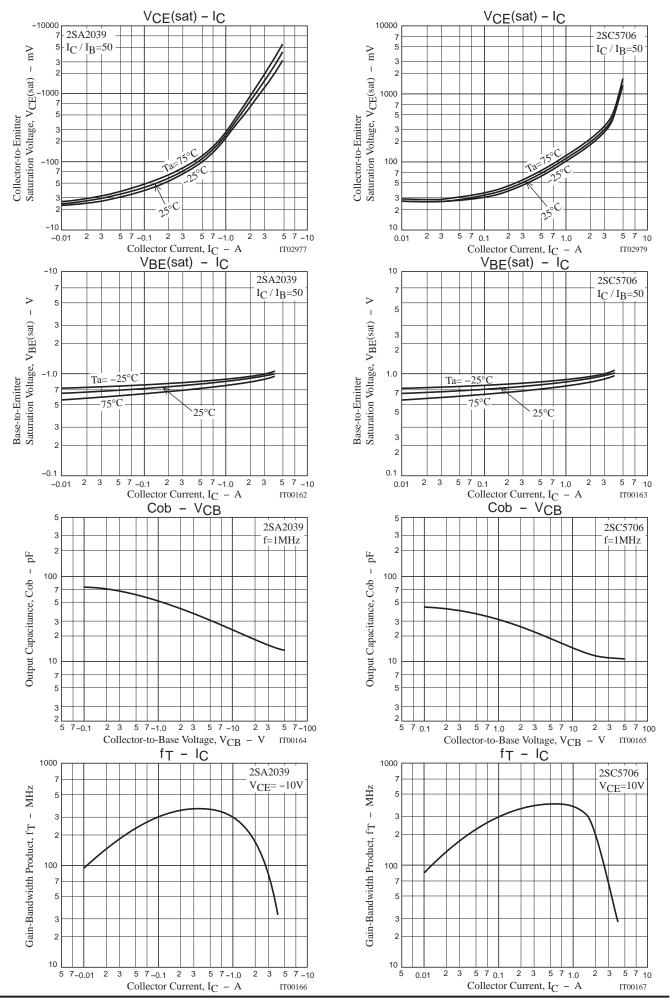
 $I_{C}=10I_{B1}=-10I_{B2}=1A$

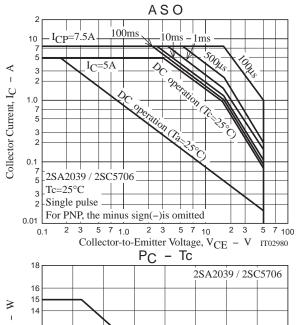
For PNP, the polarity is reversed.

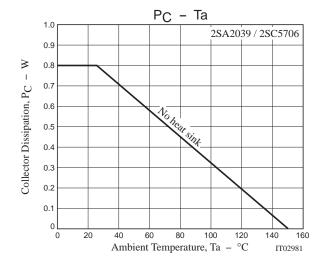
Ordering Information

Device	Package	Shipping	memo
2SA2039-E	TP	500pcs./bag	Pb Free
2SC5706-E	TP	500pcs./bag	Pb Fiee
2SC5706-H	TP	500pcs./bag	Pb Free & Halogen Free
2SA2039-TL-E	TP-FA	700pcs./reel	Pb Free
2SC5706-TL-E	TP-FA	700pcs./reel	PD Flee
2SC5706-TL-H	TP-FA	700pcs./reel	Pb Free & Halogen Free







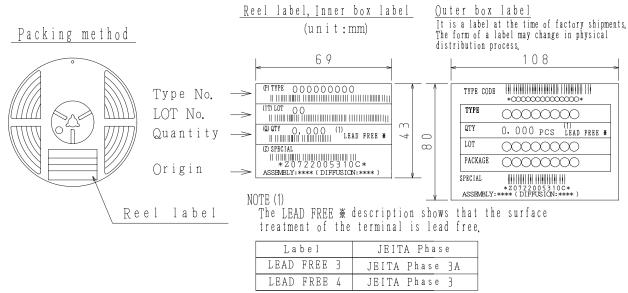


Taping Specification

2SA2039-TL-E, 2SC5706-TL-E, 2SC5706-TL-H

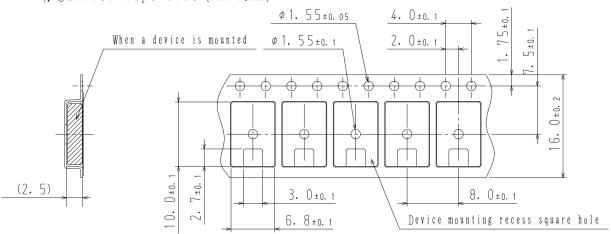
Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
TP-FA	TP	700	2, 100	12, 600	3 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210

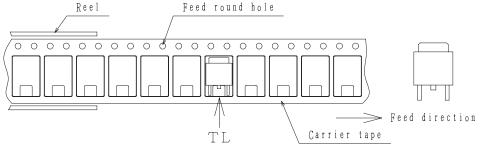


Taping configuration

1. Carrier tape size (unit:mm)



7. Device placement direction



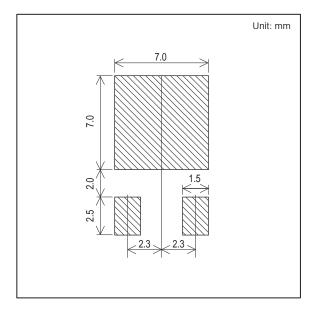
Those with one electrode terminal on the feed hole side....TL

Outline Drawing

2SA2039-TL-E, 2SC5706-TL-E, 2SC5706-TL-H

Mass (g) Unit 0.282 mm 6. 5±0. 2 2. 3±0. 2 5. O±0. 2 0. 5±0. 1 1. 5±0. 2 [*1] 7, 0±0, 3 5. 5±0. 2 LOT No. 1. 2±0. 3 0. 5±0. 15 L 0. 85±0. 2 2. 5±0. 3 3 0. 6±0. 2 1. 2±0. 3 0~0.2 2. 3±0.2 2. 3±0. 2 Pin 2 is idle pin with electrical designation only carried. *1:Lot indication

Land Pattern Example



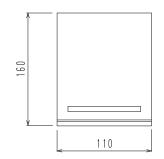
Bag Packing Specification

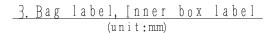
2SA2039-E, 2SC5706-E, 2SC5706-H

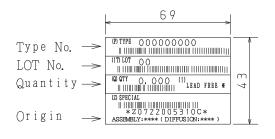
1. Packing Format

Package Name		Maximum Number of devices contained (pcs)				
1 4 0 11 4 5 0 1 1 4 4 11 0	Bag	Inner box	Outer box			
TP		B-1	A-1	A-2		
1 Γ	500	10,000	50,000	30,000		
	Packing fo		ormat (Dimensions:mm (external))			
		Inner box	Outer box			
		B-1	A-1	A-2		
		445×225×55	470×250×300	470×250×190		

2. Bag dimensions (unit:mm)





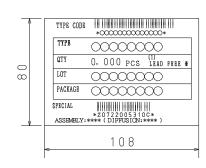


4. Outer box label (unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process,

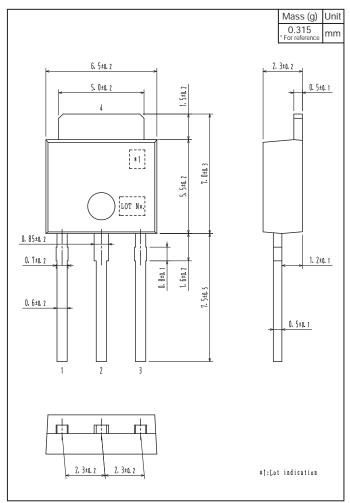


ĺ	Label			JEITA Phase
	LEAD	FREE	3	JEITA Phase 3A
ĺ	LEAD	FREE	4	JEITA Phase 3



Outline Drawing

2SA2039-E, 2SC5706-E, 2SC5706-H



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa