

MOS FIELD EFFECT TRANSISTOR 2SK3365

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK3365 is N-Channel MOS Field Effect Transistor designed for DC/DC converters application of notebook computers.

FEATURES

• Low on-resistance

 $R_{DS(on)1} = 14 \text{ m}\Omega \text{ (MAX.) (VGS} = 10 \text{ V, ID} = 15 \text{ A)}$

 $R_{DS(on)2} = 21 \text{ m}\Omega \text{ (MAX.) (Vgs} = 4.5 \text{ V, ID} = 15 \text{ A)}$

RDS(on)3 = 29 m Ω (MAX.) (VGS = 4.0 V, ID = 15 A)

- Low Ciss : Ciss = 1300 pF (TYP.)
- · Built-in gate protection diode

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3365	TO-251
2SK3365-Z	TO-252

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C)

Drain to Source Voltage (V _{GS} = 0 V)	VDSS	30	V
Gate to Source Voltage (V _{DS} = 0 V)	Vgss	±20	V
Drain Current (DC)	ID(DC)	±30	Α
Drain Current (Pulse) Note	ID(pulse)	±120	Α
Total Power Dissipation (Tc = 25 °C)	Рт	36	W
Total Power Dissipation (T _A = 25 °C)	P⊤	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to + 150	°C

Note PW \leq 10 μ s, Duty cycle \leq 1 %

THERMAL RESISTANCE

Channel to case	Rth(ch-C)	3.48	°C/W	
Channel to ambient	Rth(ch-A)	125	°C/W	

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

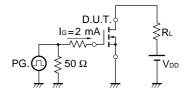


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

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CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 15 A		11.5	14	mΩ
	RDS(on)2	Vgs = 4.5 V, ID = 15 A		15.2	21	mΩ
	RDS(on)3	Vgs = 4.0 V, ID = 15 A		18	29	mΩ
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 15 A	8.0	16.0		S
Drain Leakage Current	IDSS	V _{DS} = 30 V, V _{GS} = 0 V			10	μΑ
Gate to Source Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Input Capacitance	Ciss	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		1300		pF
Output Capacitance	Coss			405		pF
Reverse Transfer Capacitance	Crss			190		pF
Turn-on Delay Time	td(on)	$I_D = 15 \text{ A}, V_{GS(on)} = 10 \text{ V}, V_{DD} = 15 \text{ V},$		37		ns
Rise Time	tr	$R_G = 10 \Omega$		500		ns
Turn-off Delay Time	td(off)			75		ns
Fall Time	tf			95		ns
Total Gate Charge	QG	ID = 30 A, VDD = 24 V, VGS = 10 V		25		nC
Gate to Source Charge	Qgs			4.5		nC
Gate to Drain Charge	Q _{GD}			7.0		nC
Body Diode forward Voltage	V _{F(S-D)}	IF = 30 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 30 A, VGS = 0 V		35		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		32		nC

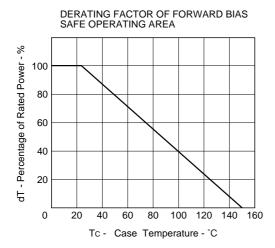
TEST CIRCUIT 1 SWITCHING TIME

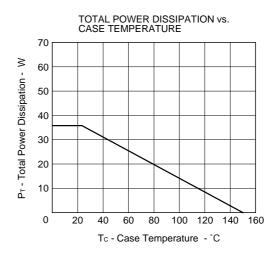
TEST CIRCUIT 2 GATE CHARGE



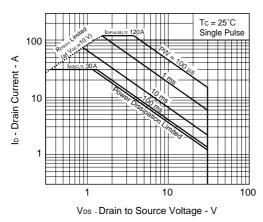


TYPICAL CHARACTERISTICS (TA = 25 °C)

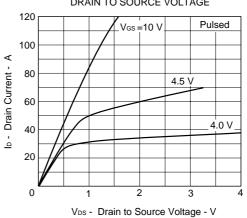


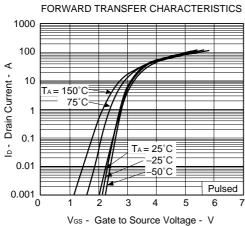






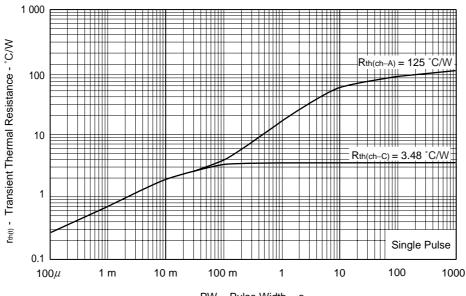
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE





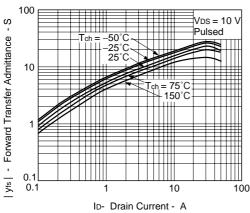
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TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

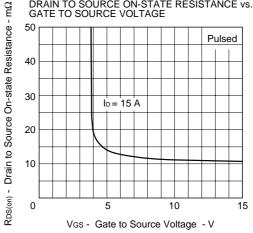


PW - Pulse Width - s

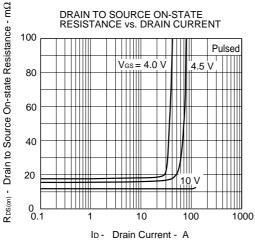


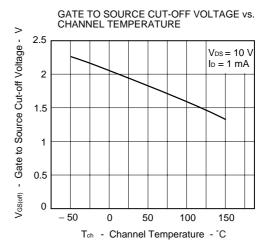


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

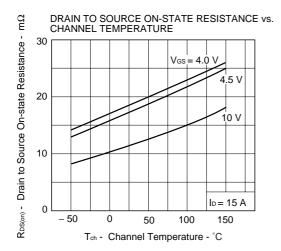


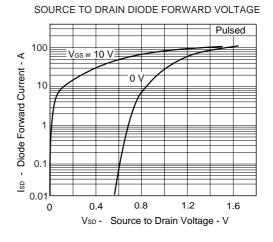
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

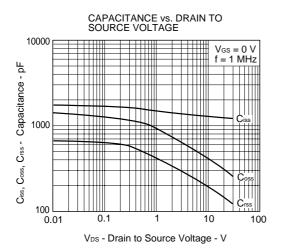


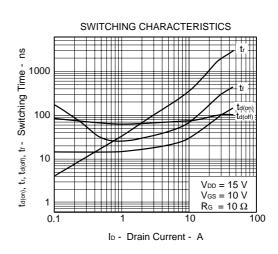


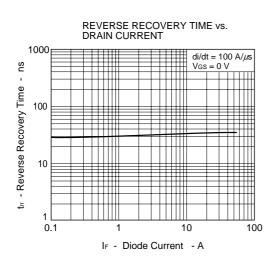


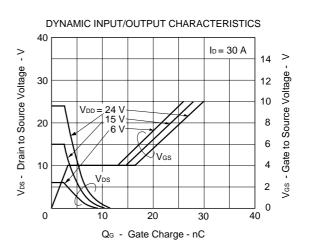








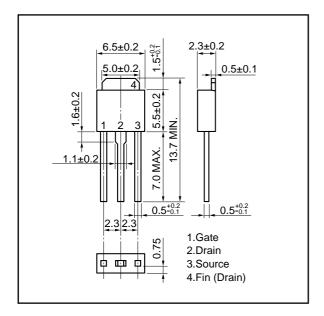




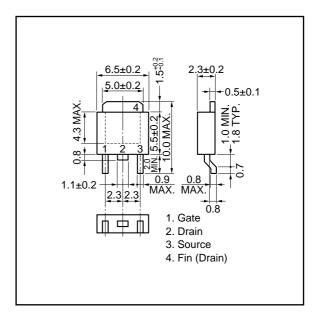


PACKAGE DRAWINGS (Unit: mm)

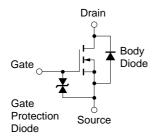
1) TO-251 (MP-3)



2) TO-252 (MP-3Z)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

[MEMO]

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