1. Description

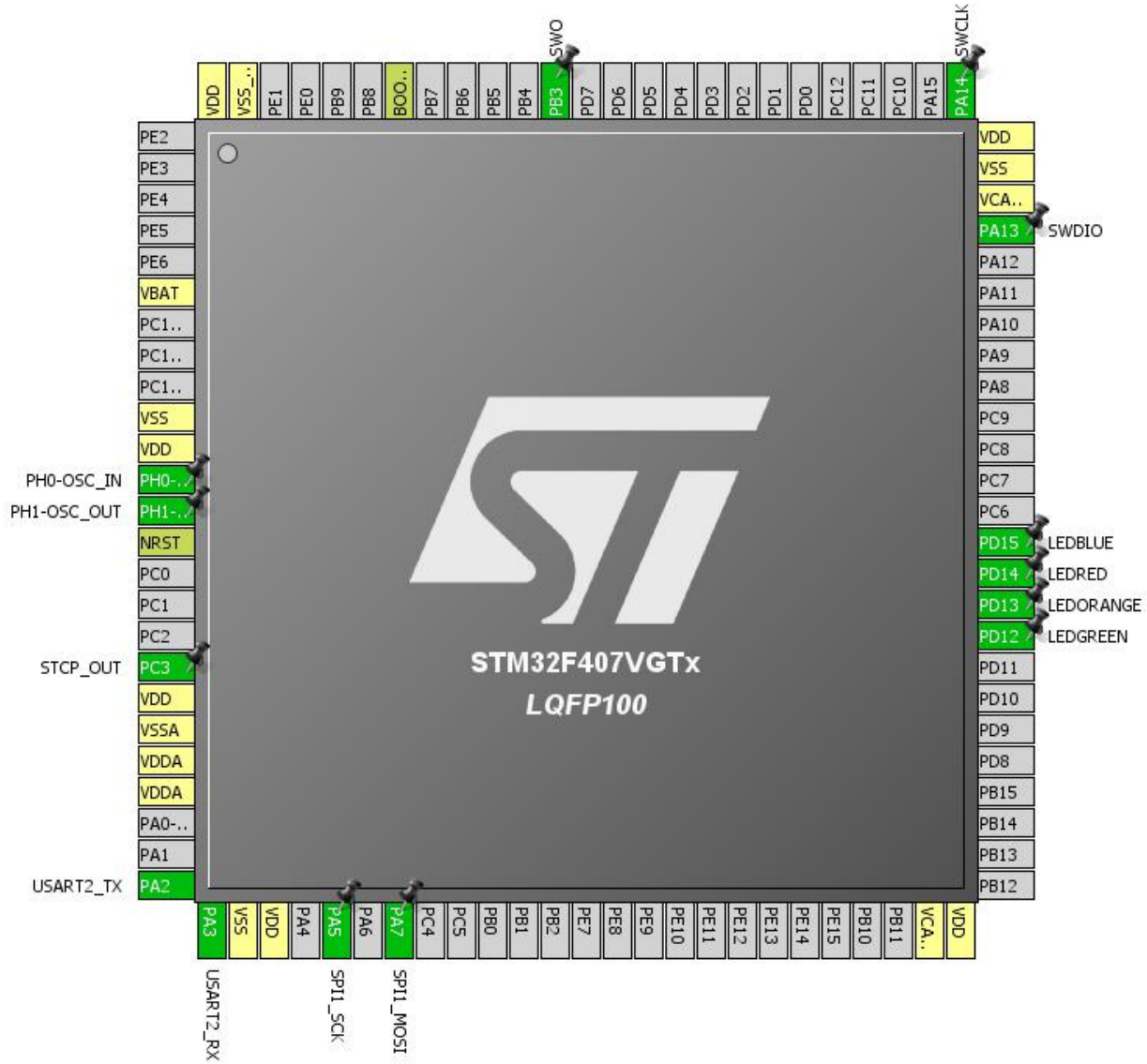
1.1. Project

Project Name	stmcubemx
Generated with:	STM32CubeMX 4.6.0
Date	02/19/2015

1.2. MCU

MCU Serie	STM32F4
MCU Line	STM32F407/417
MCU name	STM32F407VGTx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	PH0-OSC_IN
		RCC_OSC_OUT	PH1-OSC_OUT
SPI1	Mode: Transmit Only Master	SPI1_MOSI	PA7
		SPI1_SCK	PA5
SYS	Debug: SWD and Asynchronous Trace	SYS_JTMS-SWDIO	PA13
		SYS_JTCK-SWCLK	PA14
		SYS_JTDO-SWO	PB3
TIM1	Clock Source : Internal Clock	N/A	N/A
USART2	Mode: Asynchronous	USART2_RX	PA3
		USART2_TX	PA2

4. Pins Configuration

Pin	Pos	Function(s)	Label
PH0-OSC_IN	12	RCC_OSC_IN	PH0-OSC_IN
PH1-OSC_OUT	13	RCC_OSC_OUT	PH1-OSC_OUT
PC3 *	18	GPIO_Output	STCP_OUT
PA2	25	USART2_TX	
PA3	26	USART2_RX	
PA5	30	SPI1_SCK	
PA7	32	SPI1_MOSI	
PD12 *	59	GPIO_Output	LEDGREEN
PD13 *	60	GPIO_Output	LEDORANGE
PD14 *	61	GPIO_Output	LEDRED
PD15 *	62	GPIO_Output	LEDBLUE
PA13	72	SYS_JTMS-SWDIO	SWDIO
PA14	76	SYS_JTCK-SWCLK	SWCLK
PB3	89	SYS_JTDO-SWO	SWO

* The pin is affected with an I/O function

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F4
Line	STM32F407/417
MCU	STM32F407VGTx
Datasheet	022152_Rev5

5.2. Parameter Selection

Temperature	25
Vdd	3.3

6. Software Project

6.1. Project Settings

Name	Value
Project Name	stmcubemx
Project Folder	E:\DATA\DEV\ARM_WORKSPACES\ECLIPSE\TESTS\test0010_OK_timerIRQ_
Toolchain / IDE	TrueSTUDIO 4.3.1
Firmware Package Name and Version	STM32Cube FW_F4 V1.4.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	No
Set all free pins as analog (to optimize the power consumption)	No

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed