

# Referenciranje

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22. decembar 2025.

## 1 Uvod

Ovde ću citirati [1].

## 2 Zaključak

Ovde citiram referencu reda radi, [2].

## Literatura

- [1] J. A. Tierno, A. V. Rylyakov, and D. J. Friedman, “A wide power supply range, wide tuning range, all static CMOS all digital PLL in 65 nm SOI,” *IEEE Journal of Solid-State Circuits*, vol. 43, no. 1, pp. 42–51, 2008, doi: 10.1109/JSSC.2007.910966.
- [2] Ovo je referenca reda radi.