CS61C Spring 2018 Discussion 2 – C Memory Management & RISC-V

1 C Memory Management

1. Match the items on the left with the memory segment in which they are stored. Answers may be used more than once, and more than one answer may be required.

1. \$	Static variables	Α.	Code
2. I	Local variables	D	C+ - +: -
3. (Global variables	Б.	Static
4. (Constants	С.	Heap
5. I	Machine Instructions	ъ	- 1
6. I	Result of malloc()	D. Stack	
7. 5	String Literals		

- 2. Write the code necessary to properly allocate memory (on the heap) in the following scenarios
 - 1. An array arr of k integers
 - 2. A string str containing p characters
 - 3. An $n \times m$ matrix mat of integers initialized to zeros
- 3. What is wrong with the C code below?

```
int* pi = malloc(314 * sizeof(int));
if(!raspberry) {
  pi = malloc(1 * sizeof(int));
}
return pi;
```

4. Write code to prepend (add to the start) to a linked list, and to free/empty the entire list. struct ll_node { struct ll_node* next; int value; }

<pre>void prepend(struct ll_node** lst, int val)</pre>	<pre>void free_ll(struct ll_node** lst)</pre>

Note: *1st points to the first element of the list, or is NULL if the list is empty.

2 Data Structures in C

}

In this question, we will implement a array-based stack of integers in C. The stack will be represented by the struct below.

```
struct stack {
                         // Number of element in stackArray
        int size;
                         // Index of the array that is the top of the stack
        int topIndex;
        int *stackArray; // Array holding the elements of the stack
    }
Implement the functions below.
    // Create a new stack with the given array size
    struct stack *init_stack(int size) {
    }
    // Add the given element to the stack. Resize by doubling the array size if full.
    // Return 1 on success, 0 on failure. stk should be unchanged on failure.
    int push(int x, struct stack* stk) {
    }
    // Remove the top element from the stack. Return 0 if empty.
    int pop(struct stack *stk) {
```

3 RISC-V Intro

- 1. Assume we have an array in memory that contains int* arr = {1,2,3,4,5,6,0}. Let the value of arr be a multiple of 4 and stored in register s0. What do the snippets of RISC-V code do? Note that these snippits all run immediately after each other (snippet a) runs, then snippet b) and so on).
 - a) lw t0, 12(s0)
 - b) slli t1, t0, 2
 add t2, s0, t1
 lw t3, 0(t2)
 addi t3, t3, 1
 sw t3, 0(t2)
 - c) lw t0, 0(s0)
 xori t0, t0, 0xFFF
 addi t0, t0, 1
- 2. What are the instructions to branch to label on each of the following conditions? The only branch instructions you may use are beq and bne.

s0 < s1	s0 <= s1	s0 > 1
		_

4 Translating between C and RSIC-V

Translate between the C and RISC-V code. You may want to use the RISC-V Reference Card for more information on the instruction set and syntax. In all of the C examples, we show you how the different variables map to registers – you don't have to worry about the stack or any memory-related issues. You may assume all registers are initialized to zero.

```
\mathbf{C}
                                                 RISC-V
// s0 -> a, s1 -> b
// s2 -> c, s3 -> z
int a = 4, b = 5, c = 6, z;
z = a + b + c + 10;
// s0 -> int * p = intArr;
// s1 -> a;
*p = 0;
int a = 2;
p[1] = p[a] = a;
// s0 -> a, s1 -> b
int a = 5, b = 10;
if(a + a == b) {
    a = 0;
} else {
    b = a - 1;
                                                     addi s0, x0, 0
                                                     addi s1, x0, 1
                                                     addi t0, x0, 30
                                                 loop:
                                                     beq s0, t0, exit
                                                     add s1, s1, s1
                                                     addi s0, s0, 1
                                                     jal x0, loop
                                                 exit:
// s0 -> n, s1 -> sum
// assume n > 0 to start
for(int sum = 0; n > 0; n--) {
  sum += n;
```