

Connected Embedded Rust

A Survey of system software and network stacks in Rust

Elena Frank
Freie Universität Berlin

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Embedded Rust

- Rust Programming Language
- Hardware Abstraction
- Asynchronous Code

Survey of System Software

- Concurrency Frameworks
- Tock
- Hubris

Networking stacks

- smoltcp
- Hubris' usage of smoltcp
- Tock's Network Stack

Summary

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Summary

- ▶ Popular Alternative to C for low-level Systems Programming
- ▶ Focus on Reliability and Performance
- ▶ Compiler enforced Memory, Thread and Type Safety

Ownership and Mutability

C:

```
struct item {
    int n;
};

void inc_if_zero(struct item *var) {

    if (var->n == 0) {
        var->n += 1;
    }
}

void foo(void) {
    struct item var = { n: 0 };

    struct item *ref1 = &var;
    struct item *ref2 = &var;

    create_thread(inc_if_zero, ref1);
    create_thread(inc_if_zero, ref2);
    return;
}
```

```

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    create_thread(inc_if_zero, ref1);
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    return;
}

```

```
struct Item {
    n: u8
}

fn inc_if_zero(var: &Item) {

    if var.n == 0 {
        var.n += 1;
    }
}

fn foo() {
    let var = Item { n: 0 };

    let ref1 = &var;
    let ref2 = &var;

    thread::spawn(move || inc_if_zero(ref1));
    thread::spawn(move || inc_if_zero(ref2));
}
```

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    thread::spawn(move || inc_if_zero(ref1));
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}
```

```
error[E0594]: cannot assign to 'item.n', which is behind a '&' reference
-> src/main.rs:7:9
5 | fn increase_if_zero(var: &Item) {
  |                               --- help: consider changing this to be a mutable reference: '&mut Item'
6 |     if var.n == 0 {
7 |         var.n += 1;
  |         ~~~~~~ 'var' is a '&' reference, so the data it refers to cannot be written
```

```
struct item {
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void inc_if_zero(struct item *var) {

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    }
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    create_thread(inc_if_zero, ref1);
    create_thread(inc_if_zero, ref2);
    return;
}
```

```
struct Item {
    n: u8
}

fn inc_if_zero(var: &mut Item) {
    if var.n == 0 {
        var.n += 1;
    }
}

fn foo() {
    let mut var = Item { n: 0 };

    let ref1 = &mut var;
    let ref2 = &mut var;

    thread::spawn(move || inc_if_zero(ref1));
    thread::spawn(move || inc_if_zero(ref2));
}
```


Ownership and Mutability

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Rust:

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struct Item {
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    let mut var = Item { n: 0 };

    let ref1 = &mut var;
    let ref2 = &mut var;

    thread::spawn(move || inc_if_zero(ref1));
    thread::spawn(move || inc_if_zero(ref2));
}
```

error[E0499]: cannot borrow 'var' as mutable more than once at a time

-> src/main.rs:17:16

```
16 |     let ref1 = &mut var;
    |               ----- first mutable borrow occurs here
17 |     let ref2 = &mut var;
    |               ~~~~~ second mutable borrow occurs here
```

C:

```
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void foo(void) {
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    return;
}
```

Rust:

```
struct Item {
    n: u8
}

fn inc_if_zero(var: Mutex<Item>) {
    let mut var = var.lock().unwrap();
    if var.n == 0 {
        var.n += 1;
    }
}

fn foo() {
    let var = Mutex::new(Item { n: 0 });

    let ref1 = &var;
    let ref2 = &var;

    thread::spawn(move || inc_if_zero(ref1));
    thread::spawn(move || inc_if_zero(ref2));
}
```

Generics and Traits

```
pub trait Write<Word> {
    type Error;

    fn write(&mut self, word: Word) -> Result<(), Self::Error>;
    fn flush(&mut self) -> Result<(), Self::Error>;
}

fn write_all<S>(
    serial: &mut S,
    buffer: &[u8]
) -> Result<(), S::Error>
where
    S: Write<u8>
{
    for &byte in buffer {
        block!(serial.write(byte))?;
    }

    Ok(())
}

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```

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Hardware Abstraction Layers

Micro-architecture Layer:

- ▶ Processor-specific logic
- ▶ Processor calls, timer management, interrupt handling, ...
- ▶ Example: ARM Cortex-M Processors

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Peripheral Access Layer:

- ▶ Provide access to a chips memory-mapped registers and periphery
- ▶ GPIO, SPI, I2C, UART, ..
- ▶ Example: Nordic Conductor nRF52805 SoC

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- ▶ Processor-specific logic
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- ▶ Provide access to a chips memory-mapped registers and periphery
- ▶ GPIO, SPI, I2C, UART, ..
- ▶ Example: Nordic Conductor nRF52805 SoC

Board Layer:

- ▶ Configure underlying layers for one concrete Board
- ▶ Abstracts Hardware details, configures periphery & assigns drivers, ...
- ▶ Example: STM32 Nucleo-32 Board

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Challenge:

- ▶ Embedded Systems do a lot of I/O
- ▶ I/O is asynchronous => result not immediately available
- ▶ Program execution should continue with other work in the meantime

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```
pub enum Poll<T> {  
    Ready(T),  
    Pending,  
}  
  
pub trait Future {  
    type Output;  
    fn poll(...) -> Poll<Self::Output>;  
}
```

Challenge:

- ▶ Embedded Systems do a lot of I/O
- ▶ I/O is asynchronous => result not immediately available
- ▶ Program execution should continue with other work in the meantime

```
pub enum Poll<T> {          pub trait Future {  
    Ready(T),               type Output;  
    Pending,                fn poll(...) -> Poll<Self::Output>;  
}                            }
```

- ▶ The *await* keyword allows blocking the current execution until a Future is ready
- ▶ Functions that use *await* themselves become a Future and must be annotated as *async*

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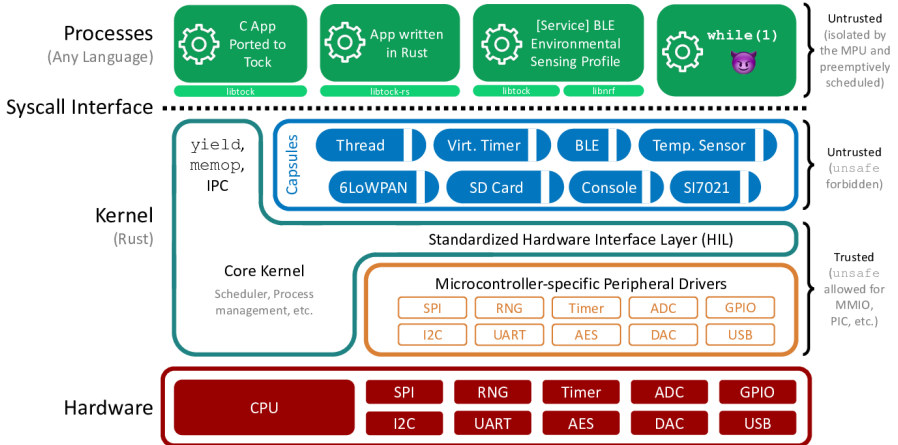
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Summary

- ▶ Goal: enable secure multi-programming on embedded system
- ▶ Monolithic kernel: units sand-boxed via Rust type system
- ▶ Supports dynamic loading of processes and memory management
- ▶ IPC implemented as Client-Server communication
- ▶ Own Hardware Interface-Layer



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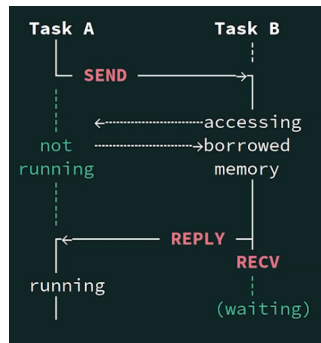
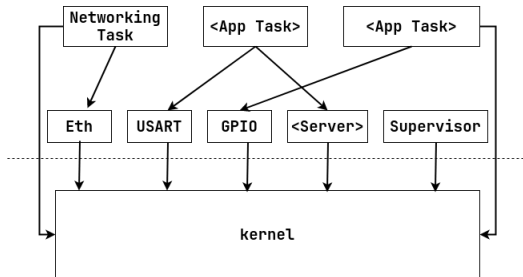
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Summary

- ▶ Very small hubris kernel + collection of tasks
- ▶ Tasks: Separately-compiled Programs & Drivers
 - ▶ Unprivileged processor mode
 - ▶ Isolated in memory
 - ▶ No dynamic creation of tasks; fixed priorities
- ▶ "Aggressively static" for all allocatable or routable resources
- ▶ Synchronous IPC: Sender is blocked until receiver responded



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Summary

- ▶ Sockets
 - ▶ Supported: raw Sockets, ICMP, UDP and TCP
 - ▶ Implementation of socket logic: buffering, packet construction, ...
 - ▶ Sans-IO: no actual I/O done by the socket
- ▶ *Device* trait
 - ▶ Methods for interacting with the actual device
 - ▶ User-provided
- ▶ Interfaces
 - ▶ Supported: Ethernet, IP, IEEE 802.15.4 + 6LoWPAN
 - ▶ Takes care of physical addressing, neighbour discovery, sending of control packets
 - ▶ Implemented as state-machine

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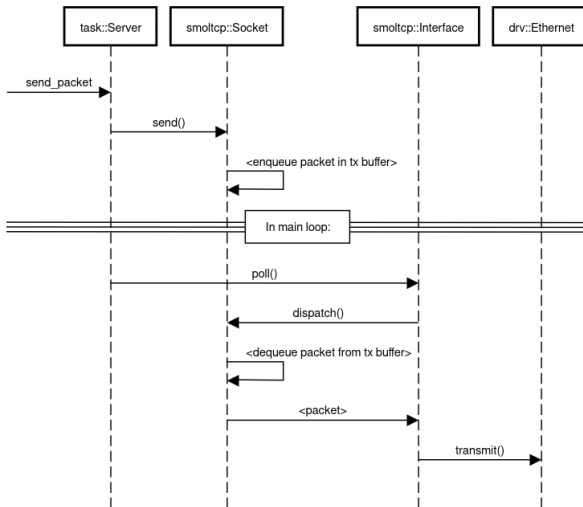
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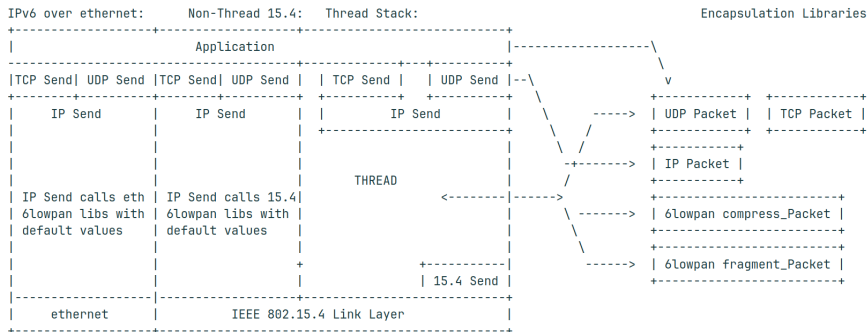
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- ▶ Rust offers powerful primitives for building secure and reliable low-level systems
- ▶ The type system can be leveraged in very different designs

- [1] [Cliff L. Biffle. *On Hubris and Humility: developing an OS for robustness in Rust.*](#)
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- [2] [Embassy. *https://github.com/embassy-rs/embassy.*](#) June 2023.
- [3] [Real-Time Interrupt-driven Concurrency \(RTIC\).](#)
<https://github.com/rtic-rs/rtic>. June 2023.
- [4] [Tock. *https://github.com/tock/tock.*](#) June 2023.
- [5] [embedded-hal. *https://crates.io/crates/embedded-hal.*](#) v0.2.7.