

YANLIANG LI

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EDUCATION

University of California, Santa Cruz *Expected Graduation: June 2018*
Major: Bachelor of Science in Computer Engineering, Concentration: Digital Hardware
Awards: Undergraduate Dean's Award (September 2014 through June 2017), Dean's Honor (Fall 2014&Fall 2015)
Course Work: Logic Design with Verilog, Computer Architecture, Analog Electronics, Electronic Circuits, Signals And Systems, Data Structures

EXPERIENCE

Radio Sports Association 8/5/2017 - 9/5/2017(40hrs/week) & 8/1/2014 - 9/1/2014
Coach of the Guangzhou Radio Direction Finding team *Guangzhou, China*

- Instructed 50+ students in the circuit diagram of the PJ-80 radio receiver
- Illustrated using the receiver to locate the transmitter hidden in the forest
- Coached 200+ students training for RDF and led 20+ students to win National Championship
- Won 1st Place (6 years straight) in China national competition

Electronic Circuits 4/3/2017 - 6/15/2017 & 9/15/2016 - 12/15/2016

- Designed the CMOS circuit with PMOS and NMOS transistors to create an inverter
- Sinusoidal Steady-State Filters: Investigated the frequency domain characteristics of steady state linear wave filters and inspected salient characteristics
- Second Order Switched RLC Circuits: Evaluated transient voltage and current relationships for switched second order RLC circuits

Logic Design with Verilog 1/6/2017 - 6/15/2017

- Designed and verified large-scale systems using the Verilog Hardware Description Language with emphasis on verification and high-frequency ASIC/FPGA targets
- Octopong (Basys 3 board and VGA monitor): Played in a rectangular region with blue borders, one paddle that moves up and down, and ball(s)
- Turkey Traffic (Basys 3 board) : Using signals from push buttons as two infrared light sensors inputs to count the number of objects crossing the two sensors in both directions

University of California, Santa Cruz 3/28/2016 - 6/14/2016 & 9/17/2016 - 12/9/2016
Linear Algebra Tutor *Santa Cruz, CA*

- Provided academic support for more than 100+ fellow undergraduate students in linear algebra
- Maintained regular departmental request as specified by instructors

Running 7/31/2016 & 7/23/17

- Won San Francisco Full Marathon Finisher
- Earned 40 for 40 challenge metal

Conservation Volunteers AUS 6/10/2015 - 7/10/2015
Environment Conservation Volunteer *Hamilton & Naracoorte, Australia*

- Responsible for planting 300+ wind-blocking trees and 100+ Stringybark trees for the Red-Tailed Black Cockatoo

SKILLS

Programming Languages Verilog, SystemVerilog, Assembly, C, Matlab, Java, Git
Languages English (Professional), Cantonese (Native), Mandarin (Native), French (Limited)