

Summary

2	1	Introduction	3
3	2	Pixel detectors	5
4	2.1	Signal formation	5
5	2.2	CCDs	7
6	2.3	Hybrid pixels	7
7	2.4	CMOS MAPS and DMPAS	7
8	2.4.1	DMAPS: large and small fill factor	9
9	2.4.2	A modified sensor	10
10	2.5	Analog front end	11
11	2.5.1	Preamplifier	12
12	2.6	Readout logic	12
13	3	Use of pixel detectors	16
14	3.1	Tracking in HEP	16
15	3.1.1	Hybrid pixels at LHC and at SuperKEKB	17
16	3.1.2	First attempts to MAPS	18
17	3.2	Other applications	20
18	3.2.1	Applicability to FLASH radiotherapy	20
19	4	TJ-Monopix1	24
20	4.1	The sensor	26
21	4.2	Front end	27
22	4.2.1	ALPIDE-like	27
23	4.3	Readout logic	29
24	5	Arcadia-MD1	32
25	5.1	The sensor	32
26	5.2	Readout logic and data structure	33
27	5.2.1	Matrix division and data-packets	33
28	6	Characterization	36
29	6.1	TJ-Monopix1 characterization	36
30	6.1.1	Threshold and noise: figure of merit for pixel detectors	36
31	6.1.2	Linearity of the ToT	39
32	6.1.3	Calibration of the ToT	40
33	6.1.4	PMOS flavor: changing the bias	44
34	6.1.5	Measurements with radioactive sources	45
35	6.1.6	Dead time measurements	46
36	6.2	ARCADIA-MD1 characterization	47

37	7 Test beam measurements	49
38	7.1 Apparatus description	49
39	7.1.1 Accelerator	49
40	7.1.2 Mechanical carriers	50
41	7.2 Measurements	51
42	A Pixels detector: a brief overview	54
43	A.1 Radiation damages	54
44	Bibliography	57
45	Characterization of monolithic CMOS pixel sensors for charged particle detectors and for high	
46	intensity dosimetry	

⁴⁷ Chapter 1

⁴⁸ Introduction

⁴⁹ Pixel detectors, members of the semiconductor detector family, have significantly been used at the
⁵⁰ accelerator experiments for energy and position measurement. Because of their dimension (today
⁵¹ $\sim 30 \mu\text{m}$ or even better) and their spatial resolution ($\sim 5\text{-}10 \mu\text{m}$), with the availability of technology
⁵² in 1980s they proved to be perfectly suitable for vertex detector in the inner layer of the detector.

⁵³ Despite the monolithic pixels came up with CCDs, invented in 1969 and fastly used in cameras,
⁵⁴ their usage had to wait for microelectronics developement: in MAPS device the readout electronics
⁵⁵ is build on the pixel's area, then the pixel dimension is limited by the dimension of transistors. This
⁵⁶ constraint favoured the usage in physics experiment of hybrid pixels, which currently constitute
⁵⁷ the state-of-art for large scale pixel detector. These ones are made by two different wafer each one
⁵⁸ containing or the sensor or the ASIC, which are after joined together through microconnection.
⁵⁹ This structure allows a separate optimization for the two components and makes hybrid pixels
⁶⁰ flexible and versatile.

⁶¹ Requirement imposed by accelerator are stringent and they will be even more with the increase
⁶² of luminosity in terms of radiation hardness, efficiency and occupancy, time resolution, material
⁶³ budget and power consumption. For this reason experiments (as ATLAS, CMS, BelleII) began to
⁶⁴ look at the more innovative and well-performing monolithic active pixels (MAPS) as perspective for
⁶⁵ their future upgrades.

⁶⁶ Che condiziona la risoluzione e l'efficienza di ricostruzione della sua traccia, e consumi del
⁶⁷ detector, sono diventati sempre più rilevanti; molti esperimenti (ATLAS, CMS, BelleII,...) stanno
⁶⁸ infatti valutando la possibilità di sostituire gli ibridi con i MAPS, che per i tempi precedenti offrono
⁶⁹ prestazioni migliori, a scapito di tempi di lettura mediamente più lunghi, vista anche la positiva
⁷⁰ esperienza di ALICE ad LHC, primo esperimento ad introdurre un detector a pixel monolitico.

⁷¹ During my thesys I studied and characterised two monolithic active pixel chips, TJ-Monopix1
⁷² and MD1; these devices, that are still prototypes, have been conceived and designed for physics
⁷³ experiments at colliders, space experiments and also for medical applications.

⁷⁴ il primo, TJ-Monopix1, è un prototipo di un modello selezionato per l'upgrade di Belle II
⁷⁵ durante il LSD nel 2025 (il chip finale si chiamerà OBELIX e avrà come sensore TJ-Monopix2,
⁷⁶ successore di Monopix1); il secondo chip è stato progettato da ARCADIA che potrà avere, nelle
⁷⁷ versioni future, applicazioni in fisica medica, in esperimenti nello spazio e ai collider.

⁷⁸ Le differenze principali tra i due chip risiedono nel segnale fornito in output (Monopix fornisce il
⁷⁹ tempo sopra soglia dell'impulso triangolare, proporzionale alla carica rilasciata nel sensore, mentre
⁸⁰ arcadia fornisce un segnale puramente digitale), nella sequenza di readout dei pixel (monopix ha
⁸¹ una lettura puramente sequenziale di tipo "column drain") mentre arcadia ha una lettura più
⁸² moderna che consente di poter aggregare dati durante la trasmissione (ad esempio nel caso di
⁸³ formazione di cluster e creazione di hti su pixel adiacenti).

⁸⁴ I performed a threshold and noise characterization ($\sim 400 \text{ e}^-$ and $\sim 15 \text{ e}^-$) of TJ-Monopix1 in
⁸⁵ order

⁸⁶ Tra i test con Monopix1 ho effettuato una caratterizzazione in soglia ($\sim 400 \text{ e}^-$) e rumore (\sim
⁸⁷ 15 e^-) al fine di visualizzare la dispersione di questi valori sulla matrice; per poter minimizzare la

88 dispersione sulla matrice e avere una più uniforme selezione della soglia (che è globale su tutta
89 la matrice), le versioni successive di TJ-Monopix1 includono e includeranno la possibilità di fare
90 piccole correzioni (3 bit per pixel vengono allocati in Monopix2) di quest'ultima pixel per pixel.
91 Per poter fornire le misure dei segnale fornito, tempo sopra soglia ToT, in elettroni, che assieme
92 alle lacune vengono create dal passaggio della particella incidente e che quindi sono la quantità
93 fisica "importante" nella misura, è stata necessaria una calibrazione assoluta dell'oggetto. Per
94 quest'ultima e per altri test ?? mi sono servita di sorgenti radiattive come il ferro 55 (emissione di
95 un fotone gamma a 5.9 kev e dello stronio 90 il cui spettro dell'elettrone emesso ha un end point
96 a x) e dei cosmici. Inoltre ho partecipato ai test di Monopix1 su fascio: abbiamo testato il chip in
97 una modalità diversa da quella per cui è stato progettato (tracking) e più simile al funzionamento
98 delle CCD, in cui non si cerca di distinguere il singolo elettrone incidente ma si integra in un
99 singolo segnale di output la carica rilasciata da più elettroni incidenti. Il fascio utilizzato (elettroni
100 da 7-9 MeV) è un fascio ad altissima intensità e verrà utilizzato per fare ricerca su radioterapia
101 ad alto rate (l'acceleratore è in grado di rilasciare dosi -con riferimento in acqua- fino a 40 Gy/s,
102 corrispondenti ad un numero di particelle di ..). Per quanto riguarda, invece, le misure sul chip
103 MD1, ho partecipato ai test elettrici e sul front end di un prototipo non ancora completamente
104 funzionante. Un nuovo chip dovrebbe arrivare nei prossimi giorni a Pisa.

¹⁰⁵ **Chapter 2**

¹⁰⁶ **Pixel detectors**

¹⁰⁷ I pixel detector fanno parte della famiglia dei detector a semiconduttore e il loro funzionamento si
¹⁰⁸ basa sulla creazione di coppie elettrone lacuna all'interno del bulk. Dalla creazione della particella
¹⁰⁹ incidente di queste coppie e facendole driftare attraverso l'applicazione di un campo elettrico, si
¹¹⁰ ottiene quindi un segnale all'interno del rivelatore correlabile all'energia della particella incidente.
¹¹¹ Il campo elettrico applicato, lo spessore della zona di svuotamento, le modalità con cui il
¹¹² segnale viene processato e trasmesso all'esterno del rivelatore sono caratteristiche specifiche del
¹¹³ tipo di chip. In questo capitolo tratterò dunque i principali tipi di rivelatori a pixel, sofferandomi
¹¹⁴ in particolare sui pixel monoliti.

¹¹⁵ **2.1 Signal formation**

¹¹⁶ When a charge particle passes through a pixel and loses energy by ionization a part of that
¹¹⁷ energy is used to generate electron-hole pairs (another part is used for other processes, as the
¹¹⁸ lattice excitation) which are then separated by the electric field and collected at their respectively
¹¹⁹ electrodes (*p* for holes and *n* for electrons)¹; by the drift of these charges, a signal i_e is generated
¹²⁰ on the electrode *e* as stated by the Shockley-Ramo's theorem:

$$i_e(t) = -q v(t) E_{WF,e} \quad (2.1)$$

¹²¹ where $v(t)$ is the instantaneous velocity of the charge q and E_{WF} is the weighting field, that is the
¹²² field obtained biasing the electrode *e* with 1V and all the others with 0V. The drift velocity of the
¹²³ charge depends on the electric field and on the mobility of the particle:

$$v = \mu(E) E \quad (2.2)$$

¹²⁴ where $\mu(E)$ is a function of the electric field and is linear with E only for small E : at higher values
¹²⁵ the probability of interactions with optical phonons increases and the mobility drops and this leads
¹²⁶ to an independence of the velocity from the electric field (fig. ??).

¹²⁷ The average energy needed to create a pair at 300 K in silicon is $w_i = 3.65$ eV, that is more
¹²⁸ than the mean ionization energy because of the interactions with phonon, since for a minimum
¹²⁹ ionizing particle (MIP) the most probable value (MPV) of charge released in the semiconductor is
¹³⁰ 0.28 keV/ μ m, hence the number of electrons-vacuum pairs is:

$$\langle \frac{dE}{dx} \rangle \frac{1}{w_i} \sim 80 \text{ e}/\text{h} \sim \frac{1.28 \cdot 10^{-2} fC}{\mu m} \quad (2.3)$$

¹³¹ CON UN'INCERTEZZA CHE È RADICE DI N; ED EVENTUALMENTE SI AGGIUNGE IL
¹³² FATTORE DI FANO NEL CASO DI ASSORBIMENTO TOTALE. IL FATTORE DI FANO È
¹³³ 0.115 NEL SILICIO. ecc

¹Even if in principle both the electrode can be used to read a signal, for pixel detectors, where the number of channel and the complexity of readout are high, only one is actually used. In strip and pad detectors, instead, is more common a dual-side readout

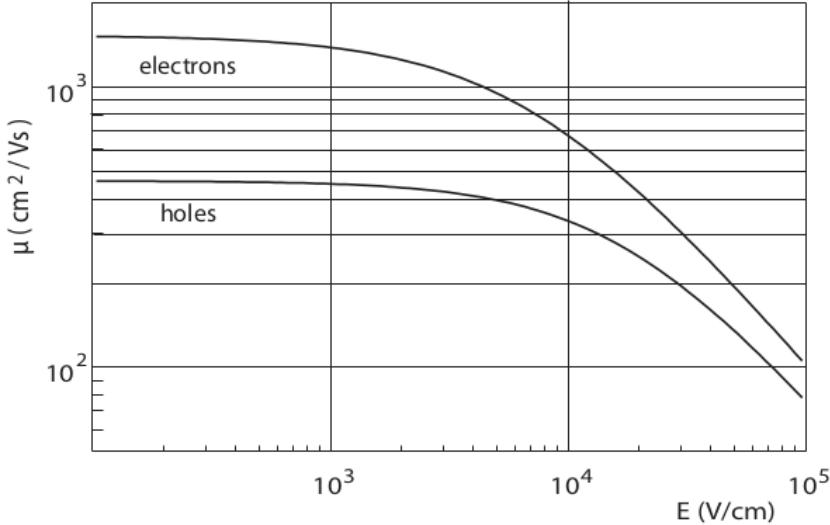


Figure 2.1

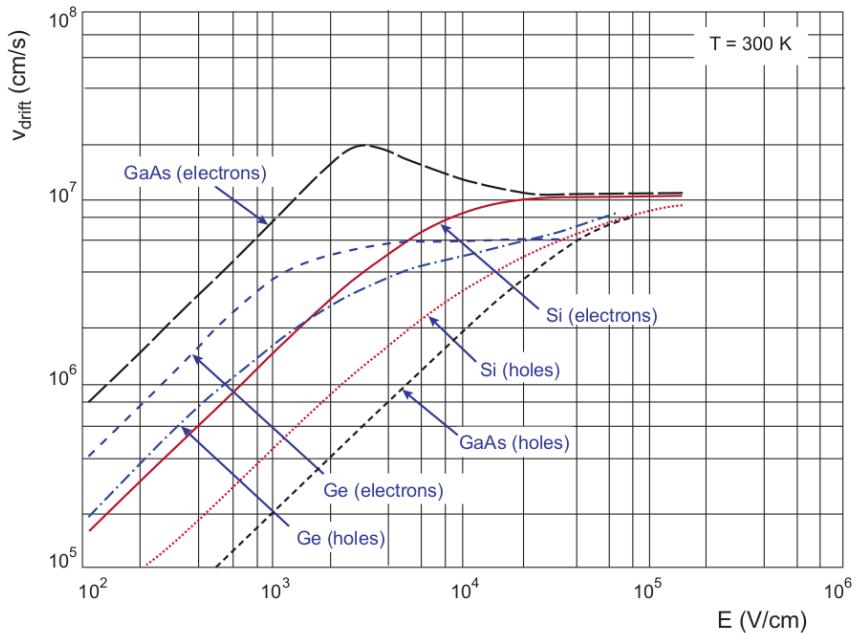


Figure 2.2: (a) Typical values for electrons and holes mobility in silicon at room temperature are $\mu_n \sim 1450 \text{ cm}^2/\text{Vs}$, $\mu_h = 500$. (b) Drift velocity at room temperature in different semiconductors

134 It is fundamental that pairs e/h are produced in the depleted region of the semiconductor where
 135 the probability of recombination with charge carriers is low to avoid loss of signals. Pixel detectors
 136 are then commonly reverse biased: a positive bias is given to the n electrode and a negative to the
 137 p to grow the depletion zone in the epitaxial layer below the electrode. The width of the depletion
 138 region is related with the external bias V_{ext} , the resistivity ρ and also with the dopant:

$$d_n \sim 0.55 \sqrt{\frac{\rho}{\Omega \text{cm}}} \frac{V_{ext}}{V} \mu\text{m} \quad (2.4) \quad d_p \sim 0.32 \sqrt{\frac{\rho}{\Omega \text{cm}}} \frac{V_{ext}}{V} \mu\text{m} \quad (2.5)$$

139

140

142 For that reason high resistivity wafers ($100 \Omega\text{cm} - k\Omega\text{cm}$) are typically preferred because they

¹⁴³ allow bigger depletion zone with smaller voltage bias. Metto il disegno "standard" di una giunzione

¹⁴⁴ 2.2 CCDs

¹⁴⁵ descrivi come sono fatte e come funziona il readout Tens of ms due to the need to transfer the
¹⁴⁶ charge signals pixel by pixel through a single output circuit For photon imaging the need of high
¹⁴⁷ assorbtion efficiency, per cui usi materiali con alto Z

¹⁴⁸ 2.3 Hybrid pixels

¹⁴⁹ METTI IN EVIDENZAZ CHE PUOI FARE UN READOUT CON TECNOLOGIA CMOS. Metti
¹⁵⁰ in evidenza che sono più veloci Hybrid pixels are made of two parts (fig. 2.3a), the sensor and the
¹⁵¹ electronics: for each pixel these two parts are welded together through microconnection (bump
¹⁵² bond).

¹⁵³ They provide a practical system where readout and sensor can be optimized separately, although
¹⁵⁴ the testing is less easy-to-do since the sensor and the R/O must be connected together before.

¹⁵⁵ In addition, the particular and sophisticated procedure to bond sensor and ASIC (application spe-
¹⁵⁶ cific integrated circuit) makes them difficult to produce, delicate, especially when exposed to high
¹⁵⁷ levels of radiation, and also expensive.

¹⁵⁸ A critical parameter for accelerator experiments is the material budget, which represents the main
¹⁵⁹ limit factor for momentum measurement resolution in a magnetic field; since hybrid pixels are
¹⁶⁰ thicker (\sim hundreds of μm) than monolithic ones (even less than 100 μm), using the latter the
¹⁶¹ material budget can be down by a third: typical value for hybrid pixels is 1.5 % X_0 per layer,
¹⁶² while for monolithic 0.5 % X_0 .

¹⁶³ Among other disadvantages of hybrid pixels there is the bigger power consumption that implies,
¹⁶⁴ by the way, a bigger cooling system leading in turn to an increase in material too.

¹⁶⁵

¹⁶⁶ DEPFET are the first attempt towards the integration of the front end (FE) on the sensor bulk:
¹⁶⁷ they are typically mounted on a hybrid structure but they also integrate the first amplification
¹⁶⁸ stage.

¹⁶⁹ Each pixel implements a MOSFET (metal-oxide-semiconductor field-effect transistor) transistor
¹⁷⁰ (a p-channel in fig. 2.3b): an hole current flows from source to drain which is controlled by the
¹⁷¹ external gate and the internal gate together. The internal gate is made by a deep $n+$ implant
¹⁷² towards which electrons drift after being created in the depletion region (to know how the signal
¹⁷³ is created in a pixel detector look at appendix A); the accumulation of electrons in the region
¹⁷⁴ underneath the n implant changes the gate potential and controls the transistor current.

¹⁷⁵ DEPFET typically have a good S/N ratio: this is principally due the amplification on-pixel and
¹⁷⁶ the large depletion region. But, since they need to be connected with ASIC the limiting factor still
¹⁷⁷ is the material budget.

¹⁷⁸ 2.4 CMOS MAPS and DMPAS

¹⁷⁹ With respect to CCDs, the radiation tolerance could be greatly increased by sensing the signal
¹⁸⁰ charge within its own pixel, instead of transporting it over thousands of pixels. The readout
¹⁸¹ speed could also be dramatically increased by in-pixel amplitude discrimination, followed by sparse
¹⁸² readout of only the hit pixels Monolithic active pixels accommodate on the same wafer both the
¹⁸³ sensor and the front end electronics, with the second one implanted on top within a depth of about
¹⁸⁴ 1 μm below the surface.

¹⁸⁵ MAPS have been first proposed and realized in the 1990s and their usage has been enabled by the
¹⁸⁶ development of the electronic sector which guarantees the decrease in CMOS transistors dimension
¹⁸⁷ at least every two years, as stated by the Moore's law².

²Moore's law states that logic density doubles every two years.

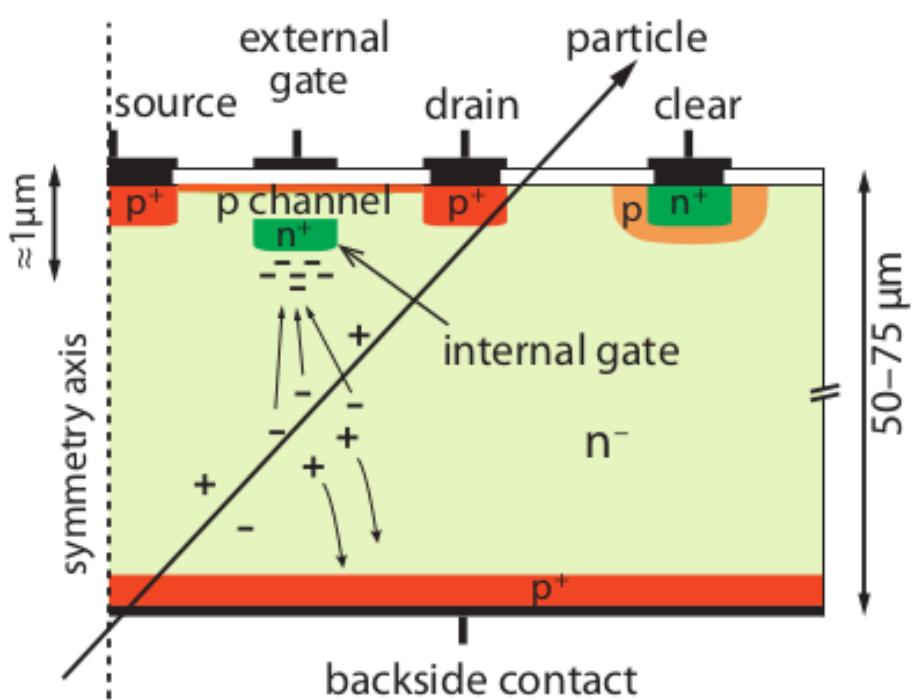
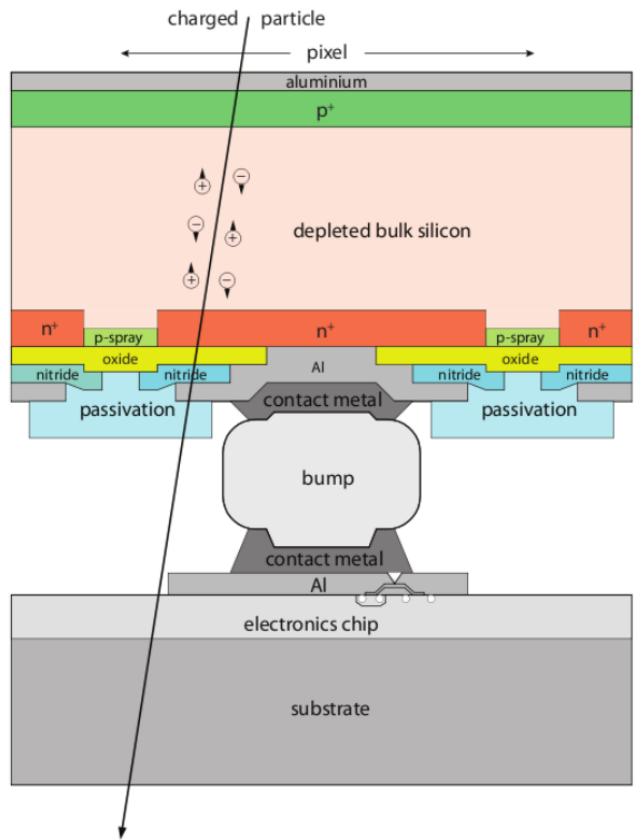


Figure 2.3: Concept cross-section of hybrid pixel (a) and of a DEPFET (b)

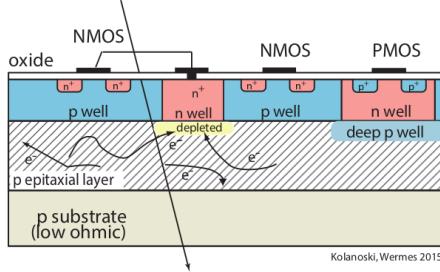


Figure 2.4: Concept cross-section of CMOS MPAS pixel

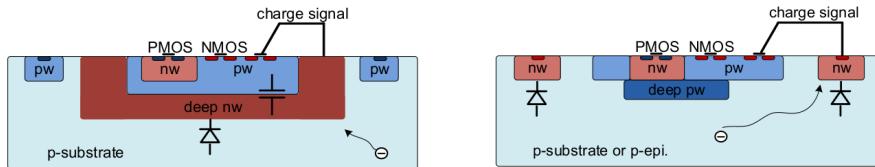


Figure 2.5: Concept cross-section with large and small fill factor

188 As a matter of fact the dimension of components, their organization on the pixel area and logic
 189 density are important issues for the design and for the layout; typically different decisions are taken
 190 for different purposes.

191 Monolithic active pixel can be distinguished between two main categories: MAPS and depleted
 192 DMAPS (DMAPS).

193 MAPS (figure a 2.4) have typically an epitaxial layer in range $1\text{ }\mu\text{m}$ to $20\text{ }\mu\text{m}$ and because they
 194 are not depleted, the charge is mainly collected by diffusion rather than by drift. This makes the
 195 path of charges created in the bulk longer than usual, therefore they are slow (of order of 100 ns)
 196 and the collection could be partial especially after the irradiation of the detector (look at A for
 197 radiation damages), when the trapping probability become higher.

198 In figure 2.4 is shown as example of CMOS MAPS: the sensor in the scheme implements an n well
 199 as collection diode; to avoid the others n wells (which contain PMOS transistor) of the electronic
 200 circuit would compete in charge collection and to shield the CMOS circuit from the substrate,
 201 additionally underlying deep p well are needed. DMAPS are instead MAPS depleted with d
 202 typically in $\sim 25\text{ }\mu\text{m}$ to $150\text{ }\mu\text{m}$ (eq. 2.1) which extends from the diode to the deep p-well, and
 203 sometimes also to the backside (in this case if one wants to collect the signal also on this electrode,
 204 additional process must be done).

205 2.4.1 DMAPS: large and small fill factor

206 There are two different sensor-design approaches (figure 2.5) to DMAPS:

- 207 • large fill factor: a large collection electrode that is a large deep n-well and that host the
 208 embedded electronics
- 209 • small fill factor: a small n-well is used as charge collection node

210 To implement a uniform and stronger electric field, DMAPS often uses large electrode design that
 211 requires multiple wells (typically four including deep n and p wells); this layout adds on to the
 212 standard terms of the total capacity of the sensor a new term (fig. 2.6), that contributes to the
 213 total amplifier input capacity. In addition to the capacity between pixels (C_{pp}) and between the
 214 pixel and the backside (C_b), a non-negligible contribution comes from the capacities between wells
 215 (C_{SW} and C_{WW}) needed to shield the embedded electronics. These capacities affect the thermal
 216 and 1/f noise of the charge amplifier and the τ_{CSA} too:

	small fill factor	large fill factor
small sensor C	✓ (< 5 fF)	✗ (~ 100 200fF)
low noise	✓	✗
low cross talk	✓	✗
velocity performances	✓	✗ (~100 ns)
short drift paths	✗	✓
radiation hard	✗	✓

Table 2.1: Small and large fill factor DMAPS characteristics

$$ENC_{thermal}^2 \propto \frac{4}{3} \frac{kT}{g_m} \frac{C_D^2}{\tau_{sh}} \quad (2.6) \quad \tau_{CSA} \propto \frac{1}{g_m} \frac{C_D}{C_f} \quad (2.7)$$

where g_m is the transconductance, τ_{sh} is the shaping time.
 Among the disadvantages coming from this large input capacity could be the coupling between the sensor and the electronics resulting in cross talk: noise induced by a signal on neighbouring electrodes; indeed, since digital switching in the FE electronics do a lot of oscillations, this problem is especially connected with the intra wells capacities. So, larger charge collection electrode

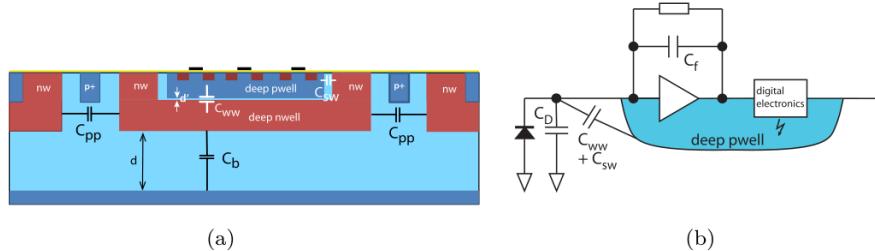


Figure 2.6: C_{pp} , C_b , C_{WW} , C_{SW}

sensors provide a uniform electric field in the bulk that results in short drift path and so in good collection properties, especially after irradiation, when trapping probability can become an issue. The drawback of a large fill-factor is the large capacity (~100 fF): this contributes to the noise and to a speed penalty and to a larger possibility of cross talk.

The small fill-factor variant, instead, benefits from a small capacity (5 fF to 20 fF), but suffers from a not uniform electric field and from all the issue related to that. **Ho già detto prima parlando dei MAPS, devo ripetere qui?**

As we'll see these two different types of sensor require different amplifier: the large electrode one is coupled with the charge sensitive amplifier, while the small one with voltage amplifier (sec 2.5.1).

2.4.2 A modified sensor

A process modification developed by CERN in collaboration with the foundries has become the standard solution to combine the characteristics of a small fill factor sensor (small input amplifier capacity) and of large fill factor sensor (uniform electric field) is the one carried out for ALICE upgrade about ten years [1].

A compromise between the two sensors could also be making smaller pixels, but this solution requires reducing the electronic circuit area, so a completely new pixel layout should be though. The modification consists in inserting a low dose implant under the electrode and one its advantage lies in its versatility: both standard and modified sensor are often produced for testing in fact.

Before the process modification the depletion region extends below the diode towards the substrate, and it doesn't extend laterally so much even if a high bias is applied to the sensor (fig. 2.7). After, two distinct pn junctions are built: one between the deep p well and the n^- layer, and the

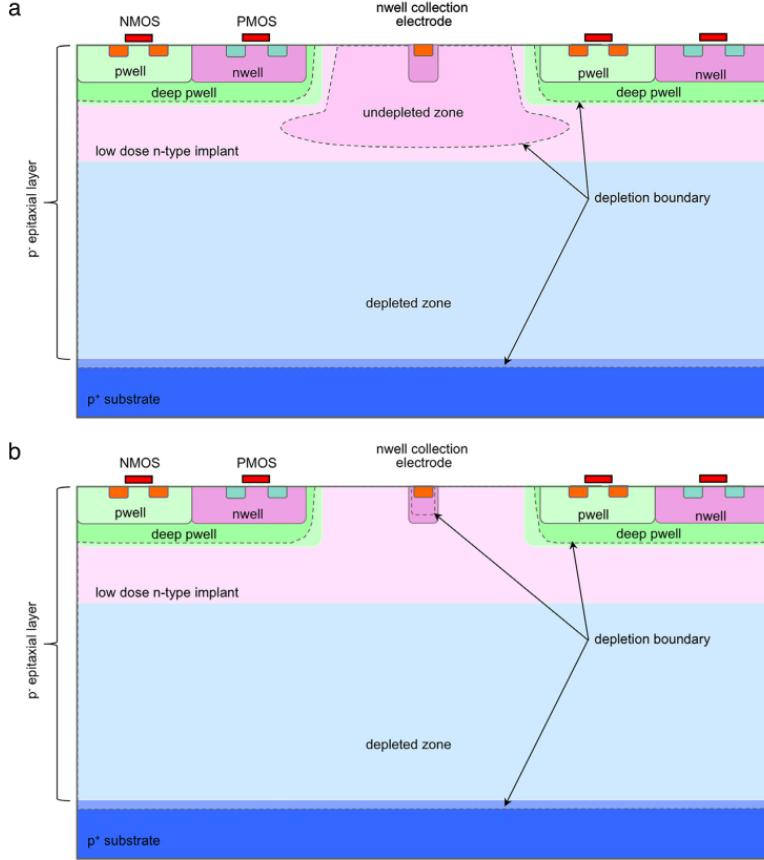


Figure 2.7: A modified process for ALICE tracker detector: a low dose n implant is used to create a planar junction. In (a) the depletion is partial, while in (b) the pixel is fully depleted.

other between the n^- and the p^- epitaxial layer, extending to the all area of the sensor.
 Since deep p well and the p-substrate are separated by the depletion region, the two p electrodes can be biased separately³ and this is beneficial to enhance the vertical electric field component.
 The doping concentration is a trimmer parameter: it must be high enough to be greater than the epitaxial layer to prevent the punchthrough between p-well and the substrate, but it must also be lower enough to allow the depletion without reaching too high bias.

2.5 Analog front end

After the creation of a signal on the electrode, the signal enters the front end circuit (fig.2.8), ready to be molded and transmitted out of chip. Low noise amplification, fast hit discrimination and an efficient, high-speed readout architecture, consuming as low power as possible must be provided by the readout integrated electronics (ROIC).

Let's take a look to the main steps of the analog front end chain: the preamplifier (that actually often is the only amplification stage) with a reset to the baseline mechanism and a leakage current compensation, a shaper (a band-pass filter) and finally a discriminator. The whole chain must be optimized and tuned to improve the S/N ratio: it is very important both not to have a large noise before the amplification stage in order to not multiply that noise, and chose a reasonable threshold of the discriminator to cut noise-hits much as possible.

³This is true in general, but it can be denied if other doping characteristics are implemented, and we'll see that this is the case of TJ-Monopix1

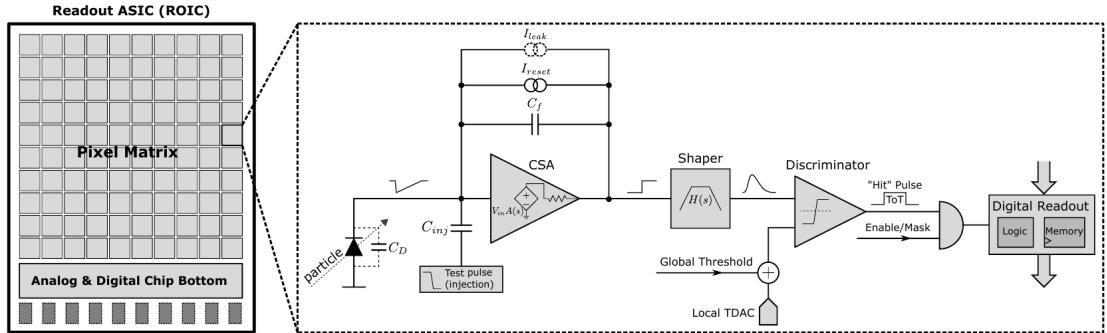


Figure 2.8: Readout FE scheme: in this example the preamplifier is a charge sensitive one (CSA) but changing the capacitive feedback into a resistive one, this can be converted in a voltage or current amplifier.

2.5.1 Preamplifier

Even if circuits on the silicon crystal are only constructed by CMOS, a preamplifier can be modeled as an operational amplifier (OpAmp) where the gain is determined by the input and feedback impedance (first step in figure 2.8):

$$G = \frac{v_{out}}{v_{in}} = \frac{Z_f}{Z_{in}} \quad (2.8)$$

Depending on whether a capacity or a resistance is used as feedback, respectively a charge or a voltage amplifier is used: if the voltage input signal is large enough and have a sharp rise time, the voltage sensitive preamplifier is preferred. Consequently, this flavor doesn't suit to large fill factor MAPS whose signal is already enough high: $v_{in} = Q/C_D \approx 3fC/100 \text{ pF} = 0.03 \text{ mV}$, but it's fine for the small fill factor ones: $v_{in} = Q/C_D \approx 3fC/3 \text{ pF} = 1 \text{ mV}$.

In the case of a resistor feedback, if the signal duration time is longer than the discharge time ($\tau = R_S C_D$) of the detector the system works as current amplifier, as the signal is immediately transmit to the amplifier; in the complementary case (signal duration longer than the discharge time) the system integrates the current on the C_D and operates as a voltage amplifier.

2.6 Readout logic

Readout logic includes the part of the circuit which takes the FE output signal, processes it and then transmit it out of pixel and/or out of chip; depending on the situation of usage different readout characteristics must be provided.

To store the analogical information (i.e. charge collected, evolution of signal in time, ...) big buffers and a large bandwidth are needed; the problem that doesn't occur, or better occur only with really high rate, if one wants record only digital data (if one pixel is hit 1 is recorded, and if not 0 is recorded).

A common compromise often made is to save the time over threshold (ToT) of the pulse in clock cycle counts; this needs of relatively coarse requirement as ToT could be trimmer to be a dozen bits but, being correlated and hopefully being linear with the deposited charge by the impinging particle in the detector, it provides a sufficient information. The ToT digitalization usually takes advantage of the distribution of a clock (namely BCID, bunch crossing identification) on the pixels' matrix. The required timing precision is at least around 25 ns, that corresponds to the period of bunch collisions at LHC; for such reason a reasonable BCID-clock frequency for pixels detector is 40 MHz.

Leading and trailing edges' timestamp of the pulse are saved on pixel within a RAM until they have been read, and then the ToT is obtained from their difference.

Moreover, the readout architecture can be full, if every hit is read, or triggered, if a trigger system decides if the hit must be stored or not. On one hand the triggered-readout needs buffers

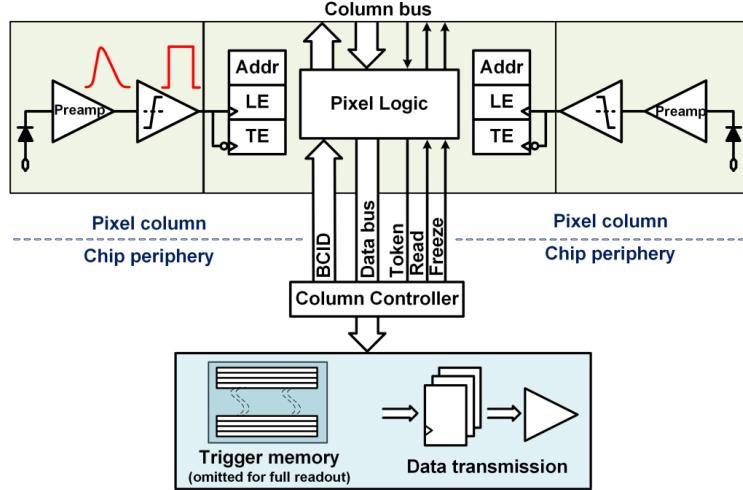


Figure 2.9: Column drain R/O scheme where ToT is saved

and storage memories, on the other the full readout, because there is no need to store hit data on chip, needs an high enough bandwidth.

A triggered readout is fundamental in accelerator experiments where the quantity of data to store is too large to be handled, and some selections have to be applied by the trigger: to give an order of growth, at LHC more than 100 TBit/s of data are produced, but the storage limit is about 100 MBit/s [2] (pag. 797).

Typically the trigger signal is processed in a few μs , so the pixel gets it only after a hundred clock cycles from the hit arrival time: the buffer depth must then handle the higher trigger latency.

After having taken out the data from the pixel, it has to be transmitted to the end of column (EoC) where a serializer deliver it out of chip, typically to an FPGA.

There are several ways of transmitting data from pixel to the end of column: one of the most famous is the column-drain read out, developed for CMS and ATLAS experiments [3]. All the pixels in a double-column share a data bus and only one pixel at a time, according to a priority chain, can be read. The reading order circuit is implemented by shift register (SR): when a hit arrives, the corresponding data, which can be made of timestamp and ToT, is temporarily stored on a RAM until the SH does not allow the access to memory by data bus.

Even if many readout architectures are based the column-drain one, it doesn't suit for large size matrices. The problem is that increasing the pixels on a column would also raise the number of pixels in the priority chain and that would result in a slowdown of the readout.

If there isn't any storage memory, the double-column behaves as a single server queue and the probability for a pixel of waiting a time T greater than t , with an input hit rate on the column μ and an output bandwidth B_W is [4]:

$$P(T > t) = \frac{\mu}{B_W} e^{-(B_W - \mu)t} \quad (2.9)$$

To avoid hit loss (let's neglect the contribution to the inefficiency of the dead time τ due to the AFE), for example imposing $P_T > t \sim 0.001$, one obtains $(B_W - \mu) t_t \sim 6$, where t_t is the time needed to transfer the hit; since t_t is small, one must have $B_W \gg \mu$, that means a high bandwidth [4].

Actually the previous one is an approximation since each pixel sees a different bandwidth depending on the position on the queue: the first one sees a full bandwidth, but the next sees a smaller one because occasionally it can be blocked by the previous pixel. Then the bandwidth seen by the pixel i is $B_i = B - \sum_j \mu_j$, where μ_j is the hit rate of the j th pixel.

The efficiency requirement on the bandwidth and the hit rate becomes: $B_{W,i} > \mu_i$, where the index i means the constraint is for a single pixel; if all the N pixels on a column have the same rate $\mu = N\mu_i$, the condition reduces to $B_W > \mu$. The bandwidth must be chosen such that the

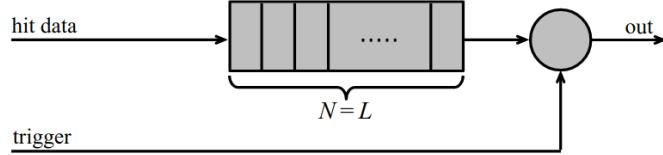


Figure 2.10: Block diagram of a pipeline buffer: N is the dimension of memory buffer and L is the trigger latency expressed in BCID cycles

327 mean time between hits of the last pixel in the readout chain is bigger than that.
 328 In order to reduce the bandwidth a readout with zero suppression on pixel is typically employed;
 329 this means that only information from channels where the signal exceeds the discriminator thresh-
 330 old are stored. Qualcosa sulla zero suppression? La metto qui questa affermazione?

331 If instead there is a local storage until a trigger signal arrives, the input rate to column bus
 332 μ' is reduced compared to the hit rate μ as: $\mu' = \mu \times r \times t$, where r is the trigger rate and t is
 333 the bunch crossing period. In this situation there is a more relaxed constraint on the bandwidth,
 334 but the limiting factor is the buffer depth: the amount of memory designed depends both on the
 335 expected rate μ and on the trigger latency t as $\propto \mu \times t$, that means that the higher the trigger
 336 latency and the lower the hit rate to cope with.

337 In order to have an efficient usage of memory on pixels' area it's convenient grouping pixels
 338 into regions with shared storage. Let's compare two different situations: in the first one a buffer
 339 is located on each pixel area, while in the second one a core of four pixels share a common buffer
 340 (this architecture is commonly called FE-I4).

Consider a 50 kHz single pixel hits rate and a trigger latency of 5 μs , the probability of losing

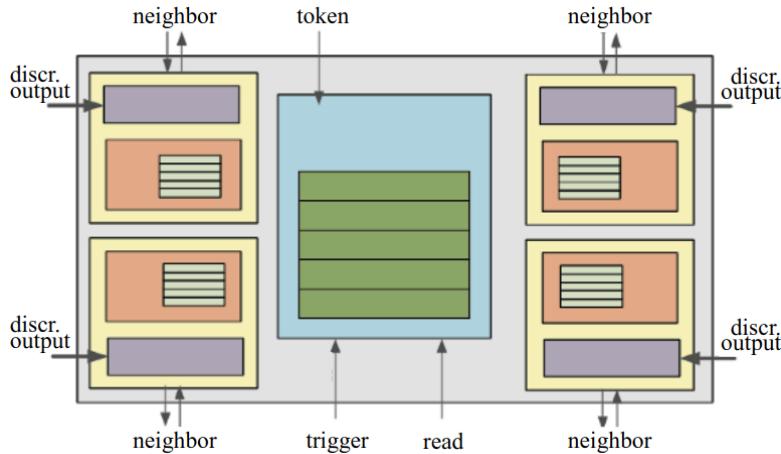


Figure 2.11: Block diagram of the FE-I4 R/O. Read and memory management section is high-
 lighted in light blue; latency counters and trigger management section are highlighted in green;
 hit processing blocks are highlighted in purple; ToT counters and ToT management units are high-
 lighted in orange

341 hits is:
 342

$$P(N > 1|\nu) = 1 - P(N = 0|\nu) - P(N = 1|\nu) = 1 - e^{-\nu}(1 + \nu) \approx 2.6\% \quad (2.10)$$

343 where I have assumed a Poissonian distribution with mean $\nu = 0.25$ to describe the counts N.
 344 To get an efficiency ϵ greater than 99.9 % a 3 hit depth buffer is needed:

$$P(N > 3|\nu) = 1 - \sum_{i=0}^3 P(N = i|\nu) < 0.1\% \quad (2.11)$$

345 Considering the second situation: if the average single pixel rate is still 50 kHz, grouping four pixels

³⁴⁶ the mean number of hits per trigger latency is $\nu = 0.25 \times 4 = 1$. To get an efficiency of 99.9% (eq.
³⁴⁷ 2.11) a buffer depth of 5 hits in the four-pixels region, instead of 3 per pixels, is needed.

³⁴⁸ Chapter 3

³⁴⁹ Use of pixel detectors

³⁵⁰ There always was a tight relation between the development of cameras and pixel detectors since
³⁵¹ 1969, when the idea of CCDs, thanks to whom Boyle and Smith were awarded the Nobel Prize in
³⁵² Physics in 2009, revolutionized photography allowing light to be captured electronically instead of
³⁵³ on film. Even though the CMOS technology was already known when CCDs spread, the costs of
³⁵⁴ productions were too high to allow the diffusion of these sensors for which needed to wait until
³⁵⁵ 1990s. From that period on, the fast diffusion of CMOS was mainly due to the less cost than
³⁵⁶ CCD, and the less power required for supply. Nowadays CCDs are still preferred over MAPS in
³⁵⁷ astronomy, where the astronomical sources' rate are low enough to cope with tens of ms for the
³⁵⁸ readout.

³⁵⁹ The principal use cases of pixel detectors are particle tracking and imaging: in the former case
³⁶⁰ individual charged particles have to be identified, in the latter instead an image is obtained by
³⁶¹ the usually un-triggered accumulation of the impinging radiation. Also the demands on detectors
³⁶² performance depends on their usage, in particular tracking requires high spatial resolution, fast
³⁶³ readout and radiation hardness.

³⁶⁴ 3.1 Tracking in HEP

³⁶⁵ At first the physics world overlooked the CCDs, and all pixel in general, as against the gaseous
³⁶⁶ detector for tracking: there was no need to replace these ones which had a sufficient good resolution
³⁶⁷ ($100\text{ }\mu\text{m}$). Since 1974, with the measurement of the invariant mass of the **j psi** and the affirmation
³⁶⁸ of the quark model, all experiments start to look for better spatial resolutions in order to achieve
³⁶⁹ the possibility of reconstructing short lived particle.

³⁷⁰ Historically, the first pixel detector employed in particle physics was a CCD: it was installed in
³⁷¹ the spectrometer at the CERN's Super Proton Synchrotron (SPS) by the ACCMOR Collaboration
³⁷² (Amsterdam, CERN, Cracow, Munich, Oxford, RAL) at mid 1980s, with the purpose of studying
³⁷³ the recently-discovered charm particles. The second famous usage of CCDs took place at SLAC
³⁷⁴ in the Large Detector (SLD) during the two years 1996-98. **Cosa vedono di così importante da**
³⁷⁵ **dire che servono i pixel detector?** From that period on particle tracking in experiments have been
³⁷⁶ transformed radically: it was mandatory for HEP experiments to build an inner vertex detector.
³⁷⁷ In 1991, the more demanding environments led to the development of hybrid pixel detectors:
³⁷⁸ a dedicated collaboration, RD19, was established at CERN with the specific goal to define a
³⁷⁹ semiconductor micropattern detector with an incorporated signal processing at a microscopic level.
³⁸⁰ In those years a wide set of prototypes of hybrid pixel has been manufactured; among the greatest
³⁸¹ productions a mention goes to the huge ATLAS and CMS vertex detectors. From the middle of
³⁸² 2013 a second collaboration, RD 53, has been established with the new goal to find a pixel detector
³⁸³ suitable for phase II future upgrades of those experiments. Even if the collaboration is specifically
³⁸⁴ focused on design of hybrid pixel readout chips (aiming to 65 nm technique so that the electronics
³⁸⁵ fits within the pixel area), also other options have been taken in account and many test have been
³⁸⁶ done on MAPS for example. Requirements imposed by HL-LHC will become higher in time: for

example, a dose and radiation of 5 Mrad and 1016NIEL are expected after 5 years of operation. Time resolution, material budget and power consumption are also issues for the upgrade: a time resolution better than 25 ns for a bunch crossing frequency of 40 MHz, a material budget lower than 2% and a power consumption lower than 500 mW/cm² are required.

Amidst the solutions proposed 3D silicon detector, invented by Sherwood Parker in 1995, and MAPS are the most promising. In 3D sensors the electrode is a narrow column of n-type implanted vertically across the bulk instead of being implanted on the wafer's surface. The charge produced by the impinging particle is then drifted transversally within the pixel, and, as the mean path between two electrode can be sufficient low, the trap probability is not an issue. 3D pixels have been already proved in ATLAS tracker [quando?](#). Even if 3D detector are adequately radiation hard, MAPS architecture looked very promising from the beginning: they overcome both the CCDs long reading time and the hybrid problems (I have already explained in section ?? the benefits of MAPS). Experiments such as ALICE at LHC and STAR at RHIC have already introduced the CMOS MAPS technology in their detectors. ALICE Tracking System (ITS2), upgraded during the LHC long shut down in 2019-20, was the first large-area ($\sim 10 \text{ m}^2$ covered by 2.5 Gpixels) silicon vertex detector based on CMOS MAPS.

3.1.1 Hybrid pixels at LHC and at SuperKEKB

ATLAS

With CMS, ATLAS is one of two general-purpose detectors at the LHC and has the largest volume detector ever constructed for a particle collider (46 m long and 25 m in diameter). The Inner Detector consists of three different systems all immersed in a magnetic field parallel to the beam axis whose main components are: the pixel, the micro-strips and transition radiation trackers. Concerning the pixel detector, 92 million pixels are divided in 4 barrel layers and 3 disks in each end-cap region, covering a total area of 1.9 m² and having a 15 kW of power consumption.

As stated by the ATLAS collaboration the pixel detector is exposed by an extreme particle flux: "By the end of Run 3¹, the number of particles that will have hit the innermost pixel layers will be comparable to the number it would receive if it were placed only a few kilometres from the Sun during a solar flare". Considering that the particle density will increase even more with HL-LHC, radiation hardness is definitively target to achieve.

The most ambitious goal is employ a MAPS-based detector for the inner-layer barrels, and for this reason the RD53 collaboration is performing many test on MAPS prototypes, as Monopix of which I will talk about in section ??.

Up to now this possibility will be eventually implemented during the second phase of the HL-LHC era, as at the start of high-luminosity operation the selected option is the hybrid one. The sensor will be bonded with ITkPix, the first full-scale 65 nm hybrid pixel-readout chip developed by the RD53 collaboration. Regarding the sensor, a valuable option is using 3D pixels, which have already proved themselves in ATLAS, for the insertable B layer (IBL).[qualcosa in più sui 3d](#). The number of pixels will be increased of a factor about 7, passing from 92 millions to 6 billion.

CMS

[da scrivere](#) 124 million pixels; cylindrical layers roughly at 3cm, 7cm, 11cm and 16cm and disks at either end, and so will be vital in reconstructing the tracks of very short-lived particles. Each of these silicon pixels is 100um by 150um, even with only around 50 microwatts per pixel, the total power output is 7.5kW-

LHCb

LHCb is a dedicated heavy-flavour physics experiment that exploits pp interactions at 14 TeV at LHC. It was the last experiment to upgrade the vertex detector, the Vertex Locator (VELO),

¹Run 3 start in June 2022

433 replacing the silicon-strip with pixels in May 2022. As the instantaneous luminosity in Run3 is
434 increased by a factor $\lesssim 10$, much of the readout electronics and of the trigger system have been
435 developed in order to cope with the large interaction rate. To place the detector as close as possible
436 to the beampipe and reach a better track reconstruction resolution, the VELO has a surprising
437 feature: it can be moved. During the injection of LHC protons it is parket at 3 cm from the beams
438 and only when the stability is reach it is brought at ~ 5 mm. Radiation hardness as well as readout
439 speed are then a priority for the detectors: that's why the collaboration opted for a hybrid system.
440 The Velopix is made bonding sensors, each measuring 55×55 micrometers, 200 μm -thick to a
441 200 μm -thick ASIC specially developed for LHCb and coming from the Medipix family (sec. ??),
442 which can handles hit rates up to 900 MHz per chip. Since the detector is operated under vacuum
443 near the beam pipe, the heat removal is particularly difficult and evaporative CO₂ microchannel
444 cooling are used.

445 **BelleII**

446 The current vertex detector of BelleII, VXD, is made of a pixel detector (PXD), fabricated with
447 2 layers of DEPFET-based pixels, and 4 layers of a double-sided silicon strip detectors (SVD)[5].
448 Due to the small capacitance of the collection node, DEPFET presents a high signal-to-noise ratio
449 (in 30-50) thanks to the low intrinsic noise and to the large signal achieved with the fully depleted
450 bulk: pixels are thinned to 75 μm in the active region, then a MIP is supposed to create a signal of
451 $\sim 6000 e^-$, while the typical noise of DEPFET is around 200 e^- . The ASIC read out is still based
452 on a rolling shutter logic, with an integration time of 20 μs . In order to reduce the data-storage
453 memory PXD hits are only used to improve spatial resolution of tracks: the SVD informations are
454 used by the High Level Trigger (HLT) to look for regions of interest in the pixel ladders just by
455 extrapolating back the tracks found in the tracker detector, and this method allows to store only
456 data belonging to these areas; the PXD hits are then used in offline track fit to improve the vertex
457 resolution.

458 MAPS have been proposed for the replacement of VXD during the Long Shut Down 2 (LSD2)
459 foreseen around 2026-27; the new vertex detector, VTX, should be made of 5 layers fabricated
460 by the optimized Belle II pixel sensor (OBELIX), a detector based on TJ-Monopix have been
461 selected (look at chapter ??). The main advantages VTX should bring are a obvious improving
462 in the track and vertex resolution (14 μm before upgrade, $\lesssim 10 \mu\text{m}$ expected after upgrade) and a
463 reduction in the X_0 (da.. a..), a higher background tolerance because of the smaller sensor than
464 strips dimension and a low bandwidth due to the on-chip sparsification.

465 **3.1.2 First attempts to MAPS**

466 **MIMOSA at EUDET and STAR**

467 MIMOSA [6][7] (standing for Minimum Ionizing MOS Active pixel sensor), designed in 2008,
468 prefigured the architecture of MAPS for coming vertex detector being the first large scale sensor
469 to be employed as detector. MIMOSA-26 equiped the final version of EUDET high resolution
470 beam telescope both at CERN-SPS and at DESY while the MIMOSA-28 devices are used for
471 the first MAPS-based vertex detector at the STAR experiment. MIMOSA-26 is fabricated in a
472 350 nm, and a module features 1152 columns, split into 18 independent groups, and 576 rows, with
473 square pixels having a side of 18.4 μm length; therefore, because of the small dimension, charge
474 sharing is an issue aggiungi qualcosa. The readout is done in a rolling shutter mode: the chip is
475 an Active Pixels (APS) and therefore it incorporates the amplification on pixel, while the signal
476 discrimination and zero-suppression logic are placed at the EoC, where is also placed a memory.
477 The chip is an Active Pixels (APS) and therefore it incorporates the amplification on pixel, while
478 the signal discrimination and zero-suppression logic are placed at the EoC: the readout is done in
479 a rolling shutter mode with a frame integration time that can be lowered down to 85 ms, and a
480 memory allowing to store up to six hits is.

481 The EUDET telescope, equipped with six sensor planes, requires highly granular and thin pixel

482 detectors in order to achieve an excellent track resolution (around $2\text{ }\mu\text{m}$) even at the rather low
 483 particle energies of up to 6 GeV. The STAR experiment at the Relativistic Heavy Ion Collide
 484 (RHIC) accelerator at the Brookhaven National Laboratory (BNL) is the first to include MAPS
 485 in the vertex detector[8]. The main tracking detector in STAR is a TPC with radii 60-190 cm
 486 embedded in a 0.5 T solenoidal magnetic field, that provides a pointing resolution of approximately
 487 1 mm. The pixel detector, PXL, is a part of a 3-detector system, Heavy Flavor Tracker (HFT), that
 488 has been added to the pre-existing STAR apparatus just before the 2014 Run in order to improve
 489 the impact parameter resolution and to enable the direct reconstruction of hadronic decays of heavy
 490 flavor mesons and baryons. The Heavy Flavor Tracker (HFT) is composed by the Silicon Strip
 491 Detector (SSD), the Intermediate Silicon Tracker (IST) and the Pixel Detector (PXL); the first
 492 one is placed at 22 cm from the beam pipe and consists of double sided strips with $95\text{ }\mu\text{m}$ inter-strip
 493 pitch, the second one, placed at 14 cm, is made of single sided silicon pads with $600\text{ }\mu\text{m} \times 6\text{ mm}$ pitch
 494 and the last one made by two layers is placed at 2.8 cm and 8 cm fabricated with ULTIMATE2 (also
 495 known as MIMOSA-28), a successor of MIMOSA-26 sensor, with pitch $20.7\text{ }\mu\text{m}$ and thinned down
 496 to $50\text{ }\mu\text{m}$. An area of 0.16 m^2 are covered by 400 MAPS sensor, corresponding to 356 millions of
 497 pixels divided into array size of 928×960 . Each pixel includes circuitry for readout, amplification,
 498 and Correlated Double Sampling (CDS) for signal extraction and noise subtraction and the frame
 499 integration time is $185.6\text{ }\mu\text{s}$; after the subtraction the signal to noise ratio is ~ 30 , with a noise
 500 between 10-12 electrons and a signal of 1000 e^- . Thanks to the HFT system and the PXL, STAR
 501 achieved a track pointing resolution $46\text{ }\mu\text{m}$ for 750 MeV/c kaons, and better than $30\text{ }\mu\text{m}$ for particle
 502 momenta bigger than 1 GeV/c : this performance enabled the study of D-meson production with a
 high significance signal.

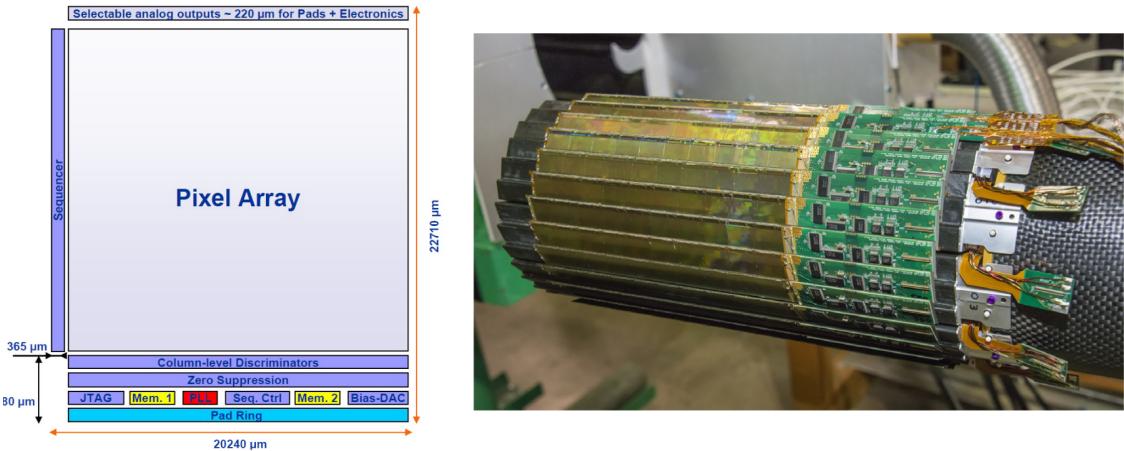


Figure 3.1: (a) The HFT PXL detector; (b) Block-diagram of the ULTIMATE-2 sensor

503

504 ALPIDE at ALICE

505 ALICE (A Large Ion Collider Experiment) is a detector dedicated to heavy-ion physics and to the
 506 study of the condensed phase of the chromodynamics at the LHC. The tracking detector consists of
 507 the Inner Tracking System (ITS), the gaseous Time Projection Chamber (TPC) and the Transition
 508 Radiation Detector (TRD), and all those are embedded in a magnetic field of 0.5 T. The ITS is
 509 made by six layers of detectors, two for each type, from the interaction point outwards: Silicon
 510 Pixel Detector (SPD), Silicon Drift Detector (SDD) and Silicon Strip Detector (SSD). Contrary
 511 to the others LHC experiments, ALICE tracker is placed in a quite different environments: the
 512 expected dose is smaller by two order of magnitude and the rate of interactions is few MHz instead
 513 of 40 MHz, but the number of particles comes out of each interaction is higher (the SPS is invested
 514 by a density of particles of $\sim 100\text{ cm}^{-2}$). The reconstruction of very complicated events with a large
 515 number of particle is a challenge, hence to segment and to minimize the amount of material, which
 516 may cause secondary interaction complicating further the event topology, is considered a viable

strategy. The detector employs the ALPIDE chip, developed by ALICE collaboration, fabricated in the 180 nm CMOS Imaging Sensor process of TowerJazz, whose design takes full advantage of process feature which allows full circuitry within the pixel matrix. Thanks to the reduction of the material budget, ITS2 obtained an amazing improvement both in the position measurement and in the momentum resolution, improving the efficiency of track reconstruction for particle with very low transverse momentum (by a factor 6 at $pT \sim 0.1$ GeV/c). Further advancements in CMOS MAPS technology are being aggressively pursued for the ALICE ITS3 vertex detector upgrades (foreseen around 2026-27), with the goals of further reducing the sensor thickness and improving the readout speed of the devices, while keeping power consumption at a minimum.

3.2 Other applications

Historically for imaging purpose the CCDs were the favoured device: they can be used as single photon counter or integrating and collecting the charge released by more impinging particles. The utilisation in the first case is similar to the tracking one, except that the requirements are less tight, so much that two noteworthy of microchips originally meant for detectors in particle physics at the LHC, and later employed in other fields are Medipix and Timepix. They are read-out chips developed by the Medipix Collaborations since early 1990s. For two decades, different Medipix generations have been produced, having a rough correlation with the feature size used: Medipix2 (1999) used 250 nm feature size CMOS while Medipix3 (2005) 130 nm. The aim of the fourth collaboration (2016), instead, is designing pixel read-out chips that prepared for **TSV processing and may be tiled on all four sides. DOVREI METTERE DUE RIGHE SU TSV OPPURE TAGLIARE.** For photons imaging other materials with higher atomic charge than silicon could be preferred, as a high photon absorption efficiency is needed: it was for this reason that Medipix2 was bump bonded to identically segmented sensors of both silicon and GaAs.

The applications in scientific imaging vary from astrophysics and medical imaging to more exotic domains as studies of protein dynamics, art authentication and dosimetry. The most important employment of Medipix is as X-ray single photon counting in industrial and medical radiography and in 3D computed tomography. Thanks to a New-Zealand company, the MARS Bioimaging detector has been fabricated, which is capable of resolving the photons energy and produce 3D coloured images. Besides tracking in HEP (I have already cited the use of Timepix3 is in the beam telescope of the LHCb VELO), an important use of Timepix is in dosimetry **Timepix Detector for Imaging in Ion Beam Radiotherapy- aggiungi qualche info** A small-Timepix detector with the dimension of a USB can also be found at the International Space Station, where it is exploited for radiation, principally made of heavy-ion, monitoring.

3.2.1 Applicability to FLASH radiotherapy

A possible new application of pixels detector is dosimetry or beam monitoring of charge particles in high intensity radiography. The radiological treatment is a common method used in 60% of tumors both as palliative care and as treatment. It can be given before, after or during a surgery, (Intra operative radiation therapy-IORT) and many different types of radiations (photons, electrons, protons and ions, which mainly are hydrogen and carbon) can be used to irradiate the affected tissues. Exploiting the ionizing energy loss, that can be parametrized by the Linear Energy Transfer (LET), a biological damage can be delivered to the tissue: while α and β particles are high LET radiations with values in $100\text{ keV}/\mu\text{m}$ to $200\text{ keV}/\mu\text{m}$, x-rays and gamma-rays are low LET radiations with values in range $0.2\text{ keV}/\mu\text{m}$ to $2\text{ keV}/\mu\text{m}$.

If x-ray photons, with energy in 4 MeV to 25 MeV are used, the ionization is caused by the Compton electrons and is more in the superficial layers of the tissue due to the exponential attenuation of the beam. The hadrons energy loss, instead, is strongly localized in the last region of the track, that is the Bragg peak. Ion beam enables better focusing of the radiation thereby improves the sparing of the surrounding healthy tissues; on the other hand the delivered dose distribution depends more on the patient's density tissues (e.g. bones, swelling, fat). **Ensuring the target**

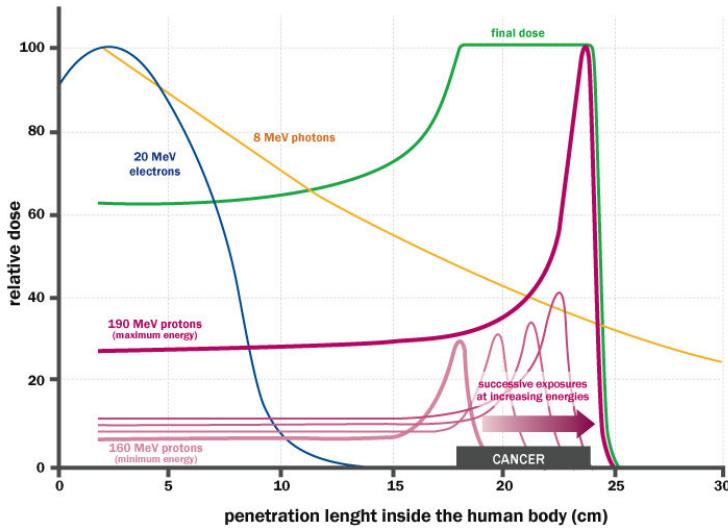


Figure 3.2: The Spread Out Bragg Peak (SOBP) curve (green), which is a constant dose distribution, is obtained from the superposition of many Bragg peak of hadrons with different energy.

	CONV-RT	FLASH-RT
Dose rate	0.03 Gy/s	40 Gy/s
Intra pulse dose rate	100 Gy/s	106 Gy/s
Treatment duration	~minutes	$\lesssim 500$ ms
DDP	0.3 mGy	1 Gy to 10 Gy
Pulse width	3 μ s	~ 2 μ s

Table 3.1: Typical value of treatment parameters

coverage is a fundamental objective in radiotherapy and is closely connected to the choice of the particles. Electrons cover the target since they tend to spread out and can cover a field size of a few cm² at a distance of a few cm from the source. Instead, the limited size of the beam for protons and photons from ultra high dose rate microbeam radiation therapy (MRT), for which FLASH effect was seen, requires the scanning of target. The radiobiological consequences of scanning both in spatial-fractionation and in prolonged exposure, which might not be sufficient to maintain a high mean dose rate to trigger FLASH effect, need to be explored. To date, the FLASH effect has been most commonly demonstrated using low-energy electron linacs

Recently² a promising method for RT at ultra high dose rate (at least 40 Gy/s) and for this reason called FLASH-RT[9], instead of CONV-RT (0.03 Gy/s), came out. This treatment takes advantages of biological differences between tumors and healthy tissues: it is characterized by reducing normal tissue toxicity and maintaining equivalent tumor damage. The response to dose can be described by the survival fraction probability, describing the fraction of surviving cell as a function of the dose:

$$S(D) = S(0) e^{-(\alpha D + \beta D^2)} \quad (3.1)$$

where α and β respectively represents the rate of cell killing by single ionizing events and by double hits. Hence, at high doses the density of damages increases and the cells repair becomes more difficult. Even if the FLASH effect is not yet completely understood and the underlying mechanisms are not clear, it looks like there are two different recipes which are involved:

²The first evidences has been observed on a mice experiments in 1966 and in 2014 by the group of Favaudon and Vozenin. After this, many test on cats and pigs have been performed, and also there has been a clinical trial on a cutaneous tumor-patient

584 • **The dose rate:** higher dose rate produce bigger damages (fig. 3.3(a)) since this prevent
 585 cells from sparing.

586 • **The presence or absence of oxygen:** while hypoxic cells are very resistant to radiation,
 587 normal oxygenated cells are highly radiosensitive. This is because if molecules containing O_2
 588 break due to the impinging radiation, then the oxygen can build Reactive Oxygen Species
 589 (ROS) (fig.3.3(b))

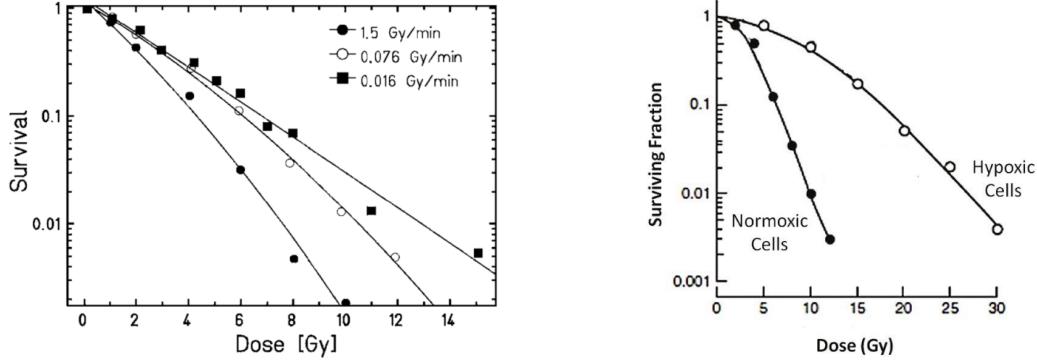


Figure 3.3: (a) Survival curve for different dose rate and (b) for different oxygen cell content

590 The Tumor Control Probability (TCP) and the Normal Tissue Complication (NTC) functions
 591 parametrize respectively the efficiency of damaging on the tumor after having released a certain
 592 dose and the probability of not affecting the healthy tissues. The intermediate zone between the
 593 increase of the TC and of the NTC is called therapeutic window, and the wider it is and the more
 594 effective the treatment is.

595 Dosimetric problems

596 Finding dosimeters suitable for online monitoring of the beam at ultra high dose rate is still an
 597 open issue since almost all standard online dosimeters show saturation problems. Differently,
 598 radiochromic films, which are the standard passive dosimeters, show dose-rate independence up to
 599 109 Gy/s. **Cosa sono i radiochromic films and they do not have the same accuracy of other detectors.**
 600 The principal detectors for reference dosimetry which provide real-time dose measurement are
 601 Ionization Chambers (IC), that show saturation issue at dose per pulse (DDP) two orders of
 602 magnitude lower than the ones used for FLASH-RT. **da qui in poi** ICs devono essere calibrate
 603 secondo la metrologia , per cui grazie a protocolli di calibrazione e introducendo dei fattori correttivi
 604 si riesce a fare una misura di dose. k_{sat} which accounts for the loss of charge collected due to
 605 recombination. Doppi problemi sia di saturazione dovuta a ion recombination sia di scariche, must
 606 be carefully accounted for: questo doppio effetto è dato dal fatto che, creandosi tante cariche nella
 607 camera, che va ad annullare il campo elettrico di drift. Questo ovviamente paralizza le cariche che
 608 non driftano più, ma che anzi si ricombinano ed inoltre facilita la formazione di scariche. Per DDP
 609 minori di 1 mGy il fattore correttivo è minore al 5%, poi però aumenta substantially.

610 Scintillators have reusable, non-exhaustible scintillation centers. However, the system has a
 611 total deadtime given by both the crystal scintillation time and the electronics read-out deadtime.

612 Semiconductors show a nonreversible saturation beyond a threshold around 15 cGy/p. The
 613 scintillator used, shows a negligible saturation up to 1 Gy/p, but it increases significantly up to at
 614 least 11 Gy/p, and it reaches a cutoff value between 11 and 36 Gy/p.

615 Scintillator dosimeters are widely used in radiotherapy. They are usually operating in counting-
 616 mode where each detected signal is processed by read-out electronics. However, the system has a
 617 total deadtime given by both the crystal scintillation time and the electronics read-out deadtime.
 618 When a scintillator dosimeter is used in integrator-mode the signal is integrated over the entire
 619 irradiation time. A deadtime, due to the decay time of the scintillating material, is considered on
 620 average every N recorded pulses, where N is the number of scintillation centres in the dosimeter.

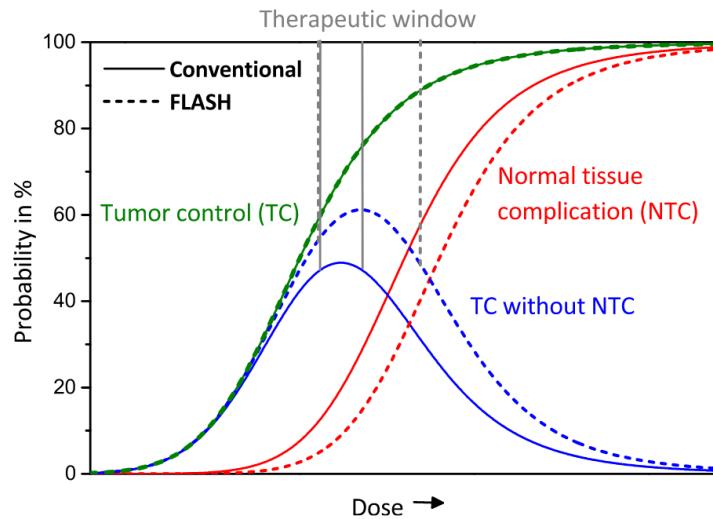


Figure 3.4: Illustration of dependence of TCP, NTCP and therapeutic window on dose, for CONV-RT ad FLASH-RT.

621 Besides saturation two other requirements for online dosimeters are high temporal and space res-
 622 olutions. Si potrebbe pensare di poter usare i pixel detector as beam monitor che hanno risoluzioni
 623 spaziali anche inferiori al 10 um e ris temporali -qua dare un valore è più difficile perchè per i
 624 maps la risoluzione temporale dipende da l occupancy. Uno dei problemi è però il lungo dead time
 625 introdotto dal lungo tempo di readout (ricorrdiamo che sopportano circa 100 Mhz/cm²).

626 **Chapter 4**

627 **TJ-Monopix1**

628 TJ-Monopix1 is a small electrode DMAPS with fast R/O capability, fabricated by TowerJazz
629 foundry in 180 nm CMOS imaging process. It is part, together with prototypes from other series
630 such as TJ-MALTA, of the ongoing R&D efforts aimed at developing DMAPS in commercial CMOS
631 processes, that could cope with the requirements at accelerator experiments. Both TJ-Monopix
632 and TJ-MALTA series [10], produced with the same technology by TowerJazz (the timeline of the
633 foundry products is shown in figure 4.1), are small electrode demonstrators and principally differ in
634 the readout design: while Monopix implements a column-drain R/O, an asynchronous R/O without
635 any distribution of BCID has been used by TJ-Malta in order to reduce power consumption.



Figure 4.1: Timeline in TowerJazz productions in 180 nm CMOS imaging process

636 Another Monopix series, but in 150 nm CMOS technology, has been produced by LFoundry [11].
637 The main differences between the LF-Monopix1 and the TJ-Monopix1 (summarized in table 4.2),
638 lay in the sensor rather than in the readout architecture, as both chips implements a fast column
639 drain R/O with ToT capability [12][13]. Concerning the sensors, either are based on a p-type
640 substrate, but with slightly different resistivities; in addition LFoundry pixels are larger, thicker
641 and have a large fill factor (the very deep n-well covers ~55% of the pixel area). The primary
642 consequence is that LF-Monopix1 pixels have a higher capacity resulting in higher consumption
643 and noise. As I discussed in section 2.4.1, the fact that LF-Monopix has a large fill factor electrode
644 is expected to improve its radiation hardness. Indeed, a comparison of the performance of the
645 two chips showed that TJ-Monopix suffers a comparatively larger degradation of efficiency after
646 irradiation, due to the low electric field in the pixel corner; on the other hand, a drawback of the
647 large fill factor in LF-Monopix is a significant cross-talk.

648 The TJ-Monopix1 chip contains, apart from the pixels matrix, all the required support blocks
649 used for configuration and testing:

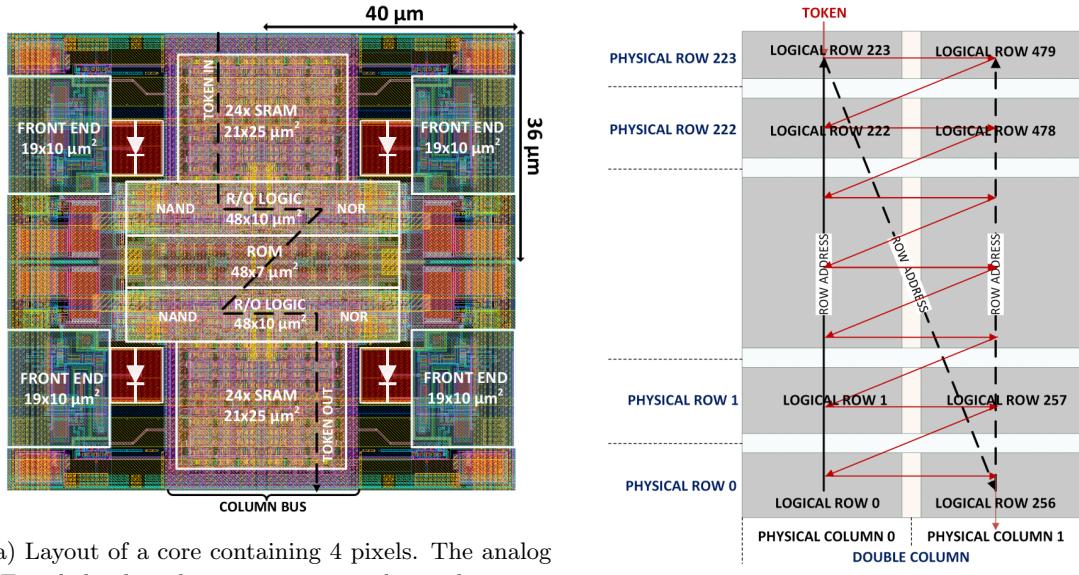
- 650 • the whole matrix contains 224×448 pixels, yielding a total active area approximately equal
651 to 145 mm^2 over a total area of $1 \times 2 \text{ cm}^2$;

	LF-Monopix1	TJ-Monopix1
Resistivity	$>2 \text{ k}\Omega\text{cm}$	$>1 \text{ k}\Omega\text{cm}$
Pixel size	$50 \times 250 \mu\text{m}^2$	$36 \times 40 \mu\text{m}^2$
Depth	$100\text{-}750 \mu\text{m}$	$25 \mu\text{m}$
Capacity	$\sim 400 \text{ fF}$	$\sim 3 \text{ fF}$
Preamplifier	charge	voltage
Threshold trimming	on pixel (4-bit DAC)	global threshold
ToT	8 bits	6 bits
Consumption	$\sim 300 \text{ mW/cm}^2$	$\sim 120 \text{ mW/cm}^2$
Threshold	$1500 e^-$	$\sim 270 e^-$
ENC	$100 e^-$	$\sim 30 e^-$

Table 4.1: Main characteristics of Monopix1 produced by TowerJazz and LFoundry [12][13]

- at the chip periphery are placed some 7-bit Digital to Analog Converter (DAC), used to generate the analog bias voltage and current levels and to configuire the FE;
- at the EoC is placed a serializer to transferred datas immediately, indeed no trigger memory is implemented in this prototypes;
- the matrix power pads are distributed at the sides
- four pixels which have analog output and which can be monitored with an oscilloscope, and therefore used for testing

Pixels are grouped in 2×2 cores (fig. 4.2a): this layout allows to separate the analog and the digital electronics area in order to reduce the possible interference between the two parts. In addition it semplifies the routing of data as pixels on double column share the same column-bus to EoC. Therefore pixels can be addressed through the physical column/row or through the logical column/row, as shown in fig. 4.2b: in figure is also highlighted the token propagation path, whose I will discuss later.



(a) Layout of a core containing 4 pixels. The analog FE and the digital part are separated in order to reduce cross-talk be

(b)

Parameter	Value
Matrix size	$1 \times 2 \text{ cm}^2$
Pixel size	$36 \times 40 \mu\text{m}^2$
Depth	$25 \mu\text{m}$
Electrode size	$2 \mu\text{m}$
BCID	40 MHz
ToT-bit	6
Power consumption	$\sim 120 \text{ mW/cm}^2$

Table 4.2

665 4.1 The sensor

666 As already anticipated, TJ-Monopix1 has a p-type epitaxial layer and a n doped small collection
 667 electrode ($2 \mu\text{m}$ in diameter); to avoid the n-wells housing the PMOS transistors competing for the
 668 charge collection, a deep p-well substrate, common to all the pixel FE area, is used. TJ-Monopix1
 669 adopts the modification described in section 2.4.2 that allows to achieve a planar depletion region
 670 near the electrode applying a relatively small reverse bias voltage. This modification improves the
 671 efficiency of the detector, especially after irradiation, however a simulation of the electric field in
 672 the sensor, made with the software TCAD (Technology Computer Aided Design), shows that a
 673 nonuniform field is still produced in the lateral regions of the pixel compromising the efficiency
 674 at the corner. Two variations to the process have been proposed in order to further enhance the
 675 transversal component of electric field at the pixel borders: on a sample of chip, which includes the
 676 one in Pisa, a portion of low dose implant has been removed, creating a step discontinuity in the
 677 deep p-well corner (fig. 4.3); the second solution proposed[MOUSTAKAS THESYS, PAG 58]
 678 consists in adding an extra deep p-well near the pixel edge. A side effect of the alteration in the
 679 low dose implant is that the separation between the deep p-well and the p-substrate becomes weak
 680 to the point that they cannot be biased separately to prevent the punchthrough.

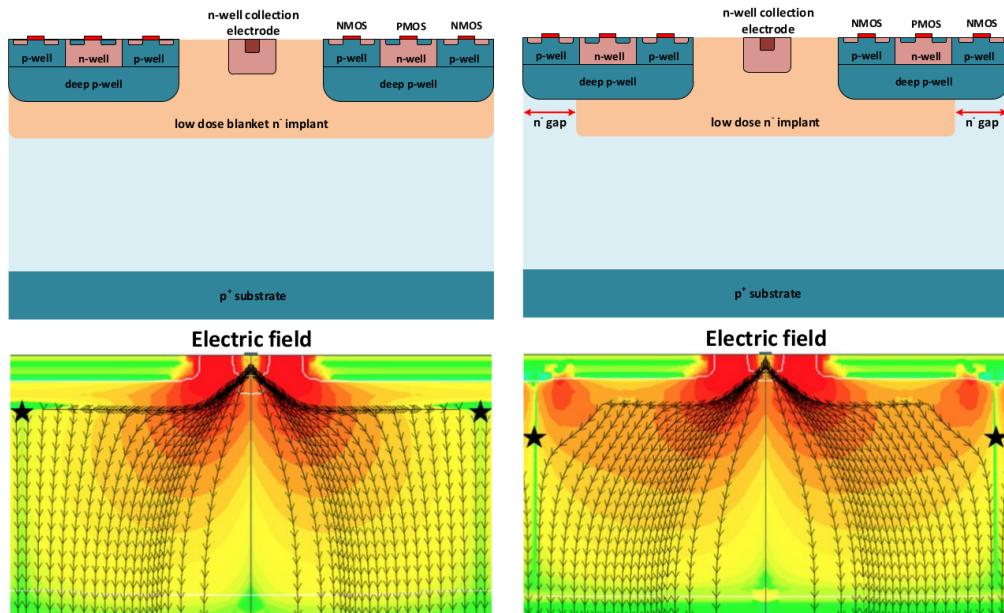


Figure 4.3: (a) The cross-section of a monolithic pixel in the TJ-Monopix with modified process; additionally in (b) a gap in the low dose implant is created to improve the collection of charge due to a bigger lateral component of the electric field. this point in figure is indicated by a star . transversal component of the electric field drops at the pixel corner

Moreover, to investigate the charge collection properties, pixels within the matrix are split between bottom top half and bottom half and feature a variation in the coverage of the deep p-well: the electronics area can be fully covered or not. In particular the pixels belonging to rows from 0 to 111 are fully covered (FDPW) and pixels belonging to rows from 112 to 223 have a reduced p-well (RDPW), resulting in a enhancement of the lateral component of the electric field.

4.2 Front end

The matrix is split in four sections, each one corresponding to a different flavor of the FE. The four variation have been implemented in order to test the data-bus readout circuits and the input reset modes.

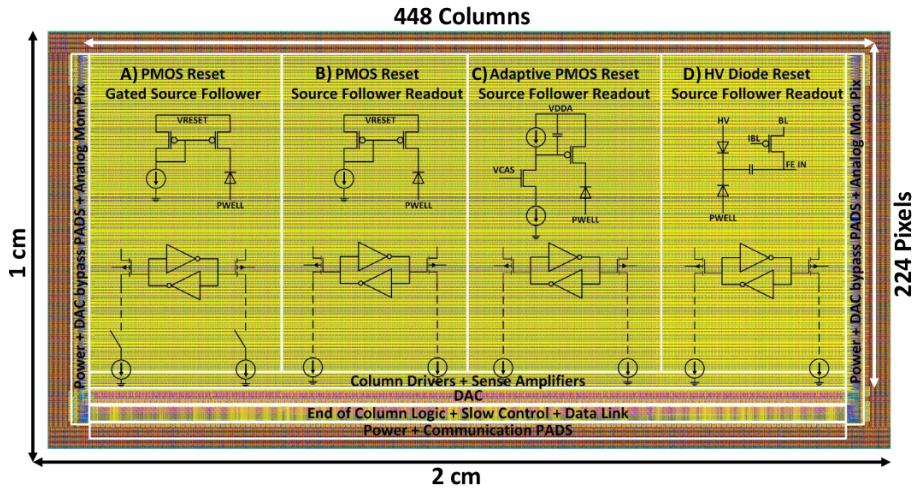


Figure 4.4

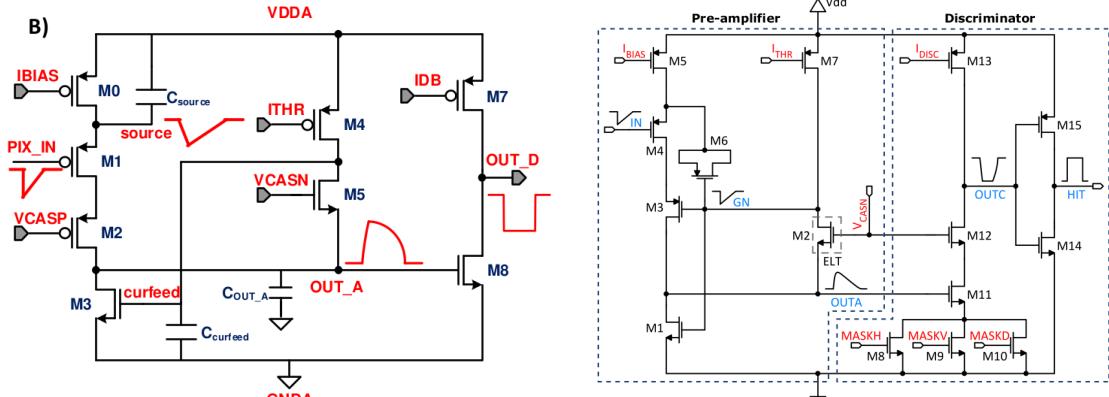
All the flavors implement a source-follower double-column bus readout: the standard variation is the flavor B, that features a PMOS input reset (referred as "PMOS reset"). Flavor A is identical to flavor B except for the realization of the source follower (it is a gated one) that aim to reduce the power consumption. **cosa significa?** C instead implements a novel leakage compensation circuit. Moreover the collection electrode in flavors A, B, C is DC-coupled to the front-end input, while in D is AC-coupled, providing to apply a high bias voltage; for this reason flavor D is called "HV flavor".

Principio generale: R resistenza di reset deve essere abbastanza grande in modo da far sì che il ritorno allo zero è abbastanza lento (non devi "interferire" con la tot slope e non deve essere più corto del tempo del preamplificatore, sennò hai perdita di segnale). **Baseline reset:** all'input solitamente hai un PMOSS o un diodo; R reset

4.2.1 ALPIDE-like

ALPIDE chips, developed by the ALICE collaboration, implemented a standard FE to the point that many CMOS MAPS detectors used a similar FE and are called "ALPIDE-like". Considering that both TJ-Monopix1 and ARCADIA-MD1 have an ALPIDE-like FE, I am going to explain the broad principles of the early FE stage. The general idea is of the amplification to transfer the charge from a bigger capacity[14], C_{source} , to a smaller one, C_{out} : the input transistor M1 with current source IBIAS acts as a source follower and this forces the source of M1 to be equal to the gate input $\Delta V_{PIX_IN} = Q_{IN}/C_{IN}$.

$$Q_{source} = C_{source} \Delta V_{PIX_IN} \quad (4.1)$$



(a) ALPIDE-like

(b)

709 The current in M2 and the charge accumulates on C_{out} is fixed by the one on C_{source} :

$$\Delta V_{OUT_A} = \frac{Q_{source}}{C_{OUT_A}} = \frac{C_{source} \Delta V_{PIX_IN}}{C_{OUT_A}} = \frac{C_{Source}}{C_{OUT_A}} \frac{Q_{IN}}{C_{IN}} \quad (4.2)$$

710 A second branch (M4, M5) is used to generate a low frequency feedback, where VCASN and ITHR
711 set the baseline value of the signal on C_{OUT_A} and the velocity to goes down to the baseline.

712 IL RUOLO DI CURVFEED NON L'HO CAPITO.

713 Finally IDB defines the charge threshold with which the signal OUT_A must be compared: de-
714 pending on if the signal is higher than the threshold or not, the OUT_D is high or low respectively.

715 The actual circuit implemented in TJ-Monopix1 is shown in figure 4.5b: the principal difference
716 lays in the addition of disableing pixels' readout. This possibility is uttermost important in order to
717 reduce the hit rate and to avoid saturating the bandwidth due to the noisy pixels, which typically
718 are those with manufacturing defects. In the circuit transistors M8, M9 and M10 have the function
719 of disabling registers with coordinates MASKH, MASKV and MASKD (respectively vertical, ori-
720 zontal and diagonal) from readout: if all three transistors-signals are low, the pixel's discriminator
721 is disabled. Compared with a configurable masking register which would allow disableing pixels
722 individually, to use a triple redundancy reduces the sensistivity to SEU but also gives amount of
723 intentionally masked ("ghost") pixels. This approach is suitable only for extremely small number
724 N of pixel has to be masked: if two coordinate projection scheme had been implemented, the N's
725 exponential is lower than 2 (fig. 4.6)

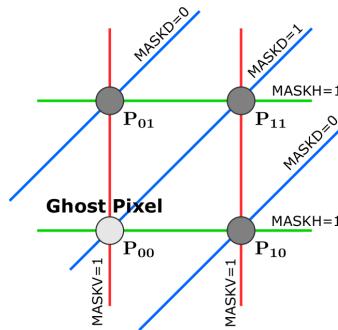


Figure 4.6

726

Parameter	Meaning	
IBIAS	mainly controls the rise time	yes? check
IDB	sets the discriminator threshold	yes
ITHR	sets the velocity of the return to the baseline	yes
ICASN	sets the baseline of the signal	yes
VRESET	sets the gain of the preamplifier	yes
IRESET	sets the gain of the preamplifier	no

Table 4.3: FE parameters which must be setted through the DAQ. "Function" means that higher parameter implies higher value

727 4.3 Readout logic

728 TJ-Monopix1 has a triggerless, fast and with ToT capability R/O which is based on a column-drain
 729 architecture. On the pixel are located two Random Access Memory (RAM) cells to store the 6-bit
 730 LE and 6-bit TE of the pulse, and a Read-Only Memory (ROM) containing the 9-bit pixel address.
 731 Excluded these memories, TJ-Monopix1 hasn't any other buffer: if a hit arrives while the pixel is
 732 already storing a previous one, the new data get lost. After being read, the data packet is sent to
 733 the EoC periphery of the matrix, where a serializer transfers it off-chip to an FPGA (4.7). There
 734 a FIFO is used to temporarily stored the data, which is transmitted to a computer through an
 735 ethernet cable in a later time.

736 The access to the pixels' memory and the transmission of the data to the EoC, following
 737 a priority chain, is managed by control signals and is based on a Finite State Machine (FSM)
 738 composed by four state: no-operation (NOP), freeze (FRZ), read (RD) and data transfer (DTA).
 739 The readout sequence (??) starts with the TE of a pulse: the pixel immediately tries to grab the
 740 column-bus turning up a hit flag signal called *token*. The token is used to control the priority chain
 741 and propagates across the column indicating what pixel that must be read. To start the readout
 742 and avoid that the arrival of new hits disrupt the priority logic, a *freeze* signal is activated, and
 743 then a *read* signal controls the readout and the access to memory. During the freeze, the state of
 744 the token for all pixels on the matrix remains settled: this does not forbid new hits on other pixels
 745 from being recorded, but forbids pixels hit from turning on the token until the freeze is ended. The
 746 freeze stays on until the token covers the whole priority chain and gets the EoC: during that time
 747 new token cannot be turned on, and all hits arrived during a freeze will turn on their token at the
 748 end of the previous freeze. Since the start of the token is used to assign a timestamp to the hit,
 749 the token time has a direct impact on the time resolution measurement; this could be a problem
 750 coping with high hits rate.

751 The analog FE circuit and the pixel control logic are connected by an edge detector which is
 752 used to determine the LE and the TE of the hit pulse(fig. 4.9): when the TE is stored in the first
 753 latch the edge detector is disabled and, if the **FREEZE** signal is not set yet, the readout starts. At
 754 this point the HIT flag is set in a second latch and a token signal is produced and depending on
 755 the value of **Token in** the pixel can be read or must wait until the **Token in** is off. In figure an OR
 756 is used to manage the token propagation, but since a native OR logic port cannot be implemented
 757 with CMOS logic, a sum of a NOR and of an inverter is actually used; this construct significantly
 758 increases the propagation delay (the timing dispersion along a column of 0.1-0.2 ns) of the token
 759 and to speed up the circuit optimized solution are often implemented. When the pixel become the
 760 next to be read in the queue, and at the rising edge of the **READ** signal, the state of the pixel is
 761 stored in a D-latch and the pixel is allowed to use the data bus; the TE and the HIT flag latches
 762 are reset and a **READINT** signal that enable access of the RAM and ROM cells is produced.

763
 764 The final data must provide all the hits' information: the pixel address, the ToT and the
 765 timestamp. All those parts are assigned and appended at different time during the R/O chain:

- 766 • **Pixel address:** while the double column address (6-bit) is appended by the EoC circuit,

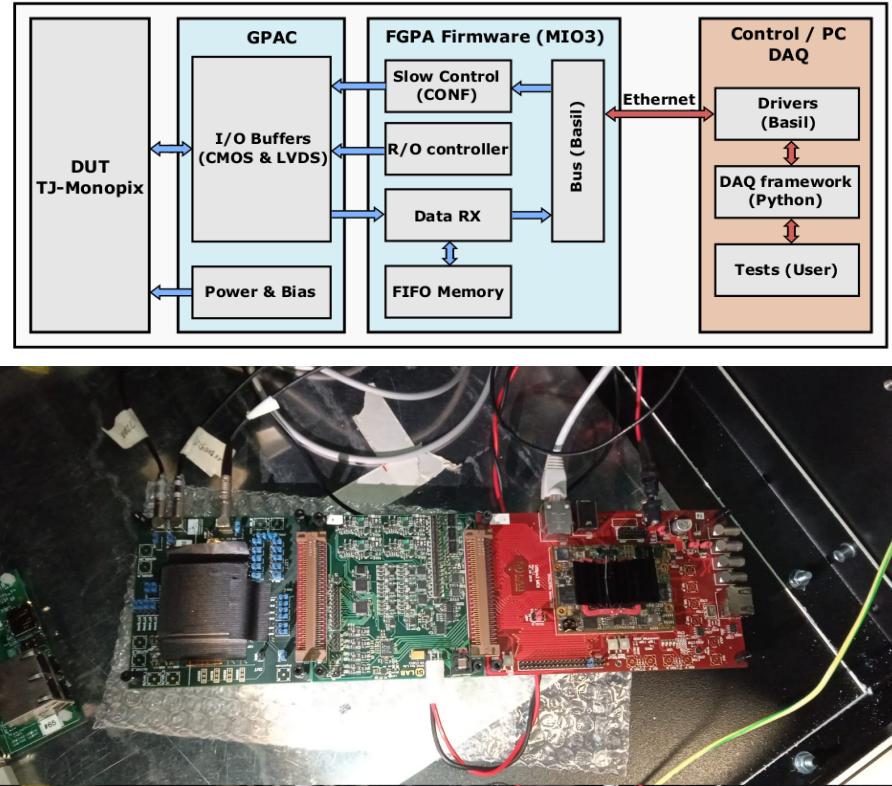


Figure 4.7: Main caption

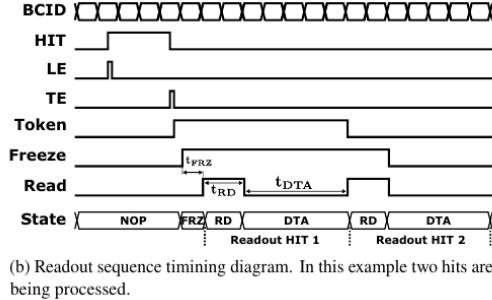


Figure 4.8: Readout timing diagram: in this example two hits are being processed

the row address (8-bits for each flavor) and the physical column in the doublet (1-bit) are assigned by the in-pixel logic

- **ToT:** is obtained offline from the difference of 6-bits TE and 6-bits LE, stored by the edge detector in-pixel; since a 40 MHz BCID is distributed across the matrix, the ToT value is range 0-64 clock cycle which corresponds to 0-1.6 μ s
- **Timestamp:** The timestamp of the hit correspond to the time when the pixel set up the token; it is assigned by the FPGA, that uses the LE, TE and a 640 MHz clock to derive it. For all those hits which arrived while the matrix is frozen, the timestamp is no more correlated with the time of arrival of the particle

When the bits are joined up together the complete hit data packet is 27-bit.

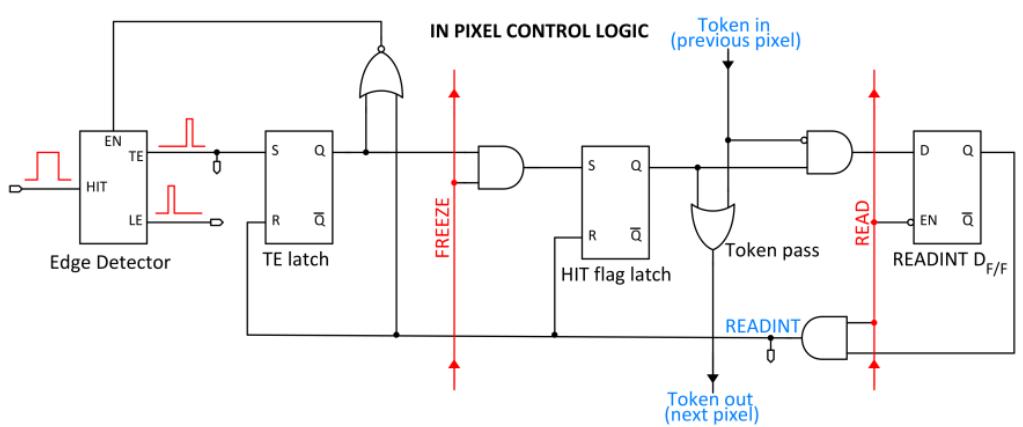


Figure 4.9

777 Chapter 5

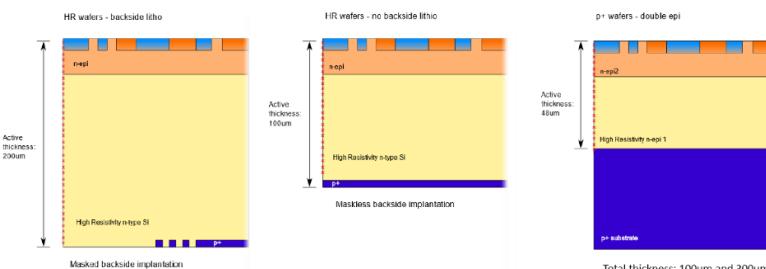
778 Arcadia-MD1

779 [15] [16]

780 Breve introduzione analoga a Monopix1 in cui descrivo brevemente la "timeline" da SEED
781 Matisse a Md1 e Md2

782 5.1 The sensor

783 ARCADIA-MD1 is an LFoundry chip which implements the technology 110 nm CMOSS node
784 with six metal layer ??. The standard p-type substrate was replaced with an n-type floating zone
785 material, that is a tecnique to produce purified silicon crystal. (pag 299 K.W.).



786 Figure 5.1

787 Wafer thinning and backside lithography were necessary to introduce a junction at the bottom
788 surface, used to bias the substrate to full depletion while maintaining a low voltage at the front side.
789 C'è un deep pwell per - priority chainseparare l'elettronica dal sensore; per controllare il punchthrough
790 è stato aggiunto un n doped epitaxial layer having a resistivity lower than the substrate.

791 RILEGGI SUL KOLANOSKY COS'È IL PUNCHTHROUGHT, FLOAT ZONE MATERIAL,
792 COME VENGONO FATI I MAPS COME FAI LE GIUNZIONI

793 It is part of the cathegory of DMAPS Small electrode to enhance the signal to noise ratio.

794 It is operated in full depletion with fast charge collection by drift.

795 Prima SEED si occupa di studiare le prestazioni: oncept study with small-scale test struc-
796 ture (SEED), dopo arcadia: technology demonstration with large area sensors Small scale demo
797 SEED(sensor with embedded electronic developement) Quanto spazio dato all'elettronica sopra il
798 pwell e quanto al diodo. ..

799 5.2 Readout logic and data structure

800 5.2.1 Matrix division and data-packets

801 The matrix is divided into an internal physical and logical hierarchy: The 512 columns are divided
 802 in 16 sections: each section has different voltage-bias + serializzatori. Each section is divided in
 803 cores () in modo che in ogni doppia colonna ci siano 1Pacchetto dei dati 6 cores. ricordati dei
 serializzatori: sono 16 ma possono essere ridotti ad uno in modalità spazio

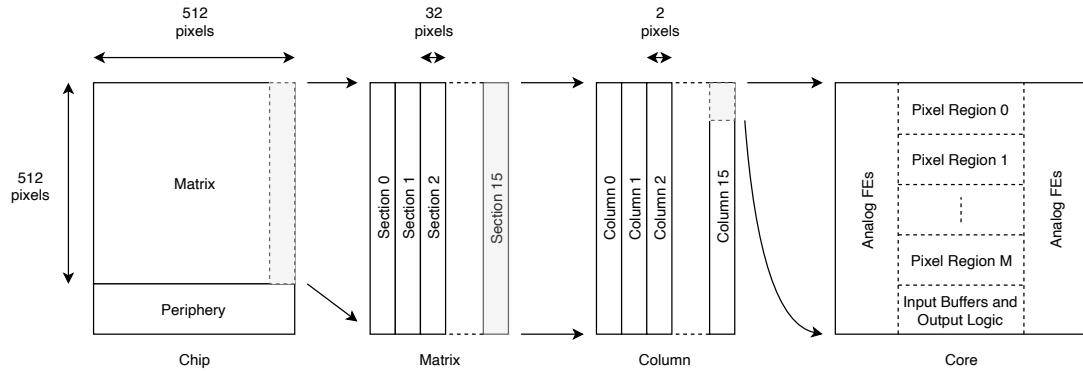


Figure 5.2

804

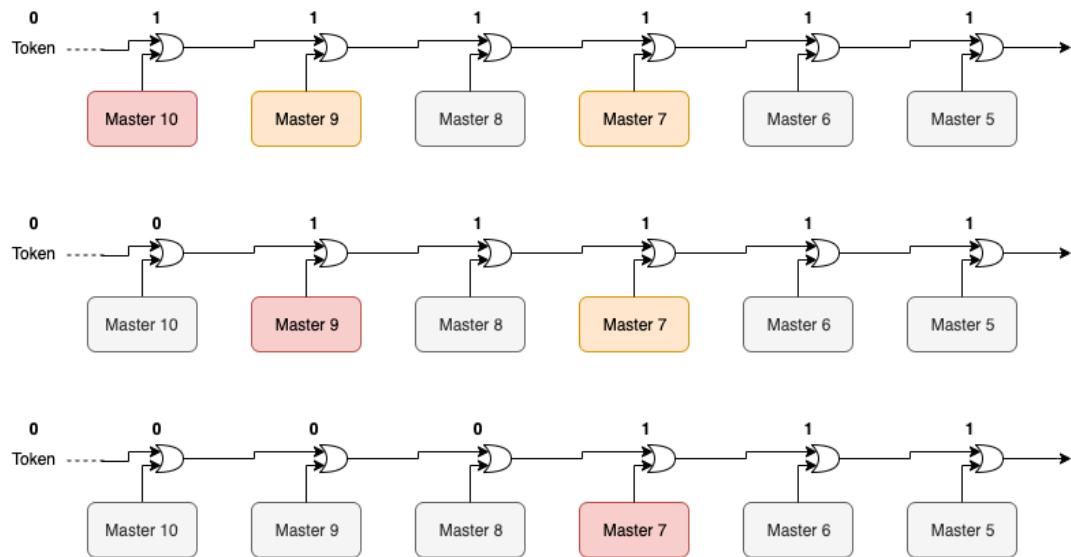


Figure 5.3

805 Questa divisione si rispecchia in come sono fatti i dati: scrivi da quanti bit un dato è fatto e le
 806 varie coordinate che ci si trovano dentro; devi dire che c'è un pixel hot e spieghi dopo a cosa serve,
 807 e devi accennare al timestamp

808 "A core is simply the smallest stepped and repeated instance of digital circuitry. A relatively
 809 large core allows one to take full advantage of digital synthesis tools to implement complex func-
 810 tionality in the pixel matrix, sharing resources among many pixels as needed.". pagina 28 della
 811 review.

812

813 TABELLA: con la gerarchia del chip Matrix (512x512 pixels) Section (512x32 pixels) Column
 814 (512x2) Core (32x2) Region (4x2)

815 Nel chip trovi diverse padframe: cosa c'è nelle padframe e End of section.

816 "DC-balance avoids low frequencies by guaranteeing at least one transition every n bits; for
 817 example 8b10b encoding n =5"

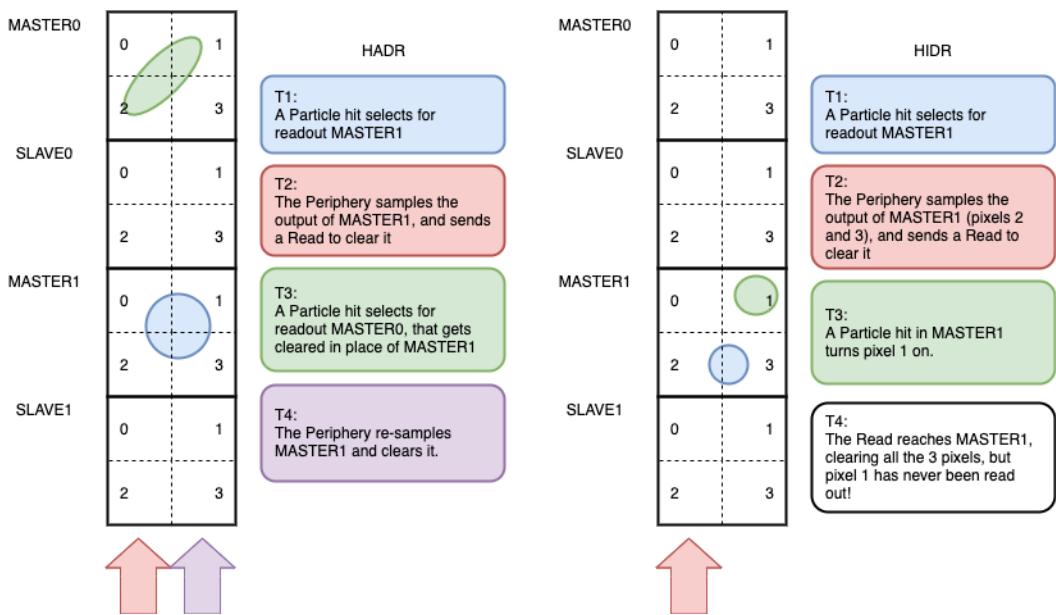


Figure 5.4

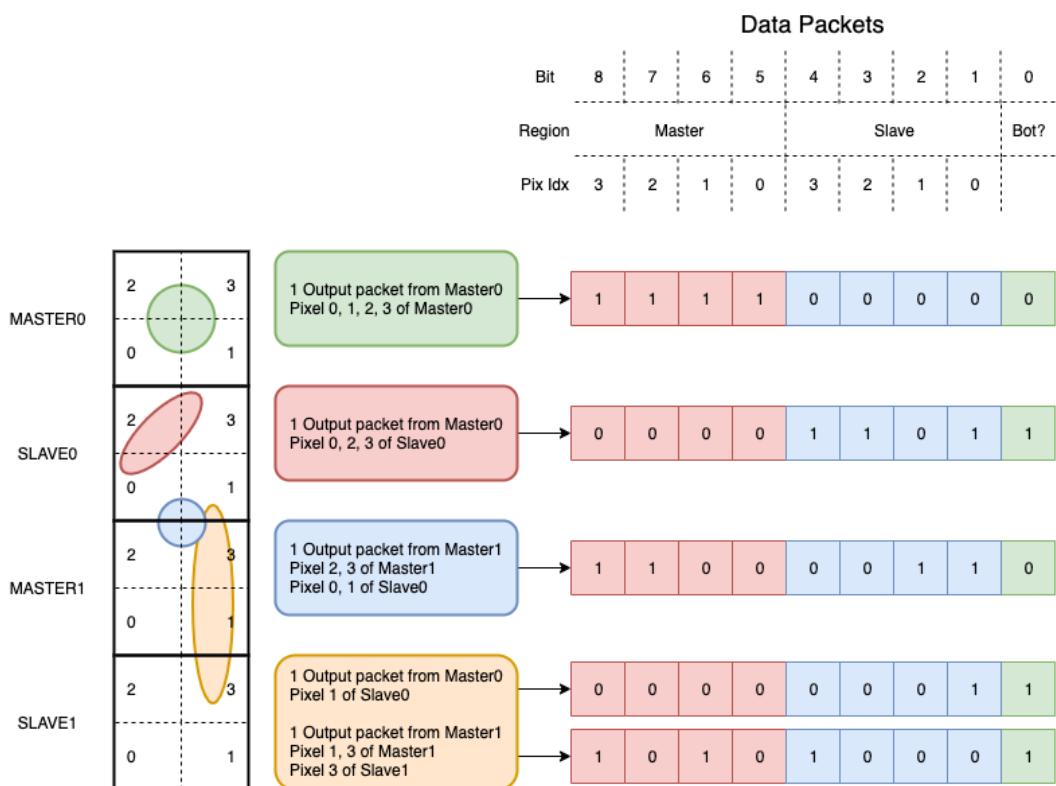


Figure 5.5

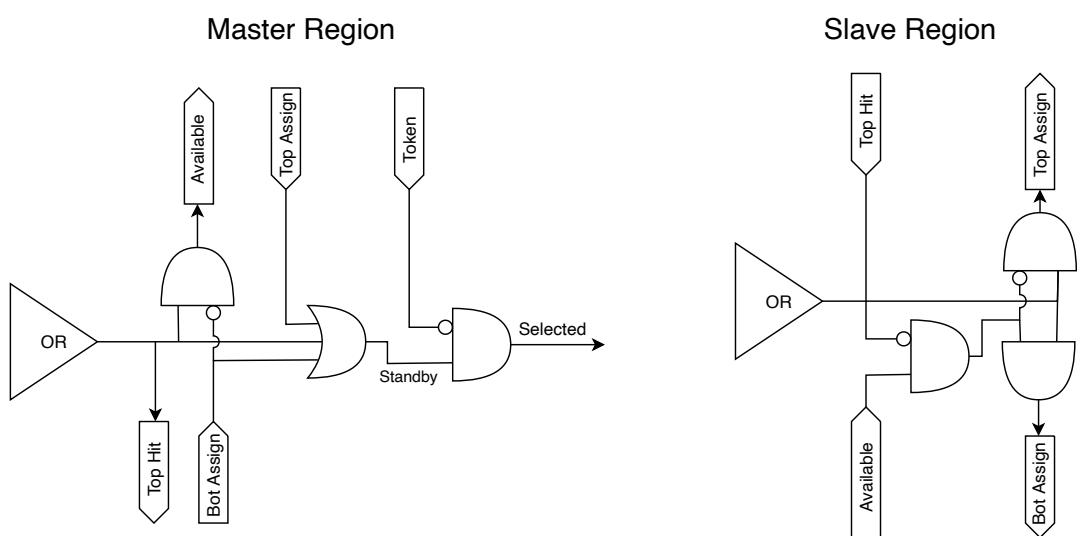


Figure 5.6

818 **Chapter 6**

819 **Characterization**

820 **6.1 TJ-Monopix1 characterization**

821 **6.1.1 Threshold and noise: figure of merit for pixel detectors**

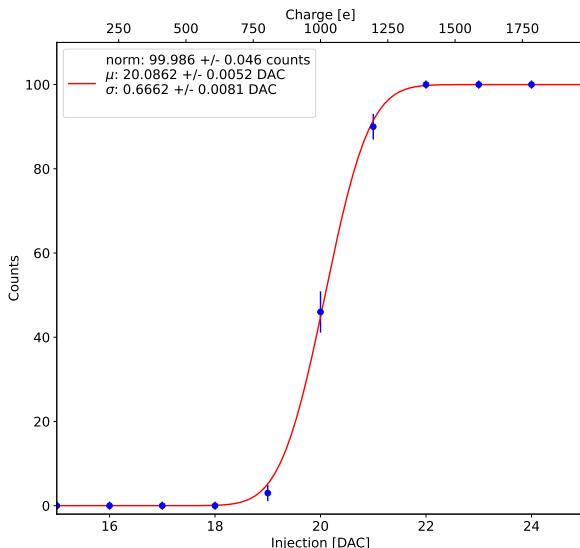


Figure 6.1: S-curve for pixel (10, 10) of the PMOS flavor (flavor 1) with IDB fixed at 40 DAC. The conversion of charge injected from DAC to electrons has been done assuming a conversion factor of 20 e-/DAC.

822 A characterization of threshold and noise is typically necessary since these values have an
823 impact on the operating conditions and on the performance of the chips. infact, the signal to
824 threshold ratio may be considered as the figure of merit for pixel detectors rather than the signal
825 to noise ratio. The mean minimum stable threshold evolved through different generation of chips:
826 in the 1st generation it was around 2500 e⁻ while in the 3rd (corresponding to nowadays chips) is
827 less than 500 e⁻. This allows in thinner sensors with smaller signals: from 16 000 e⁻ produced in
828 200 μ m, the signal expected moved down to 2000 e⁻ produced in 25 μ m. According with this ??,
829 the threshold of TJ-Monopix1 is around 500 e⁻. The following prototypes have lower threshold
830 and noise, for example TJ-Monopix2 has check, according to the above. The threshold has to be
831 located between the noise peak around the baseline and the signal distribution, in particular it has
832 to be low enough to mantain a high signal efficiency, but also high enough to cut the noise: for
833 a low threshold many pixels can fire at the same time and a positive feedback can set off a chain

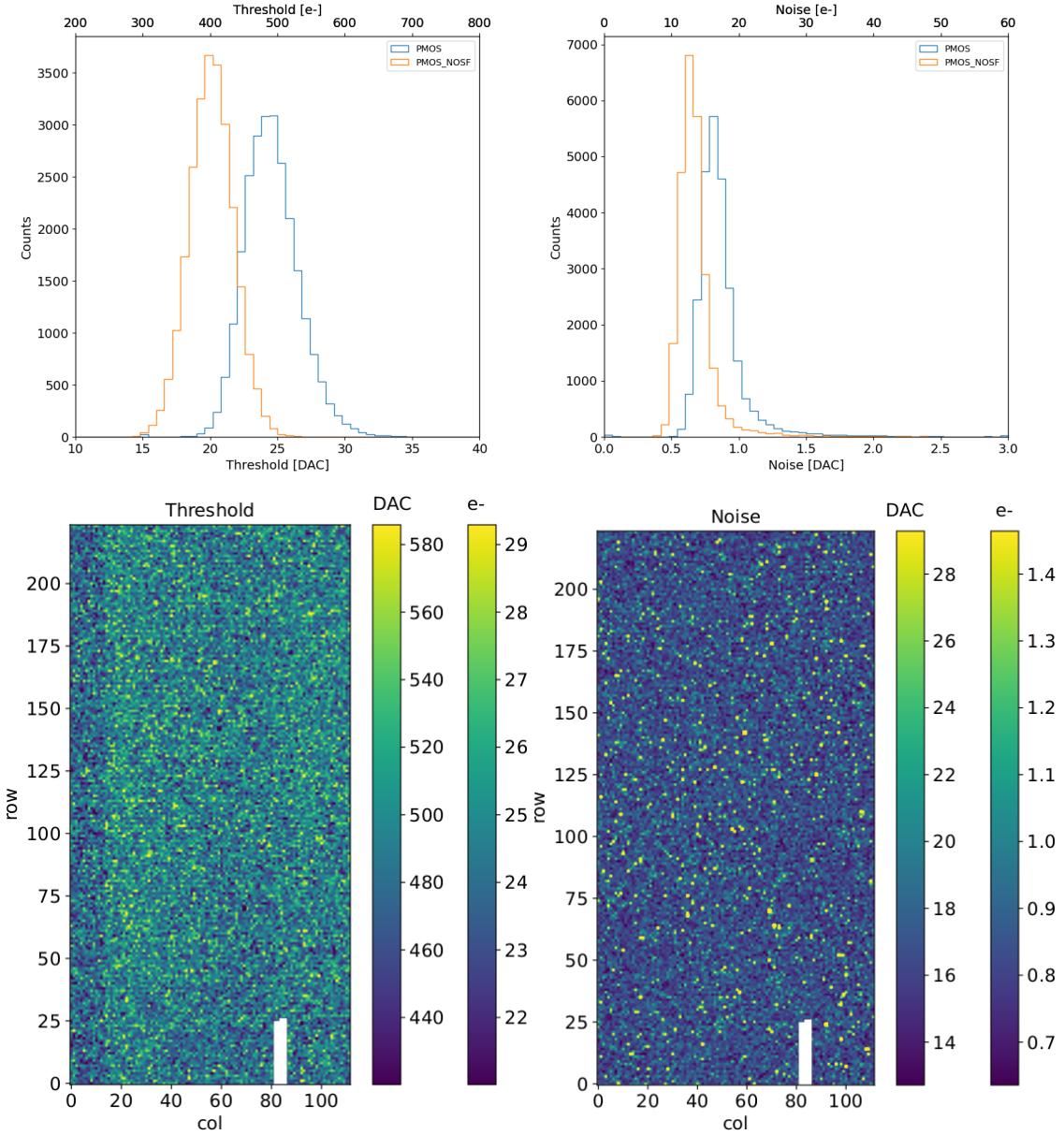


Figure 6.2: Histograms of the threshold (a) and the noise (b) found fitting the s-curve of all flavor with IDB fixed at 40 DAC. Maps of the threshold (a) and the noise (b) found fitting the s-curve with IDB fixed at 40 DAC. The white pixels have the injection circuit broken.

reaction eventually, causing all the other pixels to fire. Thus, the noise sets a lower bound to the threshold: if an occupancy $\leq 10^{-4}$ is required, for example, this correspond to the Gaussian 1-sided tail fraction for 3.7σ . In this case, if the noise is 100 e- , for example, the threshold must be higher than $3.7 \times 100\text{ e-}$. Typically this argument sets only a minimal bound to the threshold since the variation with time and from pixel to pixel have to be taken into account: the temperature, the annealing (for example, the radiation damages in the oxide layer causes shift of MOSFET threshold voltage) and the process parameters variation across the wafer (as for example process mismatch between transistors).

On the other hand the noise is valued at the preamplifier input node given that the first stage of amplification is the most crucial since the signal amplitude is high compared to additional noise introduced by the following stages. The noise is then parameterized as Equivalent Noise Charge (ENC), which is defined as the ratio between the noise N at the output expressed in Volt and the

846 out voltage signal S produced by $1e-$ entering in the preamplifier:

$$ENC = \frac{N_{out}[V]}{S_{out}[V/e-]} = \frac{V_{noise}^{RMS}}{G} \quad (6.1)$$

847 with G expressed in V/e-; as the gain increases, the noise reduces .

848 **ENC is mainly influenced by the total input capacitance and by the system bandwidth: if the**
849 bandwidth is constant, noise can be reduced by increasing the input transistor transconductance
850 (and consequently power consumption). Considering the threshold dispersion a requirement for
 851 the ENC is:

$$ENC < \sqrt{(T/3.7)^2 - T_{RMS}^2(x) - T_{RMS}^2(t)} \quad (6.2)$$

852 where the T is the threshold set, T_{RMS} is the threshold variation during time (t) and across the
 853 matrix (x); a typical reasonable value often chosen is 5 ENC.

854 Because of the changing of the 'real' threshold, the possibility of changing and adapting the
 855 setting parameters of the FE, both in time and in space is desiderable: these parameters are
 856 usually set by Digital to Analog Converter (DAC) with a number of bit in a typical range of 3-
 857 7. Unfortunately DAC elements require a lot of space that may be not enough on the pixel area;
 858 therefore, the FE parameters are typically global, which means that they are assigned for the whole
 859 chip, or they can be assigned for regions the matrix is divided into. The former case corresponds to
 860 TJ-Monopix1's design in which 7 bits are used for a total 127-DAC possible values, while the latter
 861 corresponds to the ARCADIA-MD1's one, **where quanti bit??**. An other possibility, for example
 862 implemented in TJ-Monopix2, is allocate the space on each pixel for a subset of bits, then combinig
 863 the global threshold with a fine tuning. If so, the threshold dispersion after tuning is expected to
 864 be inversely proportional to the tuning DAC number of bits and thus be improved a lot:

$$\sigma_{THR,tuned} = \frac{\sigma_{THR}}{2^{nbit}} \quad (6.3)$$

865 where σ_{thr} is the RMS of the threshold spread before tuning.

866 To measure the threshold and noise of pixels a possible way is to make a scan with different
 867 known injected charge: the threshold corresponds to the value where the efficiency of the signal
 868 exceeds the 50%, and the ENC is determined from the width of this edge. Following this path, I
 869 have used the injection circuit available on the chip to inject 100 pulses for each input charge for
 870 a fixed threshold. The injection comes on a capacity at the input of the FE circuit, whose mean
 871 value is 230 aF and from which the conversion factor from DAC units to electrons can be obtained:
 872 for the PMOS flavor, for example, since the DAC are biased at 1.8 V, the Least significant Bit
 873 (LSB) corresponds to a voltage of 14.7 mV from which the charge for LSB $1.43 e-/mV$ and the
 874 conversion factor therefore is $20.3 e-/DAC$. While this value is equivalent for all the PMOS flavor,
 875 the HV flavor is expected to have a different conversion factor, $\sim 33 e-/DAC$, beacuse of the
 876 different input capacity.

877 Besides the charge, also the duration and the period of the injection pulse can be set; it is
 878 important to make the duration short enough to have the falling edge during the dead time of
 879 the pixel (in particular during the FREEZE signal) in order to avoid the undershoot, coming at
 880 high input charge, triggering the readout and reading spurious hits. Since the injection circuit is
 881 coupled in AC to the FE, if the falling edge of the pulse is sharp enough to produce ad undershoot,
 882 this can be seen as a signal.

883 Assuming a gaussian noise, the efficiency of detecting the signal can be described through a
 884 modification of the error function:

$$f(x, \mu, \sigma) = \frac{1}{2} \left(1 + erf \left(\frac{x - \mu}{\sigma\sqrt{2}} \right) \right) \quad (6.4)$$

885 with: where the threshold and the ENC corresponds to the μ and σ . Therefore I perform a fit of
 886 the counts detected using the function in equation 6.4. In figure 6.1 there is an example with IDB
 887 (look at table 7.2 for the meaning of the parameter) equal to 40 DAC of fit for a pixel belonging
 888 to the flavor 1, while in table 7.2 and figure ?? and ?? there are the histograms and the maps of
 889 the parameters of the scurve-fit.

	DAC units	electrons
Threshold	24.529 ± 0.049 u: 24.433 ± 0.049 d: 24.623 ± 0.051	
Threshold dispersion	1.848 ± 0.033 u: 1.867 ± 0.034 d: 1.825 ± 0.035	
Noise	0.8222 ± 0.0043 u: 0.8225 ± 0.0045 d: 0.8221 ± 0.0043	
Noise dispersion	0.0975 ± 0.0030 u: 0.0968 ± 0.0031 d: 0.0970 ± 0.0030	

Table 6.1: Flavor PMOS, IDB fixed at 40 DAC

890 To verified the trend of the threshold as a function of the front end parameter IDB, I have
 891 performed a scan with different IDBs: I have injected the whole matrix and found the means and
 the standard deviation of the distributions. The results are shown in figure 6.3.

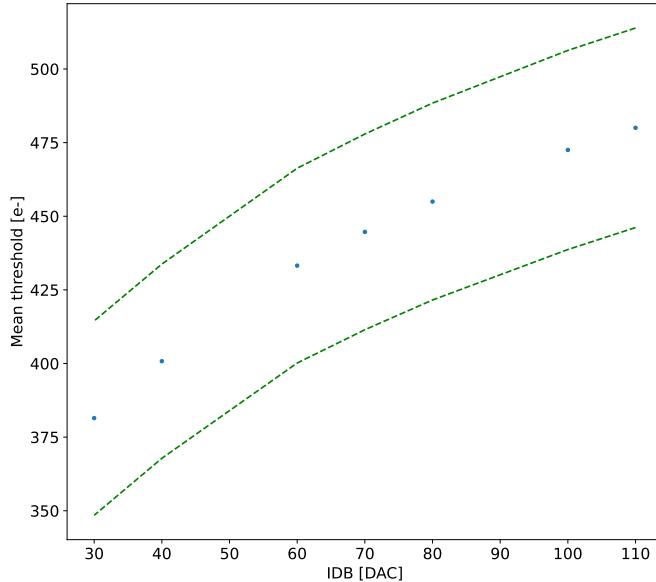


Figure 6.3

892

6.1.2 Linearity of the ToT

894 I have already said in chapter ?? that TJ-Monopix1 returns an output signal proportional to the
 895 charge released by a particle in the epitaxial layer, which is the Time over Threshold; the ToT is
 896 saved as a 6-bit variable and then has a dynamic range equal to 0-64, which corresponds to $0\text{ }\mu\text{s}$ to
 897 $1.6\text{ }\mu\text{s}$ assuming a clock frequency of 40 MHz. When a pulse is longer than $1.6\text{ }\mu\text{s}$ the counter rolls
 898 back to zero and there is no way to distinguish that charge from a lower one with the same ToT:
 899 that is the rollover of the ToT (??(a)).

900 In order to associate the ToT (in range 0-64) to the charge, a calibration of the signal is
 901 necessary. Assuming the linearity between ToT and the charge, Q can be found:

$$Q [DAC] = \frac{(ToT [au] - q [au])}{m [au/DAC]} \quad (6.5)$$

902 where m and q are the fitted parameters of the calibration. It is important to keep in mind that
 903 the main application target of TJ-Monopix1 is in the inner tracker detector of HEP experiments,
 904 then the main feature is the efficiency, then a rough calibration of the signal to charge is fine; **this**
 905 **information can be used to improve??.**

906 The study of the output signal is made possible via the injection: since the pulses are triangular,
 907 the ToT is expected to be almost linear depending on the injection charge value. To verify this
 908 statement and study the deviations from linearity I've fit the ToT versus the charge injected for
 909 all pixel within the matrix. In figure ??(b) there is an example of fit for a pixel belonging to the
 910 flavor 1, while in figure 6.5 and ?? there are the histograms and the maps of the parameters of the
 911 line-fit for all flavors with IDB fixed at 40 DAC.

912 Before performing the fit I have calculated the mean value of the ToT of the pulses recorded
 913 for each pulse amplitude and I used the mean ToT as value for the fit. The aim of the calibration
 914 obviously is finding a relation only in the range 0-64 without taking into account the rolling over
 915 hits: therefore, to prevent the rollover data from reducing the mean ToT introducing a bias in the
 916 mean value, I cut and I did not consider them. If a signal bigger than the 1.6 μ s is expected in
 917 the usage of the detector, the threshold must be raised or the gain reduced, making the expected
 918 output signal in range 0-64. In figure ?? (b) are shown both the fits with a line (red) and with
 919 a second order polynomial (green): at the bounds of the ToT range values deviate from the line
 920 model. Since the deviation is low than **controlla quant'era, questo ha un impatto sul valore della**
 921 **calibrazione di tipo .. che è trascurabile al primo ordine. e comunque hai problemi ai bounds**
 922 **mentre al centro hai comunque una buona linearità.. Chiarire la differenze negli istogrammi e**
 923 **come mai ci sono delle bande nelle mappe.**

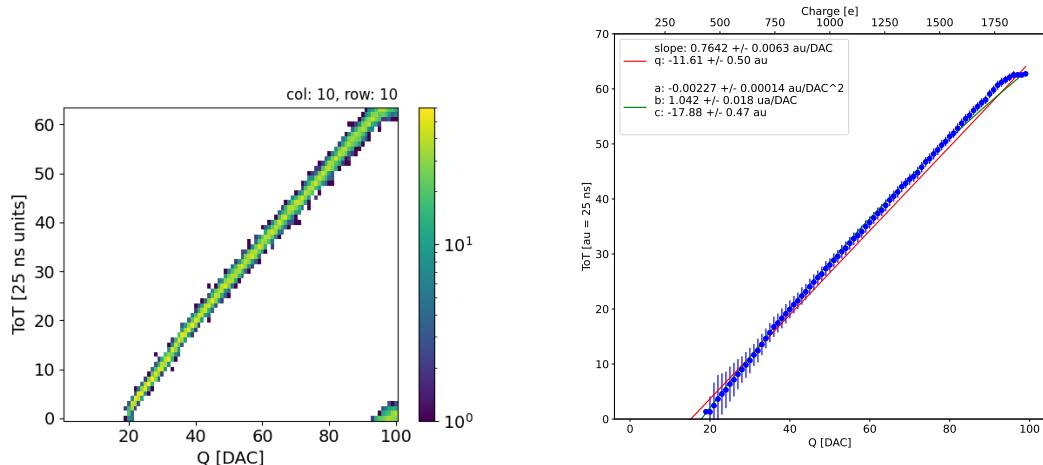


Figure 6.4: The figures refer to pixel (10,10) of the PMOS-reset flavor (1) with IDB fixed at 40 DAC. (a) Histogram of the injection pulses: the ToT is in range 0-64 since it is represented by 6 bit, so when achieving the 64 it rolls over back to the zero. (b) Mean ToT vs the the charge: the mean has been calculated cutted the rolling hits.

924 perchè noise e th sono diverse per diversi flavor? Controlla cos' hanno di diverso.

925 6.1.3 Calibration of the ToT

926 Considering that the charge injected in the FE goes to fill capacitor which is different from pixel to
 927 pixel, the true charge injected does not correspond to what expected assuming C equal to 230 aF,

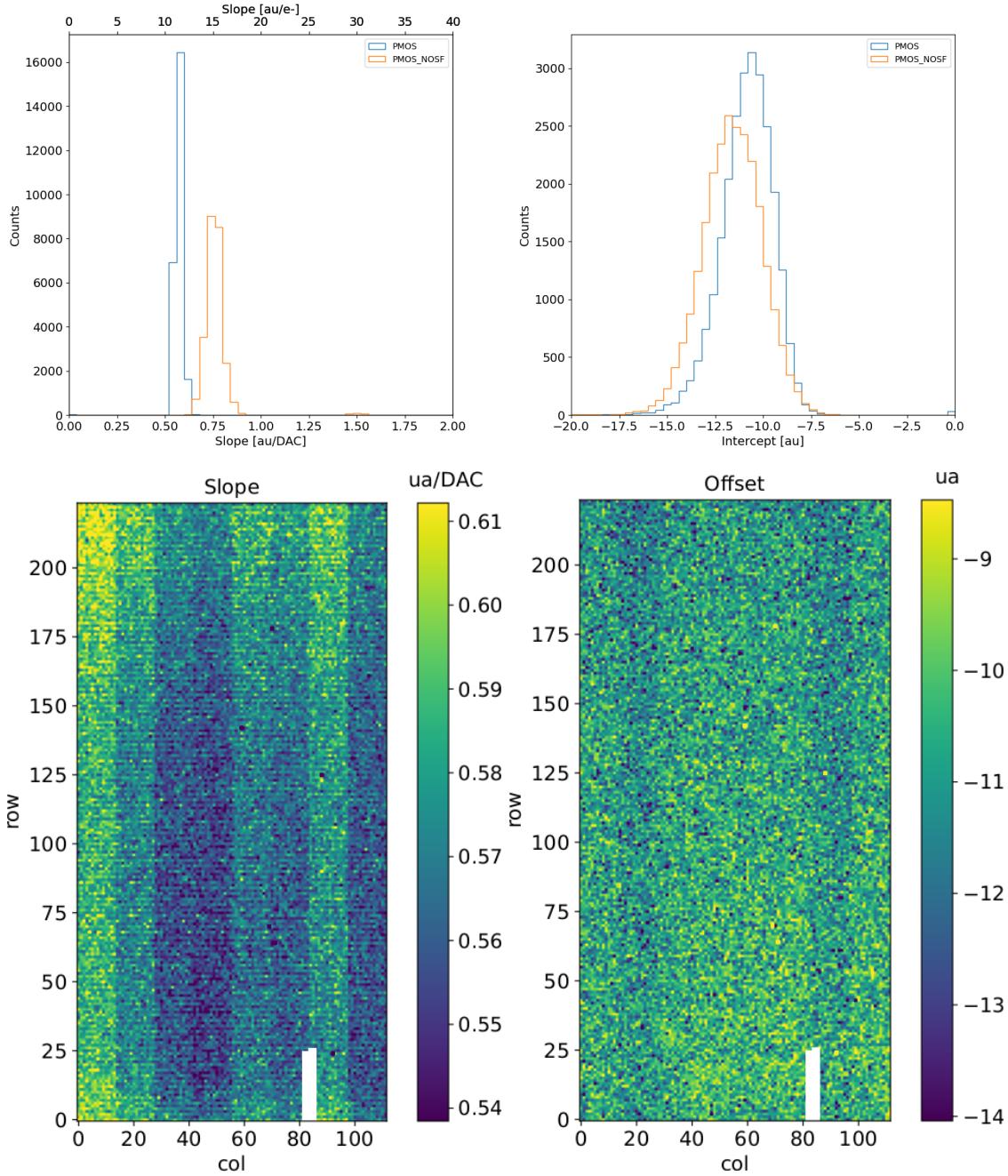


Figure 6.5: Histograms of the calibration parameters, slope (a) and offset (b), found fitting the ToT with a line, for all flavor and with IDB fixed at 40 DAC. Maps of the calibration parameters, slope (a) and offset (b), found fitting the ToT with a line, with IDB fixed at 40 DAC

the nominal value. Accordingly to that, a verification of the value provided and an absolute calibration of this capacity and of the conversion factor F is needed to have a correspondence of the signal in electrons; assuming $C = 230 \text{ aF}$, F is expected to be $20 \text{ e-}/\text{DAC}$, and is defined as:

$$F [\text{e-} / \text{DAC}] = \frac{1616 \text{ e-}}{Q [\text{DAC}]} \quad (6.6)$$

For this purpose a Fe55 radioactive source has been employed; the Fe55 is an extremely important radionuclide in the calibration of X-ray spectrometers, proportional counter and scintillator detector since it emits two X-photons during the electron capture decay: the first one (K_α) at 5.9 keV and the second one (K_β) at 6.5 keV. The K_α photon, which does photoelectric effect

	PMOS 0	PMOS 1	PMOS 2	HV
Slope [au/DAC]	0.75566 ± 0.00149	0.57145 ± 0.00025		
Slope dispersion [au/DAC]	0.03841 ± 0.00037	0.01685 ± 0.00016		
Intercept [au]	-11.6070 ± 0.0089	-10.824 ± 0.019		
Intercept dispersion [au]	1.5176 ± 0.0063	1.225 ± 0.013		

Table 6.2: Mean calibration parameters for all flavor and their dispersion on the matrix.

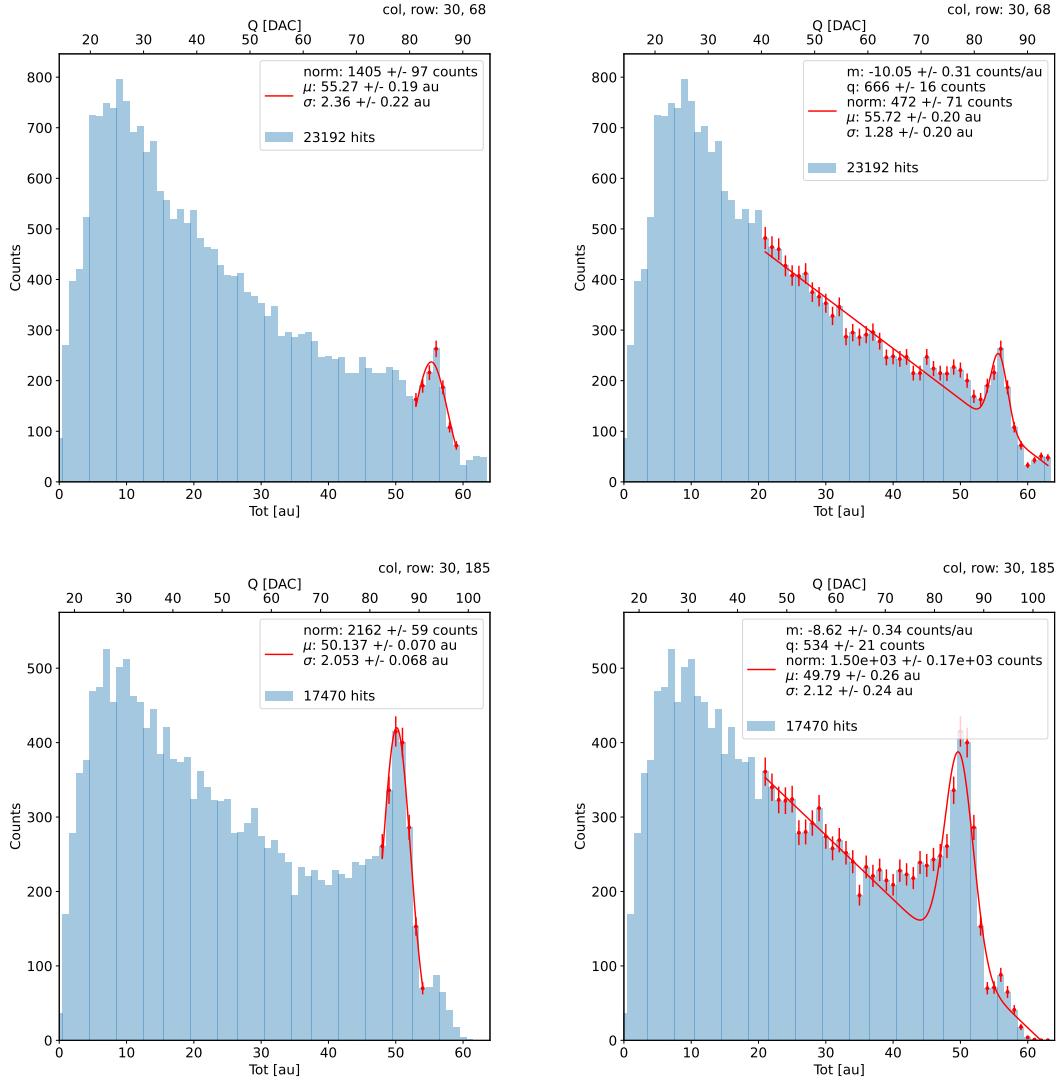


Figure 6.6: due pixel per far vedere la differenza tra i fit. Sottolinea che in rosso ci sono i bin fittati. La doppia scala utilizza le info trovate nel paragrafo precedente per il dato pixel in questione. Per avere una corrispondenza grezza in elettroni basta moltiplicare per 20 e- / dac.

in the silicium, has an absorption length $\lambda=7\text{ }\mu\text{m}$ to $8\text{ }\mu\text{m}$, and the probability of being absorbed in the $25\text{ }\mu\text{m}$ thick epitaxial layer is ~ 0.95 . The electron emitted has an energy equal to the photon one, so recalling that the mean energy needed to produce a couple electron-vacuum is 3.65 eV , the signal produced by the Fe55 source is expected to be 1616 e- . In figures ?? and ?? are shown two histograms of the ToT spectrum of the Fe55 source for two different pixels. The peak corresponds to the events with completely absorption of the charge produced in the depleted region, while the long tail on the left to all the events with partial absorption due to charge sharing among

942 neighbors pixels. In order to reduce the charge sharing, the pixel dimension in TJ-Monopix2 has
 943 been reduced down to **check**. The events on the right side of the peak, instead, corresponds to
 944 the K_β photons. Looking at the histograms for pixel (30, 185) and (30,69) a significant difference
 945 in the peak to tail ratio leaps out. This difference in the efficiency of detecting the signal can be
 946 related with the position of the pixel in the matrix: in particular pixels in the upper part of the
 947 matrix (rows 112-224) have a more prominent peak, while in pixels in the lower part (rows 0-111)
 948 there is a higher partial absorption. I recall now that there is a slightly difference in the structure
 949 of the low dose-epi layer (??) among the rows in the matrix, in particular pixels in rows 112-224
 950 are supposed to have a higher efficiency in the pixel corner.

951 For the calibration I have need to establish the peak position; to do that I perform a fit of the
 952 ToT histogram of each pixels. As fit functions I test both the solutions below:

$$f(x, N, \mu, \sigma) = \frac{N}{\sigma\sqrt{2\pi}} e^{-\frac{1}{2}(\frac{(x-\mu)}{\sigma})^2} \quad (6.7)$$

$$f(x, m, q, N, \mu, \sigma) = m x + q + \frac{N}{\sigma\sqrt{2\pi}} e^{-\frac{1}{2}(\frac{(x-\mu)}{\sigma})^2} \quad (6.8)$$

Nel primo caso ho fissato pochi pixel attorno a picco: il range è stato determinato .. controlla. Nel

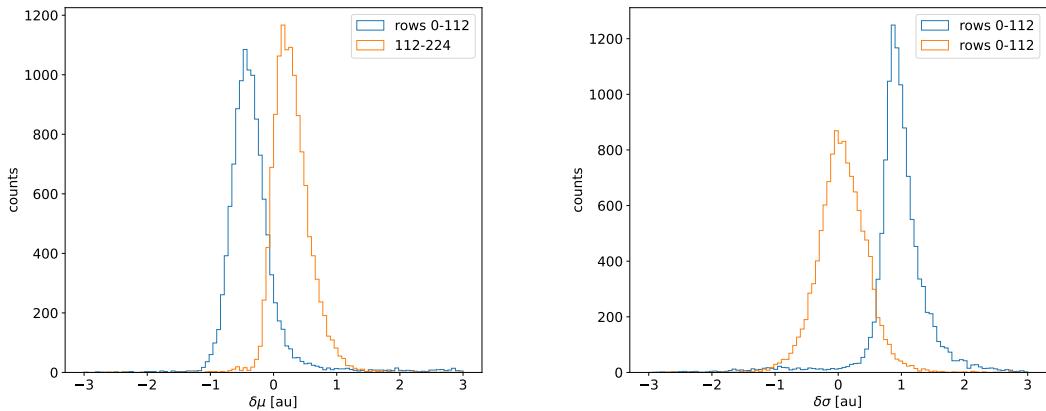


Figure 6.7: Here there are shown the defference between the parameters μ and σ fitted with only a gaussian and with a gaussia plus a line. When $\mu < 0$ the fit function 6.7 has given a worst peak (shifted on the left); when $\sigma < 0$, 6.8 has given a worst peak width (larger sigma)

953
 954 secondo caso invece il range è.. Controlla sullo script Even if the difference in the peak position
 955 between the two cases is not really relevant (6.7) being of the order of 0.8-1.5 %, it still introduces
 956 a systematic effect moving the peak on the left because of the contribution of the tail. Indeed, we
 957 know that the sharp edge on the right corresponds to the complete absorption of the photon, so
 958 excluding the little bump on the right, the more the fitted parameter is on the right, the better
 959 the fit is. Moreover, there is also systematic effect on the peak width, infact the worst fit also gives
 960 an overestimation of the peak width. Even looking at the χ^2 , the fit function 6.7 seems so be the
 961 better choise, except for a sample of pixels on the lower part of the matrix, the one with lower
 962 efficiency.

963 Mappa del ferro da cui, come descritto enll'equazione si ricava la capacity. La struttura a bande
 964 della capacità ha origine nel plot... e quindi nella calibrazione. Andando a vedere gli istogrammi
 965 di queste due variabili si vedono dei picchi. C'è qualche struttura nella matrice che condiziona
 966 il funzionamento delle righe? Larghezza della gaussiana: fai il discorso a cosa contribuisce ad un
 967 picco così largo. è compatibile con quanto ti aspetti?

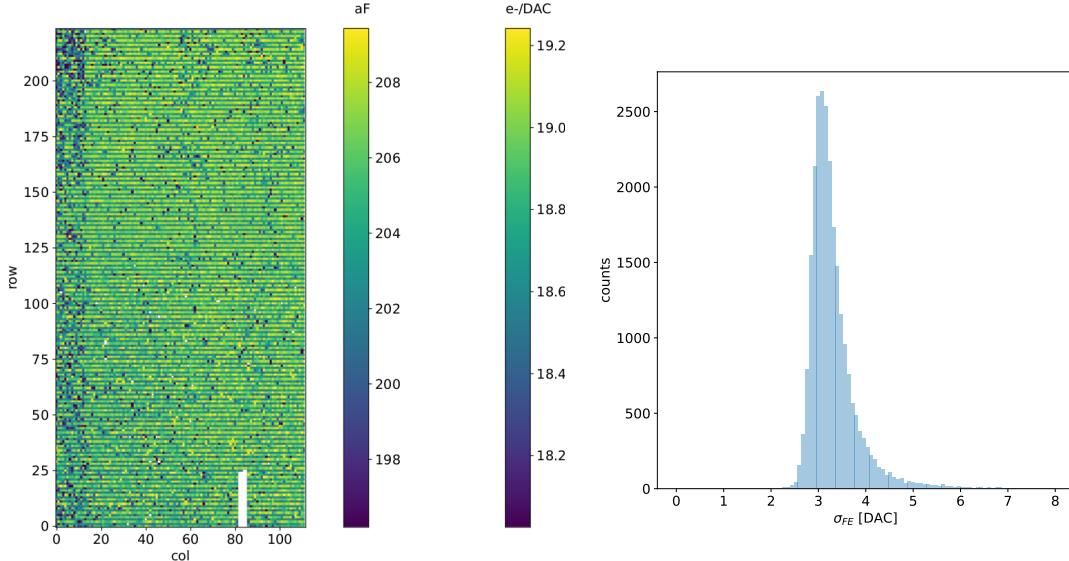


Figure 6.8

	6 V	-3 V	0 V
Threshold [DAC]	20.04 ± 1.6		24.5 ± 1.8
Noise [DAC]	0.613 ± 0.075		0.822 ± 0.098
Slope [au/DAC]	0.726 ± 0.027		0.573 ± 0.021
Offset [au]	-10.8 ± 1.9		-11.1 ± 1.5

Table 6.3: The errors are the standard deviations of the corresponding distributions.

968 6.1.4 PMOS flavor: changing the bias

969 In order to study the behavior of the sensor changing the bias, I perform some injection scans in
 970 different configurations. The thickness of the depletion has to be considered indeed an important
 971 parameters for the efficiency of the signal, and in particular it plays an important role if **bho, trova**
 972 **qualcosa da dire**.

973 Given that the chip under examination has a gap in the low dose epi-layer (look at chapter
 974 ??) we were not able to change independently the bias of the substrate (PSUB) and of the p-well
 975 (PWELL), but they must be kept at the same value, differently from other chips, where they can
 976 be changed as reported in figure 6.9. Turning down the bias, the depletion region narrows and the
 977 efficiency reduces in particular in the pixel corner; **guarda plot e commenta**.

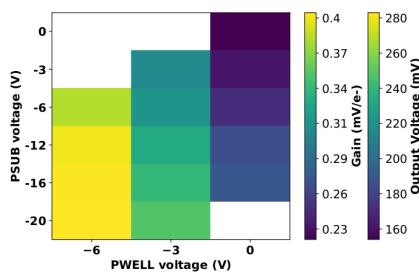


Figure 6.9: 2D map of the output voltage amplitude and gain with respect to the p-well and p-substrate in the case of the PMOS reset front-end

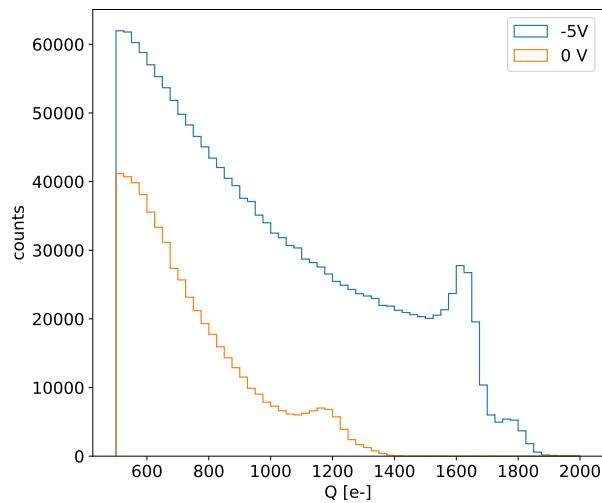


Figure 6.10

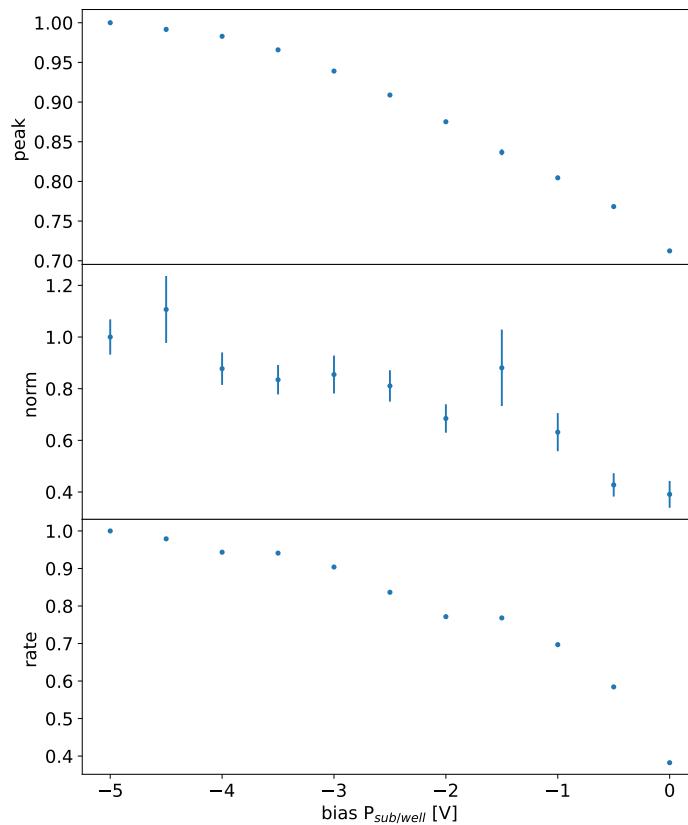


Figure 6.11: da rifare tenendo la sorgente ferma tra un'acquisizione e l'altra

6.1.5 Measurements with radioactive sources

Signal response characterization using radioactive sources and cosmic rays have been made. The Sr90 source emits electrons that: lo spettro ha un cutoff a che E? **conto/plot sulla differenza elettrone sr e mip.** The signal generated by electrons is similar to

982 the one generated by minimum ionizing particle (MIPS). The spectrum is expected to follow a
983 Langau-Gauss distribution

- 984 • sommato i cluster Spiega la tua definizione di cluster.
985 • plot dello spettro del ferro convertendo in elettroni per il PMOS flavor e facendo la somma
986 dei cluster.
987 • di che la tua definizione di cluster è ok perchè sono quasi tutti vicini: plot con la distanza e
988 qualche stima delle coincidenze casuali.
989 • mappa di qualche evento di cluster sia per Sr che per FE
990 • istogrammi in carica, con carica sommata di Sr e Fe, oer il flavor PMOS dove hai fatto la
991 calibrazione o per tutti?

992 Spiega che con il flavor HV abbiamo una perdita di sengnale, fai vedere uno spettro di delle
993 misure dell 8 marzo.

994 6.1.6 Dead time measurements

995 The hit loss is due to analog and digital pile up: the first one occurs when a new hit arrives during
996 the pre-amplifier response, the second instead, which is the more relevant contribution with high
997 rate, while the information of the previous hit has not yet been transferred to the periphery. As
998 only one hit at a time can be stored on the pixel's RAM, until the data have completed the path
999 to get out, the pixel is paralyzed and the dead time τ almost corresponds with the time needed
1000 to trasmit the data-packets off-chip. Since the exportation of data from pixel to the EoC occurs
1001 via a 21-bits data bus, only one clock cycle is need to transfer the data to the end of column and
1002 the dead time bottleneck is given by the bandwidth of the serializer at the EoC. In our setup the
1003 serializer operates at 40 MHz, thus to transmit a data packet (27-bit considering the addition at
1004 the EoC) at least 675 ns are needed. For what we have said so far, the R/O is completely sequential
1005 and therefore is expected a linear dependence of the reading time on the number of pixels to read:

$$\tau = 25 \text{ ns} \times (\alpha N + \beta) \quad (6.9)$$

1006 where α and β are parameters dependent on the readout chain setting.

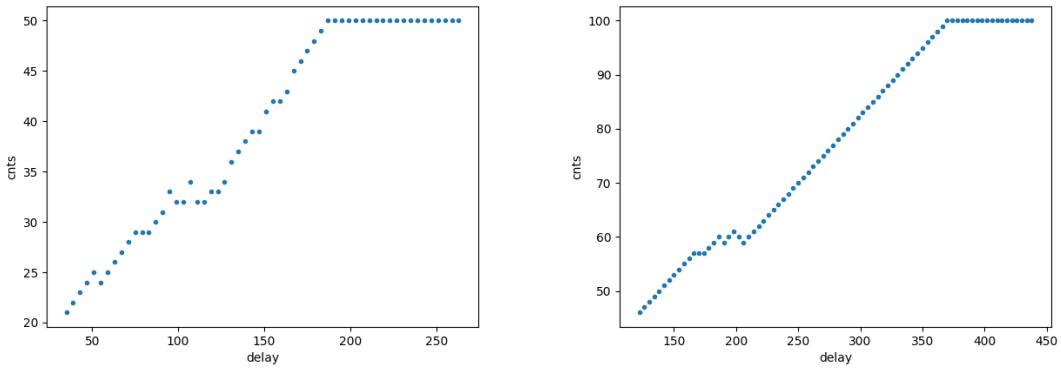
1007 To measure and test the linearity of the reading time with the number of pixels firing, I have
1008 used the injection mode available on the chip. Indeed, the injection mode allows fixing not only
1009 the amplitude of the pulse, which corresponds to the charge in DAC units, but also the period and
1010 the width. I have injected a fix number of pulses (100) and looked for the rate when the efficiency
1011 decreases. Moreover to test that there is no dependece of the digital readout time from the charge
1012 of the pulse, I have try to change the amplitude of the pulse injected, but the parameters found
1013 were consistent with the default configuration ones.

1014 Al posto degli esempi con 5 e 10 pixels metterei un esempio dell'efficienza vs il periodo quando
1015 leggo un singolo pixel. Una cosa che volevo fare era anche provare a fissare la slope con cui
1016 l'efficienza scende: se la slope è uguale per tutti il readout diventa completamente predittivo.

1017 While the single pixel reading time and the dead time do not depend on the position on the
1018 pixel matrix and are equal to 106 (46+60) clock counts within 1 clock count, on the other hand the
1019 τ depends on the pixel position on the matrix when more than one pixel are firing. In particular
1020 the priority chain goes from row 224 to row 0, and from col 0 to 112, that means the last pixels to
1021 be read is the one on le bottom right corner of the matrix.

1022 In figure 6.14 is reported the reading time versus the number of pixels injected; the R/O
1023 parameters that control the reading time and their default values are reported on table ??.

1024 The factor α , referring to eq. 6.9 is proportional to the difference (STOP_FREEZE - START_READ),
1025 while the offset β lies between 5 and 15 clock counts. Since through the injection a random hit rate
1026 on the matrix can't be simulated, as the coordinates of the pixels to inject must be specified, for



(a) efficiency vs DELAY 5 pixels

(b) efficiency vs DELAY per 10pixels

Parameter	Value [DAC]	Value [μ s]
START_FREEZE	64	1.6
STOP_FREEZE	100	2.5
START_READ	66	1.65
STOP_READ	68	1.7

Table 6.4: Default configuration of the R/O parameters

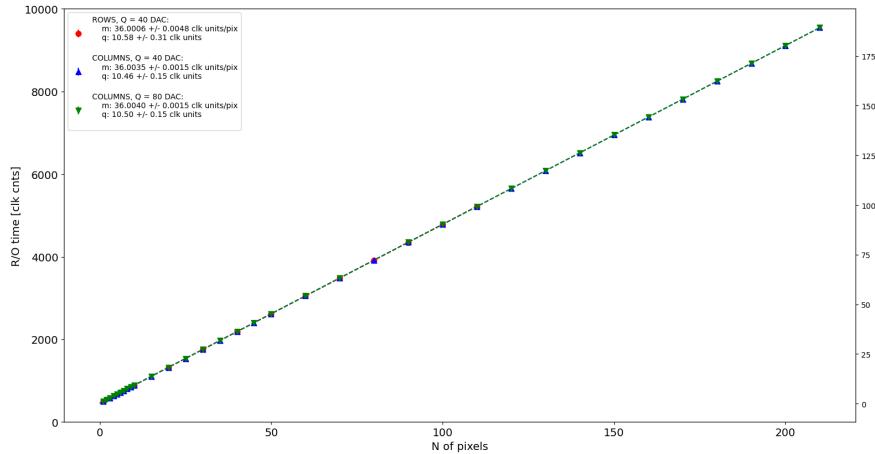


Figure 6.13

convenience I used the pixels on the same column/row. No difference in the α and β coefficients has been observed between the two case.

Ci sarebbe da spiegare perchè i parametri che usiamo noi come default non sono quelli che minimizzano il tempo di lettura. La spiegazione è che "Abbiamo copiato i valori dal repository di quelli di Bonn". Un'altra domanda potrebbe essere: come mai non ho esplorato una zona più vasta per i parametri del R/O. Cambiando molto i parametri del R/O la lettura non funzionava per niente: ad esempio CONF_STOP_FREEZE non può essere impostato né sopra 105 né sotto 95

6.2 ARCADIA-MD1 characterization

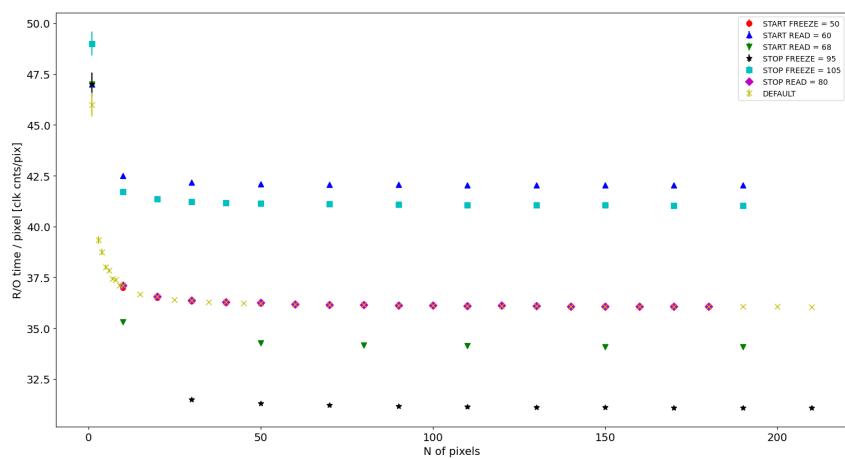


Figure 6.14

1035

Chapter 7

1036

Test beam measurements

1037 The motivation of the testbeam measurements are both the testing of TJ-Mopopix1 in condition
1038 different from the one foreseen during the design and the testing the mechanical and the DAQ setup
1039 for other tests in future. I recall that TJ-Monopix1 is supposed to be employed for tracking in
1040 HEP experiments while our goal was testing the possibility of integrating charge released by more
1041 particles on the sensor at ultra high hit rate . **Una frase di disclaimer sul fatto che non siamo**
1042 **riusciti a testare quello che volevamo.** The measurements have taken place in a bunker room at
1043 the Santa Chiara hospital in Pisa, were a new accelerator designed for the reasearch of FLASH-RT,
1044 and for this reason called "ElectronFlash", have been installed a few months ago.

1045 In medical physics the dose is indeed the standard parameter to charaterize the beam because
1046 of its obvious relation with the damage caused in the patient. By the point of view of the instru-
1047 mentation and the testing on it, a more common and usefull parameter is instead the rate or the
1048 fluence of particles. The conversion can be find thinking to the definition of dose: **definizione di**
1049 **dose e disegno del fascio che incide sul fantoccio d'acqua**

$$Dose = \frac{1}{S} \quad (7.1)$$

1050 from which:

$$R[Hz/cm^2] = \frac{DPP[Gy]}{1.6 \cdot 10^{10} S[g/cm^2]} \quad (7.2)$$

1051 where S is the stopping power in water, 2.17 g/cm^2 The medium is ordinarily water, since dosimetric
1052 protocols are based on measurements in water as reference.

1053

7.1 Apparatus description

1054

7.1.1 Accelerator

1055 The accelerator is an electron Linear Accelerator (LINAC) with two energy configurations, at
1056 7 MeV and 9 MeV, and it can reach ultra high intensity (40 Gy/pulse) keeping the possibility

\bar{D}	Dose rate (mean dose rate for a multi-pulse delivery)	0.005-10000 Gy/s
\dot{D}	Intra pulse dose rate (dose rate in a single pulse)	
DDP	Dose in a single pulse	0.04 Gy
PRF	Pulse repetition frequency(number of pulses delivered per unit of time)	1-350 Hz
t_p	Pulse width	0.2-4 μs
n	Number of pulses	

Table 7.1: The parameters that can actually be set by the control unit are the PRF, DDP, t_p and n (in particular singolar irradiation or pulse train), while the other changes consequently.

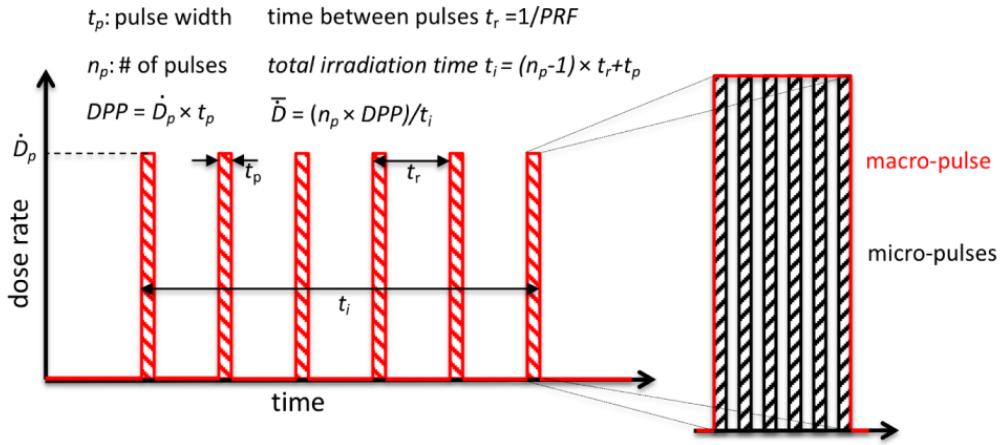


Figure 7.1

of accessing many different beam parameters. This characteristic is fundamental for research in FLASH-RT, both for the medical aspects and for the studies on detectors, infatti ci sono sia ancora questioni aperte per quanto riguarda la medicina e il funzionamento in base ai parametri che per il funzionamento di device. It is almost the only one in the world having this characteristic, ricontrolla sulla review, c'era qualcosa che puoi dire.

The accelerator implements a standard beam structure for RT with electrons, that is a macro pulse divided in many micropulses, is reported in figure 7.1;a description of the parameters used to describe the beam and their range of values settable by the control unit is reported in table 7.1.

The accelerator is provided of a set of triod cannons long 1 m with diameters from 1 cm to 12 cm and in addition the field can be produced both in a circle and squircle shape using a beam shaper. The triode is made by pleaxiglass and through the scattering of electrons with it a uniform dose profile (fig.7.2) at the output of the triod is produced, which is desired for medical treatment.

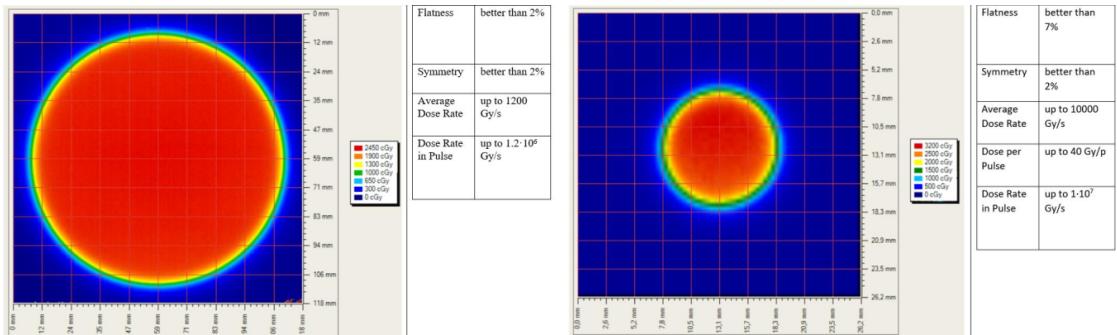


Figure 7.2: Two example of x-y isodose curves for two different triodes, 10 cm and 1 cm respectively, reported by the producer of the accelerator (S.I.T. - Sordina IORT Technologies S.p.A.). With the smaller collimator the dose rate in pulse is comparatively higher.

1068

7.1.2 Mechanical carriers

In order to er effettuare le misure è stato costruito un carrello su cui mettere il DUT, foto del carrello, con la possibilità di regolare la distanza tra dut e bocca del triodo.

The Device Under Test (DUT) have been then positioned vertically and enclosed in a box with a window on the chip and some holes in order to enable the biasing via cables and for the connection with the DAQ provided via ethernet cable. trigger collimatori

per schermare gli eltronni dei collimatori sono stati costruiti: uno con posizione fissa da mettere

1077 vicino al triodo e uno da mettere lontano con posizione spostabile in modo da illuminare solo una
1078 parte del DUT. Collimatori di alluminio spessi 3 cm con fori da 1mm. Il collimatore vicino al
1079 triodo ha la funzione di fare una sorgente puntiforme se ci si mette lontano dal fascio, in modo da
1080 avere possibilità di diminuire il rate a piacimento come 1/12. Questo ovviamente è reso possibile
1081 dal fatto che il fascio ha una divergenza superiore a 1/20, non collimato.

1082 7.2 Measurements

1083 Dunque visti gli altissimi rate in gioco, con i tempi morti del nostro chip , non è possibile avere un
1084 segnale di singole particelle, perchè in tal caso si satura completamente. Ricordiamo che i tempi
1085 morti dipendono dal rate e anche dalla posizione del pixel nella matrice, in particolare secondo la
1086 priority chain l'ultimo pixel che viene letto è qurllo con tau più lungo. Con un tau medio di 1 us
1087 per pixel. L'idea è comunque anche se non si riesce a misurare intra pulse e misurare tra i pulse:
1088 quindi far sì che la lettura tra l'arrivo di due buch consecutivi.

1089 Avendo un cut off massimo di hit a 25000 (n of pixel per un flavor), l'idea che si vuole testare è
1090 dunque: possibilità di integrare carica sul pixel: due elettroni consecutivi su un pixel ogni quanto
1091 arrivano? Vogliamo sfruttare il analog pile up tra gli eventi, e la linearità del tot. **conti** Devi avere
1092 che il tot dell'elettrone (cioè MIP) è maggiore del deltat medio; in questo caso potresti riuscire ad
1093 integrare carica.

1094 Purtroppo non è stato possibile effettuare questo test per mancanza di tempo (**chiedere a forti**),
1095 ma abbiamo effettuato solo un test preliminare, le cui misure sono riportate in sezione.

1096 We have used the PMOS flavor (1) with the standard configuration with the idea of starting
1097 by testing the setup: we have biased the PWELL and PSUB at -6 V and with the default FE
1098 parameters reported in table ???. Initially we have used both the collimators and injected smaller
1099 dose pulses and only after we have removed the collimators and increased the DDP. During all the
1100 acquisitions we have used pulses with t_p of 4 μ m and with the smallest PRF settable, which is 1 Hz,
1101 in order to start in the most conservative working point exluding the digital pile up from different
1102 pulses. Indeed the readout time for each pixel is \sim 1 μ s, then even if the whole matrix turns on,
1103 the readout time corresponds to 25 ms.

1104 We encountered a photon background higher than expected: photons are mainly produced via
1105 Bremsstrahlung during the transition of electrons through the alluminium collimators. **qualche**
1106 **numero in più sulla perdita di energia per rad, e magari plot della simulazione.**

1107 Since the long dead time of the matrix, each pixel cannot fire more than a time for each pulses.
1108 Since the readout strarts \sim 50 clk counts after the first TE of the pixel arrived, we could resolve
1109 the pulse in two different part. The hit which arrives and which goes under the thre. during the
1110 first 50 clk counts are classified in the first sub-pulse, while the hits which arrives during the freeze
1111 are in the second. In the second pulse there are also the hits arrived in the first but with a longer
1112 tot (scendono sotto soglia quando ormai è partito il freeze). During the freeze possono arrivare
1113 hit, su pixel vuoti. Questi verranno letti dopo. Obviously since the readout of the fist subpulse
1114 finished much later the bunch finished, each pixel can be store only one hit. This constitute our
1115 saturation, since we can't read more tha 25 000 hits per bunch. Actually this is not completely
1116 true, since the firsts pixels in the priority chain, which can be read before the bunch finished, can
1117 be hit again and can store a new hit. An example of the two subpulse is shown in figure 7.5. with
1118 a map of the tot of the hit.

1119 When we have put aside the collimators, the fluence was too high that the whole matrixs turn
1120 on in 50 clock counts; then the 2 pulses substructure no more appears. Also the spectrum change
1121 from the previous one. It is shown in figure 7.5. Since a MIP should produce e- in the epitaxial
1122 layer, it should provide a signal of about 2 ke-, so in our condition it shoud have done rollover. In
1123 order to compare the spectrum with the one produced by MIP, in laboratory we have done some
1124 acquisitions with cosmic rays. To be confident with having selected MIPS from cosmic rays and
1125 not noise, we have selected only the events with one more hits per timestamp, which we consider
1126 are prevalentemente cluster. Dato il rate dei cosmici e il rate del rumore infatti, le coincidenze

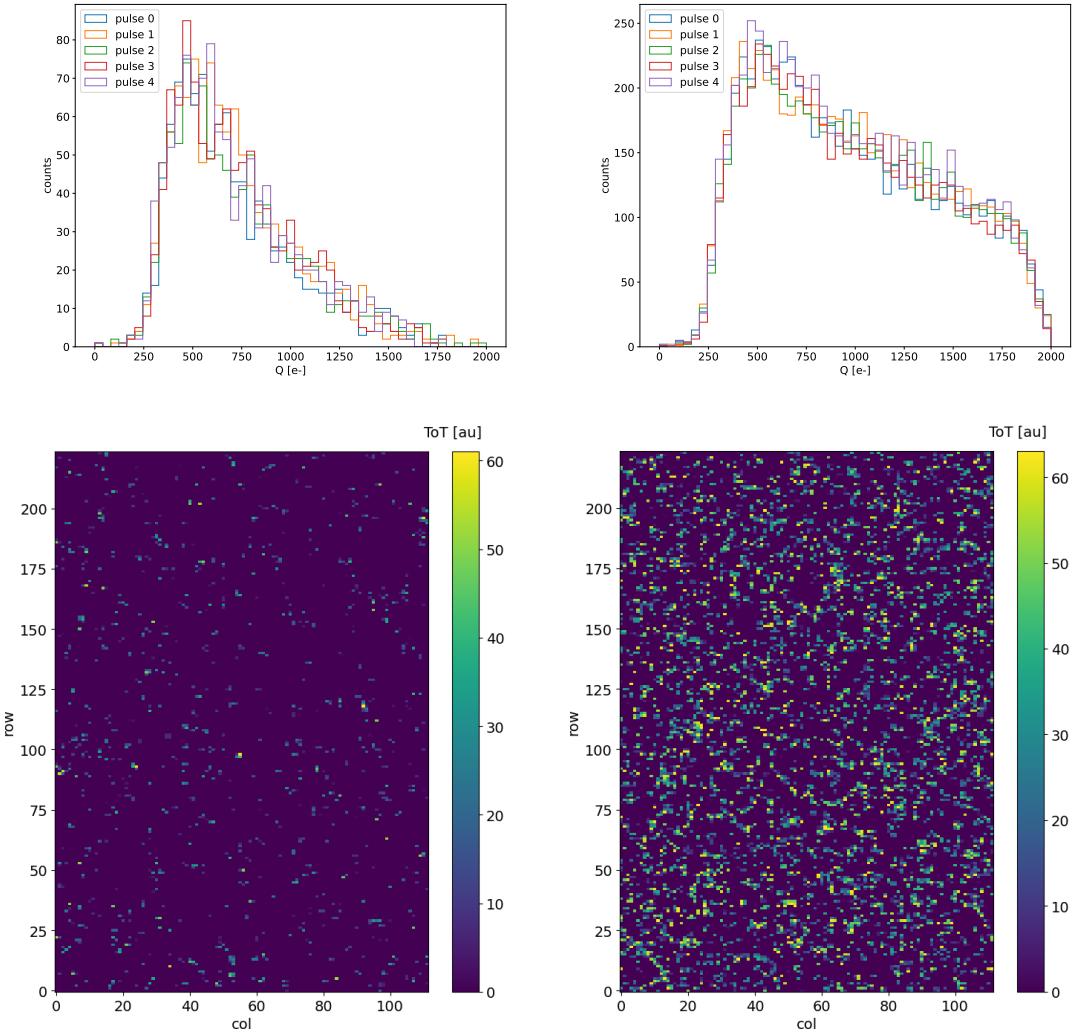


Figure 7.3: Acquisition with both the collimators at DDP=0.04 Gy

casuali per il nostro tempo di acquisizione sono.. Qualche esempio di traccia di muoni. e n of hit cluster. Un confrontare degli spettri (convertiti in carica).

DDP	Hits per pulse	
-----	----------------	--

Table 7.2

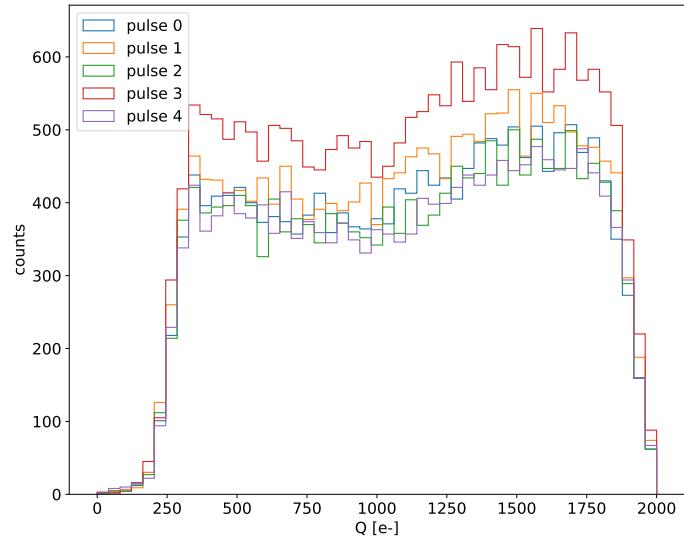


Figure 7.4: Acquisition without any collimator at DDP=0.04 Gy

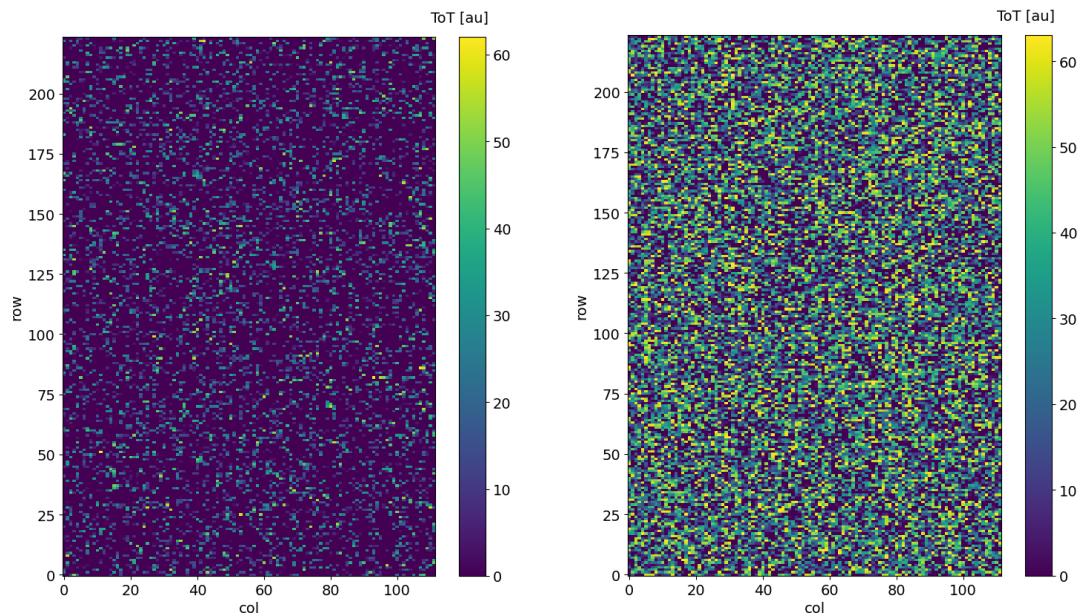


Figure 7.5

1129 **Appendix A**

1130 **Pixels detector: a brief overview**

1131 **A.1 Radiation damages**

1132 Radiation hardness is a fundamental requirement for pixels detector especially in HEP since they
1133 are almost always installed near the interaction point where there is a high energy level of radiation.
1134 At LHC the ϕ_{eq} per year in the innermost pixel detector is $10^{14} n_{eq}/cm^2$; this number reduces by
1135 an order passing to the outer tracker layer [2] pag 341 Wermes. Here the high fluence of particles
1136 can cause a damage both in the substrate of the detector and in the superficial electronics.

1137 The first one has a principal non ionizing nature, due to a non ionizing energy loss (NIEL), but
1138 it is related with the dislocation of the lattice caused by the collision with nuclei; by this fact the
1139 NIEL hypothesis states that the substrate damage is normalized to the damage caused by 1 MeV
1140 neutrons. Differently, surface damages are principally due to ionizing energy loss.

1141 **DUE PAROLE IN PIÙ SUL SURFACE DAMAGE** A charge accumulation in oxide (SiO_2) can
1142 cause the generation of parasitic current with an obvious increase of the 1/f noise. Surface damages
1143 are mostly less relevant than the previous one, since with the development of microelectronics and
1144 with the miniaturization of components (in electronic industry 6-7 nm transistors are already used,
1145 while for MAPS the dimensions of components is around 180 nm) the quantity of oxide in circuit
1146 is reduced.

1147 Let's spend instead two more other words on the more-relevant substrate damages: the general
1148 result of high radiation level is the creation of new energy levels within the silicon band gap and
1149 depending on their energy-location their effect can be different, as described in the Shockley-Read-
1150 Hall (SRH) statistical model. The three main consequence of radiation damages are the changing
1151 of the effect doping concentration, the leakage current and the increasing of trapping probability.

1152 **Changing of the effective doping concentration:** is associated with the creation/removal
1153 of donors and acceptors center which trap respectively electrons/holes from the conduction band
1154 and cause a change in effective space charge density. Even an inversion (p-type becomes n-type¹)
1155 can happen: indeed it is quite common at not too high fluences ($\phi_{eq} 10^{12-13} n_{eq} cm^{-2}$). A changing
1156 in the doping concentration requires an adjustment of the biasing of the sensor during its lifetime
1157 (eq.2.1) and sometimes can be difficult keeping to fully deplete the bulk.

1158 **Leakage current:** is associated with the generation-recombination centers. It has a strong
1159 dependence with the temperature ($I_{leak} \propto T^2$), whose solution is therefore to operate at lower
1160 temperature.

1161 **Increase of trapping probability:** since the trapping probability is constant in the depleted
1162 region, the collected charge decreases exponentially with the drift path. The exponential coefficient,
1163 that is the mean trapping path, decreases after irradiation and typical values are 125-250 μm and
1164 must be compared with the thickness of the depleted region which () corresponds to the mean drift
1165 path.

1166 Different choices for substrate resistivity, for junctions type and for detector design are typically

¹L'INVERSIONE OPPOSTA NON CE L'HA PERCHÈ?

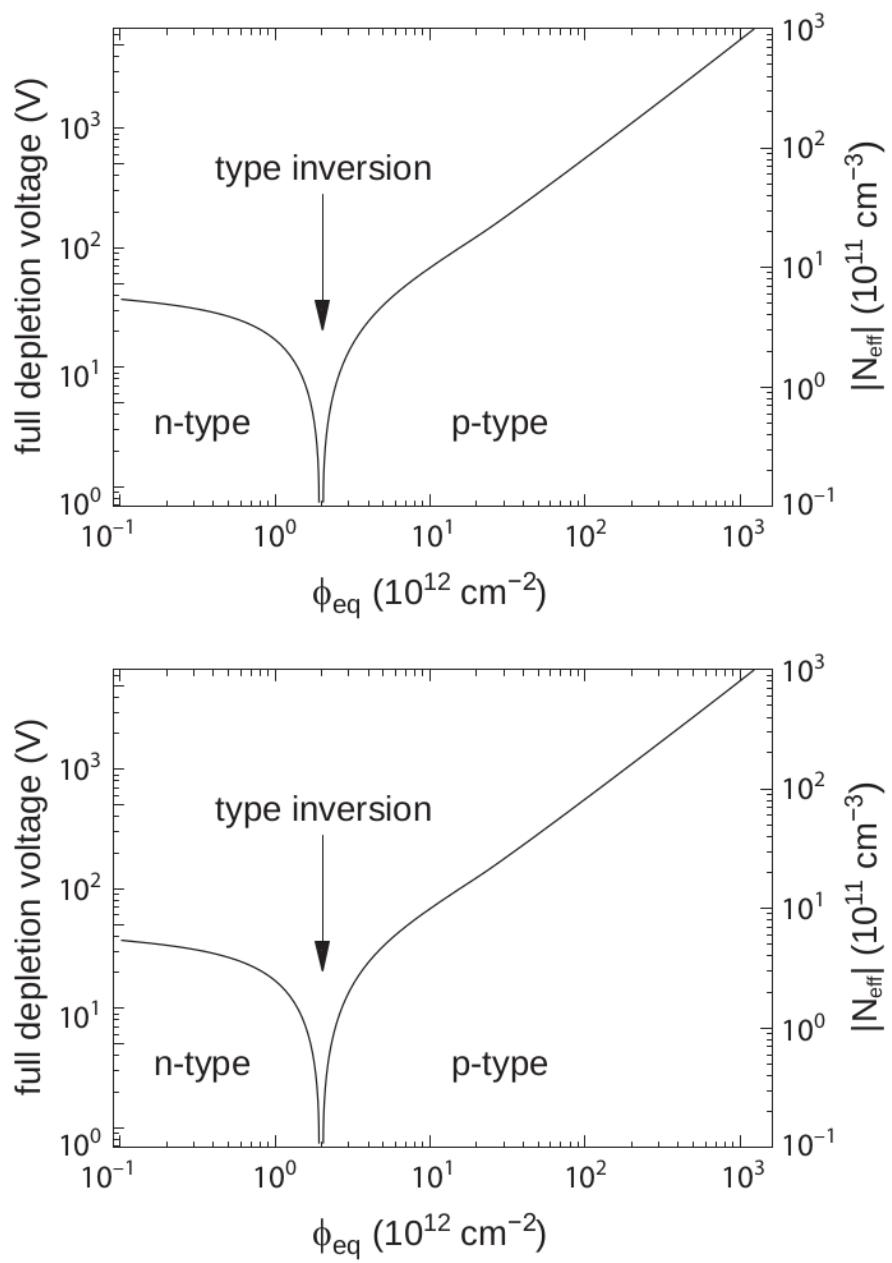


Figure A.1: 1b

1167 made to fight radiation issues. Some material with high oxygen concentration (as crystal produced
1168 using Czochralki (Cz) or float-zone (Fz) process (**CONTROLLA LA DIFFERENZA TRA I DUE**))
1169 for example, show a compensation effect for radiation damage; another example is the usage of
1170 n+ -in-p/n sensors (even if p+ -in-n sensors are easier and cheaper to obtain) to get advantage
1171 of inversion/to have not the inversion (since they are already p-type). After inversion the n+p
1172 boundary, coming from n+ in-n, but to keep using the sensor the depletion zone still must be
1173 placed near the diode.

1174 Single Event Upset, in sostanza è quando un bit ti cambia valore (da 0 a 1 o viceversa) perché
1175 una particella deposita carica nell'elettronica che fa da memoria registro/RAM/.... Questo tipo
1176 di elettronica ha bisogno di un sacco di carica prima che il bit si "flippi" (cambi valore), infatti
1177 tipicamente per avere un SEU non basta una MIP che attraversa esattamente quel pezzo di chip
1178 in cui è implementata la memoria, ma un adrone che faccia interazione nucleare producendo più
1179 carica di quanto farebbe una MIP. Questo metodo pur essendo più comodo richiede less amount of
1180 area ha però come drawback che il registro può essere soggetto a SEU problema non trascurabile
1181 in acceleratori come HL-LHC adronici

¹¹⁸² Bibliography

- ¹¹⁸³ [1] W. Snoeys et al. “A process modification for CMOS monolithic active pixel sensors for
¹¹⁸⁴ enhanced depletion, timing performance and radiation tolerance”. In: (2017). DOI: <https://doi.org/10.1016/j.nima.2017.07.046>.
- ¹¹⁸⁶ [2] H. Kolanoski and N. Wermes. *Particle Detectors: Fundamentals and Applications*. OXFORD
¹¹⁸⁷ University Press, 2020. ISBN: 9780198520115.
- ¹¹⁸⁸ [3] E. Mandelli. “Digital Column Readout Architecture for 10.1109/NSSMIC.2009.5402399 the
¹¹⁸⁹ ATLAS Pixel 0.25 um Front End IC”. In: (2002).
- ¹¹⁹⁰ [4] M. Garcia-Sciveres and N. Wermes. “A review of advances in pixel detectors for experiments
¹¹⁹¹ with high rate and radiation”. In: (2018). DOI: <https://doi.org/10.1088/1361-6633/aab064>.
- ¹¹⁹³ [5] C. Marinas. “The Belle-II DEPFET pixel detector: A step forward in vertexing in the superKEKB flavour factory”. In: (2011). DOI: [doi:10.1016/j.nima.2010.12.116](https://doi.org/10.1016/j.nima.2010.12.116).
- ¹¹⁹⁵ [6] J. Baudot. “First Test Results Of MIMOSA-26, A Fast CMOS Sensor With Integrated Zero
¹¹⁹⁶ Suppression And Digitized Output”. In: (2010). DOI: [doi:10.1109/NSSMIC.2009.5402399](https://doi.org/10.1109/NSSMIC.2009.5402399).
- ¹¹⁹⁷ [7] A. Dorokhov. “High resistivity CMOS pixel sensors and their application to the STAR PXL
¹¹⁹⁸ detector”. In: (2011). DOI: [doi:10.1016/j.nima.2010.12.112](https://doi.org/10.1016/j.nima.2010.12.112).
- ¹¹⁹⁹ [8] Giacomo Contin. “The STAR MAPS-based PiXeL detector”. In: (2018). DOI: <https://doi.org/10.1016/j.nima.2018.03.003>.
- ¹²⁰¹ [9] Nolan Espplen. “Physics and biology of ultrahigh dose-rate (FLASH) radiotherapy: a topical
¹²⁰² review”. In: (2020). DOI: <https://doi.org/10.1088/1361-6560/abaa28>.
- ¹²⁰³ [10] M. Dyndal et al. “Mini-MALTA: Radiation hard pixel designs for small-electrode monolithic
¹²⁰⁴ CMOS sensors for the High Luminosity LHC”. In: (2019). DOI: <https://doi.org/10.1088/1748-0221/15/02/p02005>.
- ¹²⁰⁶ [11] M. Barbero. “Radiation hard DMAPS pixel sensors in 150 nm CMOS technology for opera-
¹²⁰⁷ tion at LHC”. In: (2020). DOI: <https://doi.org/10.1088/1748-0221/15/05/p05013>.
- ¹²⁰⁸ [12] K. Moustakas et al. “CMOS Monolithic Pixel Sensors based on the Column-Drain Architec-
¹²⁰⁹ ture for the HL-LHC Upgrade”. In: (2018). DOI: <https://doi.org/10.1016/j.nima.2018.09.100>.
- ¹²¹¹ [13] I. Caicedo et al. “The Monopix chips: depleted monolithic active pixel sensors with a column-
¹²¹² drain read-out architecture for the ATLAS Inner Tracker upgrade”. In: (2019). DOI: <https://doi.org/10.1088/1748-0221/14/06/C06006>.
- ¹²¹⁴ [14] D. Kim et al. “Front end optimization for the monolithic active pixel sensor of the ALICE
¹²¹⁵ Inner Tracking System upgrade”. In: *JINST* (2016). DOI: [doi:10.1088/1748-0221/11/02/C02042](https://doi.org/10.1088/1748-0221/11/02/C02042).
- ¹²¹⁷ [15] L. Pancheri et al. “A 110 nm CMOS process for fully-depleted pixel sensors”. In: (2019). DOI:
¹²¹⁸ <https://doi.org/10.1088/1748-0221/14/06/c06016>.
- ¹²¹⁹ [16] L. Pancheri et al. “Fully Depleted MAPS in 110-nm CMOS Process With 100–300-um Active
¹²²⁰ Substrate”. In: (2020). DOI: [10.1109/TED.2020.2985639](https://doi.org/10.1109/TED.2020.2985639).