

Power supply

Murata
BLM18AG2215N1D

FB1
220R/100MHz

VCC

C6
1u

GND

C3
100n

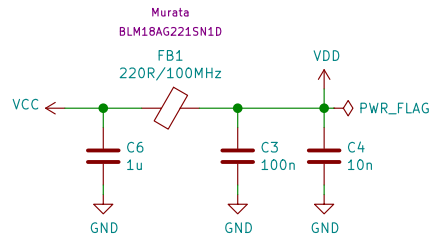
GND

C4
10n

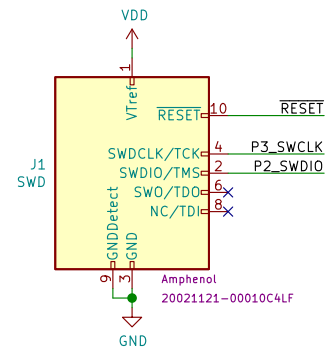
GND

VDD

PWR_FLAG



Pinout diagram of the SWD connector J1. The diagram shows a yellow square component with pins 1 through 8. Pin 1 is VDD. Pin 2 is VTrst. Pin 3 is GND. Pin 4 is RESET. Pin 5 is SWDCLK/TCK. Pin 6 is SWDIO/TMS. Pin 7 is SWO/TDO. Pin 8 is NC/TDI. The component is labeled 'Amphenol 20021121-00010C4LF'. The connector is labeled 'J1 SWD'.



External crystal oscillator (optional)

The diagram shows an external crystal oscillator (ECS) circuit. The ECS is labeled ECS-80-10-30B-CWN-TR. It has four pins: Pin 1 is connected to GND. Pin 2 is connected to the XTALIN pin of the microcontroller. Pin 3 is connected to the XTALOUT pin of the microcontroller. Pin 4 is connected to GND. A capacitor C1 (18pF) is connected between Pin 2 and GND. A capacitor C2 (18pF) is connected between Pin 3 and GND. The crystal is labeled Y1 8MHz.

C1 and C2 must be selected to match Y1 specs

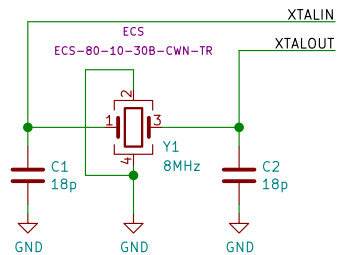
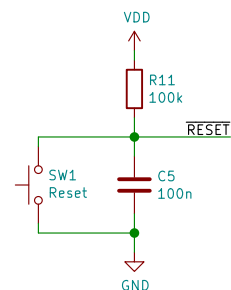
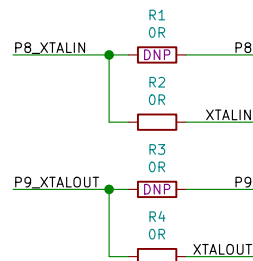


Diagram illustrating the User reset circuit. A switch SW1 (Reset) is connected to the RESET pin. The RESET pin is also connected to VDD through resistor R11 (100k) and to GND through capacitor C5 (100n).



External crystal in use: place R2 and R4
 External crystal not used: place R1 and R3



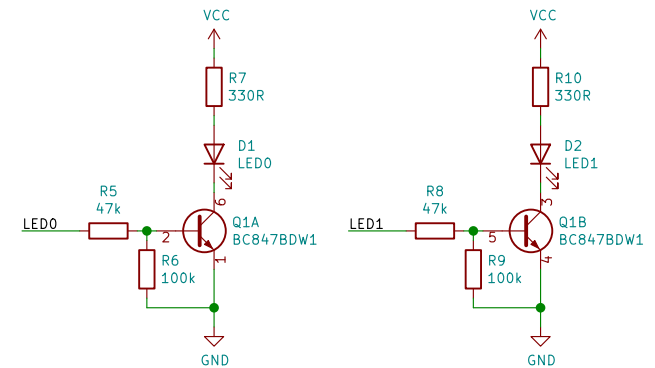
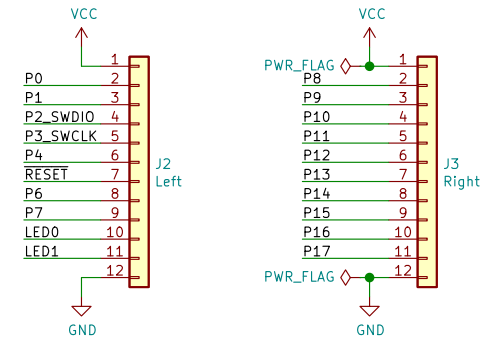
Board I/O headers

J2 Left

Pin	Signal
1	VCC
2	P0
3	P1
4	P2_SWDIO
5	P3_SWCLK
6	P4
7	RESET
8	P6
9	P7
10	LED0
11	LED1
12	GND

J3 Right

Pin	Signal
1	VCC
2	PWR_FLAG
3	P8
4	P9
5	P10
6	P11
7	P12
8	P13
9	P14
10	P15
11	P16
12	P17
12	GND



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Id: 1/1

