

Power supply

Murata
BLM18AG2215N1D

FB1
220R/100MHz

VCC

C6
1u

GND

C3
100n

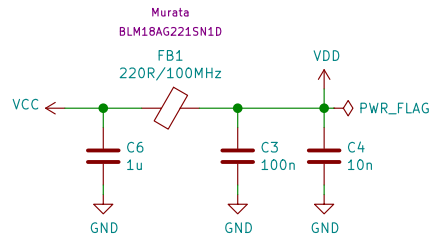
GND

VDD

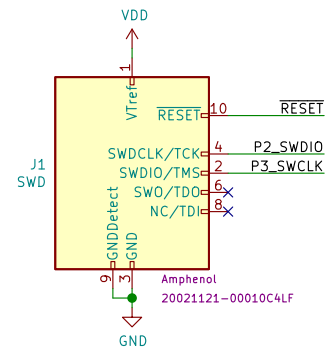
PWR_FLAG

C4
10n

GND



Pin	Signal
1	VDD
2	RESET
3	GND
4	P2_SWDIO
5	P3_SWCLK
6	SWDCLK/TCK
7	SWDIO/TMS
8	SWO/TDO



External crystal oscillator (optional)

ECS
ECS-80-10-30B-CWN-TR

XTALIN

XTALOUT

Y1
8MHz

C1
18p

C2
18p

GND

GND

GND

C1 and C2 must be selected to match Y1 specs

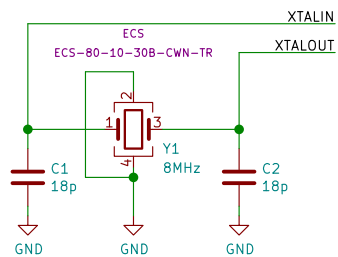
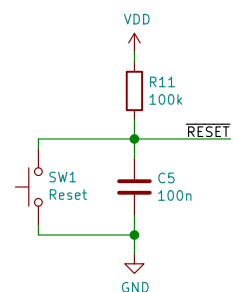


Diagram illustrating the User reset circuit. A switch SW1 (Reset) is connected to the RESET pin. The RESET pin is also connected to VDD through a resistor R11 (100k) and to GND through a capacitor C5 (100n).



U1 LPC812M101JDH20

Pin	Signal	Pin	Signal
1	P17	11	P15
2	P13	12	P1
3	P12	13	P17
4	RESET	14	P8_XTALIN
5	P4	15	VDD
6	P3_SWCLK	16	GND
7	P2_SWDIO	17	P7
8	P11	18	P6
9	P10	19	P0
10	P9_XTALOUT	20	P14

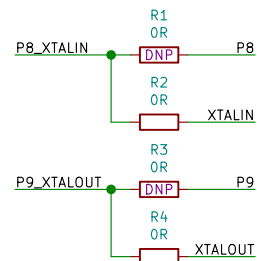
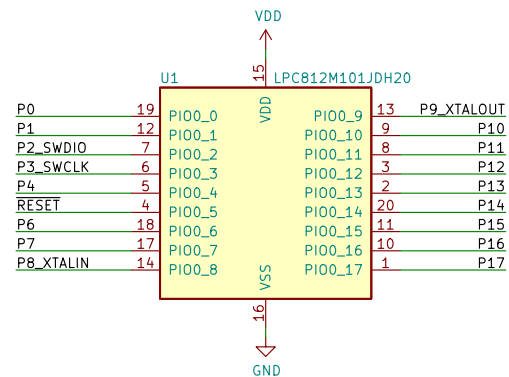


Diagram illustrating the pin connections for J2 Left and J3 Right headers.

J2 Left Header:

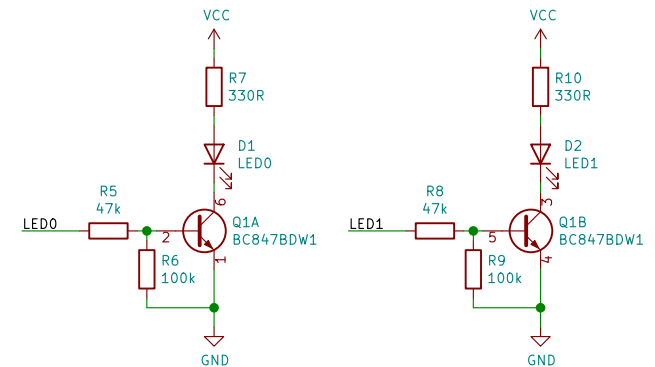
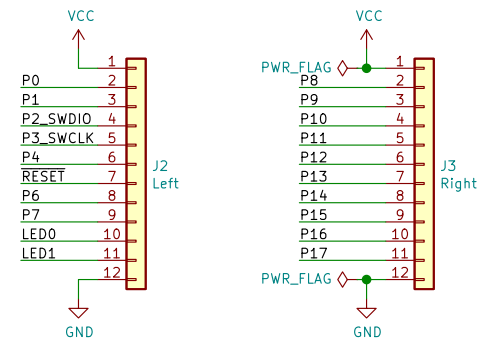
- Pin 1: VCC
- Pin 2: P0
- Pin 3: P1
- Pin 4: P2_SWDIO
- Pin 5: P3_SWCLK
- Pin 6: P4
- Pin 7: RESET
- Pin 8: P6
- Pin 9: P7
- Pin 10: LED0
- Pin 11: LED1
- Pin 12: GND

J3 Right Header:

- Pin 1: VCC
- Pin 2: P8
- Pin 3: P9
- Pin 4: P10
- Pin 5: P11
- Pin 6: P12
- Pin 7: P13
- Pin 8: P14
- Pin 9: P15
- Pin 10: P16
- Pin 11: P17
- Pin 12: GND

Additional labels for J3 Right Header:

- PWR_FLAG (connected to Pin 12)



Rev: A
Id: 1/1

