Board headers R2 and R3 placed only when the LFXT1 crystal is not in use VCC VCC Power supply O PWR_FLAG P1.1 FB1 P2.7_XOUT DNP Bead MCU → PWR_FLAG 0R P1.5 VDD _C4 R3 + 100n P1.6 P3.2 10u Left P1.7 P3.3 P3.4 MSP430G2553IPW28 GND GND RESET 24 RST/NMI/SBWTDIO SBWTDIO SBWTCK/TE P1.0 13 P2.3 TAOCLK/ACLK/AO/CAO/P1.0 RESET 14 P1.1 14 LEDO TAO.0/UCAORXD/UCAOSOMI/A1/CA1/P1.1 SBWTDIO 15 P1.2 15 LED1 TAO.1/UCAOTXD/UCAOSIMO/A2/CA2/P1.2 P1.3 ADC10CLK/CAOUT/VREF-/VeREF-/A3/CA3/P1.3 → PWR_FLAG P1.4 TCK/SMCLK/UCBOSTE/UCAOCLK/VREF+/VeREF+/A4/CA4/P1.4 P1.5 TMS/TA0.0/UCB0CLK/UCA0STE/A5/CA5/P1.5 P1.6 GND GND TDI/TCLK/TA0.1/UCB0SOMI/UCB0SCL/A6/CA6/P1.6 P1.7 TDO/TDI/CAOUT/UCBOSIMO/UCBOSDA/A7/CA7/P1.7 SBW connector P3.0 P3.0/TA0.2 P2.0 TA1.0/P2.0 8 P3.1/TA1.0 P2.1 P3.2 13 P3.2/TA1.1 TA1.1/P2.1 P2.2 TA1.1/P2.2 P3.3 P3.3/TA1.2 P2.3 TA1.0/P2.3 P3.4 P2.4 User LEDs TA1.2/P2.4 P3.5 19 P3.5/TA0.1 SBWTDIO P2.5 TA1.2/P2.5 20 P3.6/TA0.2 P3.6 P2.6_XIN VCC VCC TA0.1/XIN/P2.6 P3.7/TA1CLK/CAOUT 26 P2.7_X0UT XOUT/P2.7 SBW 🕁 28 D1 LED0 D2 ₩ LED1 J3 GND \rightarrow **1** R7 1 R9 GND - 470R 470R Q1 BSS138 Q2 BSS138 R6 R8 1M LFXT1 crystal oscillator External reset circuit GND GND VDD RESET License: CC BY-SA 4.0 I^O SW1 Michal Potrzebicz <michal@elevendroids.com> Reset Mini breakout board for the MSP430G2553 microcontroller \triangle Sheet: / GND C2 and C3 must match the X1 specification File: msp430g2553-breakout.sch R4 and R5 placed only if the crystal is populated Title: MSP430G2553 Breakout Board Date: 2020-09-18 Size: A4 Rev: A KiCad E.D.A. kicad 5.1.6 ld: 1/1