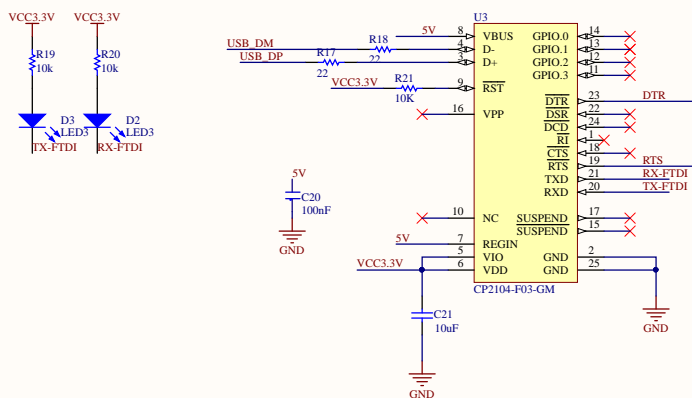


Figure 1 shows six circuit diagrams illustrating different decoupling capacitor configurations for a microcontroller. Each diagram shows a VCC3.3V supply, a decoupling capacitor, and a ground connection (GND).

- Diagram 1 (Top Left):** A single capacitor C13 (100nF) is connected between VCC3.3V and GND. A VDD1 pin is also shown connected to VCC3.3V.
- Diagram 2 (Top Middle):** A single capacitor C14 (100nF) is connected between VCC3.3V and GND. A VDD2 pin is also shown connected to VCC3.3V.
- Diagram 3 (Top Right):** A single capacitor C16 (100nF) is connected between VCC3.3V and GND. A VBAT pin is also shown connected to VCC3.3V.
- Diagram 4 (Bottom Left):** A single capacitor C15 (100nF) is connected between VCC3.3V and GND. A VDD4 pin is also shown connected to VCC3.3V.
- Diagram 5 (Bottom Middle):** Two capacitors, C3 (4.7uF) and C4 (100nF), are connected in parallel between VCC3.3V and GND. A VDD3 pin is also shown connected to VCC3.3V.
- Diagram 6 (Bottom Right):** Two capacitors, C5 (1uF) and C6 (10nF), are connected in parallel between VCC3.3V and GND. A VDDA pin is also shown connected to VCC3.3V.

Pin diagram of the STM32F107RCT6 microcontroller. The diagram shows a 48-pin package with pins numbered 1 to 48. Various functions are assigned to the pins, including reset, SPI, TX, RX, SWDIO, JTAG, JTDI, BOOT, buzzer, button, SD, nSEL, SCLK, SDO, SDI, PC0-PC15, NRST, OSC_IN, OSC_OUT, GND, VCC, and VCC3.3V.

Pin	Function
1	PIN_RESET
2	PIN_RESET
3	PIN_RESET
4	PIN_RESET
5	PIN_RESET
6	PIN_RESET
7	PIN_RESET
8	PIN_RESET
9	PIN_RESET
10	PIN_RESET
11	PIN_RESET
12	PIN_RESET
13	PIN_RESET
14	PIN_RESET
15	PIN_RESET
16	PIN_RESET
17	PIN_RESET
18	PIN_RESET
19	PIN_RESET
20	PIN_RESET
21	PIN_RESET
22	PIN_RESET
23	PIN_RESET
24	PIN_RESET
25	PIN_RESET
26	PIN_RESET
27	PIN_RESET
28	PIN_RESET
29	PIN_RESET
30	PIN_RESET
31	PIN_RESET
32	PIN_RESET
33	PIN_RESET
34	PIN_RESET
35	PIN_RESET
36	PIN_RESET
37	PIN_RESET
38	PIN_RESET
39	PIN_RESET
40	PIN_RESET
41	PIN_RESET
42	PIN_RESET
43	PIN_RESET
44	PIN_RESET
45	PIN_RESET
46	PIN_RESET
47	PIN_RESET
48	PIN_RESET
49	PIN_RESET
50	PIN_RESET
51	PIN_RESET
52	PIN_RESET
53	PIN_RESET
54	PIN_RESET
55	PIN_RESET
56	PIN_RESET
57	PIN_RESET
58	PIN_RESET
59	PIN_RESET
60	PIN_RESET
61	PIN_RESET
62	PIN_RESET
63	PIN_RESET
64	PIN_RESET



Pin configuration diagram for the RF1 module. The module has 14 pins. Pin 1 is GND. Pin 2 is GPIO0. Pin 3 is GPIO1. Pin 4 is VCC3.3V. Pin 5 is SDO. Pin 6 is SDI. Pin 7 is SCLK. Pin 8 is aSEL. Pin 9 is nIRQ. Pin 10 is SDN. Pin 11 is GND. Pin 12 is RF. Pin 13 is ANTENA. Pin 14 is GND. A capacitor C11 (100nF) is connected between VCC3.3V and GND.

Título:	Dongle V1.3	<i>Kleverness</i>
Elaboró:	Josué Rosario G.	<i>Hipodromo Condesa</i>
Revisó:		<i>Amsterdam 271</i>
Fecha:	24/11/2020	<i>Cuauhtemoc</i>
		<i>CDMX</i>

