Totally Integrated Automation Portal	

PLC_1 [CPU 1212C AC/DC/Rly]

PROFINET interface [X1]\Ethernet PROFINET device name is set directly at the device Converted name: plcxb1d0ed PROFINET interface [X1]\Time syno	erface_1 Project information addresses\Interface		AA_LAB7 O Work memory 75 KB; 120/240VAC power supply with DI8 x 24VDC SINK/SOURCE, DQ6 x relay and Al2 on board; 4 high-speed counters (expandable with digital signal board) and 4 pulse outputs on board; signal board expands onboard I/O; up to 3 communication modules for serial communication; up to 2 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC to PLC communication	Comment Article number Installation date	6ES7 212-1BE40-0XB0 2019-03-12 16:32:03.545
General/Catalog information Short designation Short interface [X1]/General/Filter of the short interface [X1]/General/Filter of the short interface [X1]/General/Filter of the short interface [X1]/Ethernet Short	erface_1 Project information addresses\Interface	Rack Description Location identifier Author Comment networked with	Work memory 75 KB; 120/240VAC power supply with DI8 x 24VDC SINK/SOURCE, DQ6 x relay and AI2 on board; 4 high-speed counters (expandable with digital signal board) and 4 pulse outputs on board; signal board expands onboard I/O; up to 3 communication modules for serial communication; up to 2 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC to PLC communication	Article number	
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ROFINET interface [X1]\Digital in hannel address 10.2 ROFINET interface [X1]\Digital in nable rising edge etection 0 ROFINET interface [X1]\Digital in nable falling edge etection 0		Falling edge1	Falling edge1	-	
ROFINET interface [X1]\Digital in nable rising edge etection ardware interrupt: 0 ROFINET interface [X1]\Digital in nable falling edge etection	nputs\Channel2				
nable rising edge etection ardware interrupt: 0 ROFINET interface [X1]\Digital in nable falling edge etection	nputs\Channel2\	Input filters	6.4 millisec	Enable pulse catch	U
ardware interrupt: 0 ROFINET interface [X1]\Digital in nable falling edge 0 etection	, , , , , , , , , , , , , , , , , , , ,	RidPrefixRisingEdg-	49154	Event name:	0
ROFINET interface [X1]\Digital in nable falling edge 0 etection		eEvent Rising edge2	Rising edge2		
etection	nputs\Channel2\				
ardware interrupt: 0		RidPrefixFallingEdg- eEvent		Event name:	0
-		Falling edge2	Falling edge2		
ROFINET interface [X1]\Digital in hannel address 10.3	nnuto)Che was 12	Input filters	6.4 millisec	Enable pulse catch	0
ROFINET interface [X1]\Digital in	nputs\Channel3				
nable rising edge 0 etection		RidPrefixRisingEdg- eEvent	49155	Event name:	0
ardware interrupt: 0		Rising edge3	Rising edge3		
ROFINET interface [X1]\Digital in		n: n c: - :	40202	-	
nable falling edge 0 etection	nputs\Channel3\	□DidDrofivEallingEdg		Event name:	0
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hannel address 10.4	nputs\Channel3\			Enable pulse catch	0
	nputs\Channel3\	eEvent	6.4 millisec	parae euteri	-

Automation Porta	ıl				
	[X1]\Digital inputs\Channel4\				
nable rising edge etection	0	RidPrefixRisingEdg- eEvent	49156	Event name:	0
ardware interrupt:		Rising edge4	Rising edge4		
	[X1]\Digital inputs\Channel4\	Did Due fire Falling of day	40204	F	0
nable falling edge etection	U	RidPrefixFallingEdg- eEvent	49284	Event name:	0
ardware interrupt:		Falling edge4	Falling edge4		
ROFINET interface nannel address	X1]\Digital inputs\Channel5	Input filters	6.4 millisec	Enable pulse catch	0
	[X1]\Digital inputs\Channel5\	input inters	0.4 millisec	Eliable puise catch	O
nable rising edge		RidPrefixRisingEdg-	49157	Event name:	0
etection ardware interrupt:	0	eEvent Rising edge5	Rising edge5		
ROFINET interface	X1]\Digital inputs\Channel5\	mining cages	inising cages		
nable falling edge	0	RidPrefixFallingEdg- eEvent	49285	Event name:	0
ardware interrupt:	0	Falling edge5	Falling edge5		
	X1]\Digital inputs\Channel6				
nannel address	0.6 X1]\Digital inputs\Channel6\	Input filters	6.4 millisec	Enable pulse catch	0
nable rising edge		RidPrefixRisingEdg-	49158	Event name:	0
tection		eEvent			
ardware interrupt:	0 [X1]\Digital inputs\Channel6\	Rising edge6	Rising edge6		
nable falling edge		RidPrefixFallingEdg-	49286	Event name:	0
etection		eEvent			
ardware interrupt: OFINET interface l	0 [X1]\Digital inputs\Channel7	Falling edge6	Falling edge6		
nannel address	10.7	Input filters	6.4 millisec	Enable pulse catch	0
	X1]\Digital inputs\Channel7\				-
nable rising edge etection	0	RidPrefixRisingEdg- eEvent	49159	Event name:	0
ardware interrupt:	I .	Rising edge7	Rising edge7		
	[X1]\Digital inputs\Channel7\	D' 1D (' E II' E I	10207	-	
nable falling edge	0	RidPrefixFallingEdg- eEvent	49287	Event name:	0
ardware interrupt:		Falling edge7	Falling edge7		
	[X1]\Analog inputs\Noise reduction				
tegration time OFINET interface l	50 Hz (20 ms) X1]\Analog inputs\Channel0				
nannel address	IW64	Measurement type	Voltage	Voltage range	010 V
noothing	Weak (4 cycles)			Enable overflow di-	1
OCINICT interface I				agnostics	
(OFINE) interface i	X1I\Analog inputs\Channel1				
nannel address	[X1]\Analog inputs\Channel1 W66	Measurement type	Voltage	Voltage range	010 V
nannel address moothing		Measurement type	Voltage	Enable overflow di-	010 V
nannel address noothing	IW66	Measurement type	Voltage		
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nannel address moothing ROFINET interface eaction to CPU TOP	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value	Measurement type	Voltage	Enable overflow di-	
nannel address moothing ROFINET interface eaction to CPU FOP ROFINET interface	IW66 Weak (4 cycles) X1]\Digital outputs	Substitute a value	Voltage	Enable overflow di-	
nannel address moothing ROFINET interface eaction to CPU FOP ROFINET interface	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0	Substitute a value of 1 on a change		Enable overflow di-	
nannel address moothing ROFINET interface eaction to CPU FOP ROFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0	Substitute a value		Enable overflow di-	
nannel address noothing ROFINET interface eaction to CPU TOP ROFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0 Q0.0	Substitute a value of 1 on a change from RUN to STOP.		Enable overflow di-	
nannel address noothing COFINET interface eaction to CPU COP COFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0 Q0.0 X1]\Digital outputs\Channel1	Substitute a value of 1 on a change from RUN to STOP.	0	Enable overflow di-	
nannel address noothing OFINET interface eaction to CPU OP OFINET interface nannel address OFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0 Q0.0 X1]\Digital outputs\Channel1 Q0.1 X1]\Digital outputs\Channel2	Substitute a value of 1 on a change from RUN to STOP. Substitute a value of 1 on a change from RUN to STOP.	0	Enable overflow di-	
nannel address noothing COFINET interface eaction to CPU OP COFINET interface nannel address COFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0 Q0.0 X1]\Digital outputs\Channel1 Q0.1	Substitute a value of 1 on a change from RUN to STOP. Substitute a value of 1 on a change from RUN to STOP. Substitute a value	0	Enable overflow di-	
nannel address noothing COFINET interface eaction to CPU COP COFINET interface nannel address COFINET interface nannel address	IW66 Weak (4 cycles) X1]\Digital outputs Use substitute value X1]\Digital outputs\Channel0 Q0.0 X1]\Digital outputs\Channel1 Q0.1 X1]\Digital outputs\Channel2 Q0.2	Substitute a value of 1 on a change from RUN to STOP. Substitute a value of 1 on a change from RUN to STOP.	0	Enable overflow di-	
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Totally Integrated Automation Porta						
PROFINET interface	[X1]\Advor	nced options\Real time se	attings\IO communica	tion		
Send clock:	1.000ms	iced options(kear time se	ettings (10 communica	tion		
		nced options\Real time se	ttings\Real time option	ons		
Calculated band- width for cyclic IO	0.000ms					
data: PROFINET interface l	[X1]\Advar	nced options\Port [X1 P1]	\General			
Name	Port_1		Author	AA_LAB7	Comment	
	_	nced options\Port [X1 P1]				
Local port:	[X1]\Port_	DFINET interface_1 1 [X1 P1]	Medium:	Copper	Cable name:	
			West calls	o.		
			0			
			: <u>0</u>			
PROFINET interface	X1]\Advar	nced options\Port [X1 P1]	\Port interconnection	\Partner port:		
	Monitorin	g of partner port is not	Alternative partners		Partner port:	CSM 1277_1\SCALANCE interface
Medium:	possible Copper		Cable length:			[X1]\Port_2 [X1 P2]
		nced options\Port [X1 P1]				
Activate this port for			·			
use PROFINET interface l	[X1]\Advar	nced options\Port [X1 P1]	\Port options\Connect	tion		
Transmission rate /		•	Monitor	False	Enable autonegotia-	True
duplex:	[Y1]\	nced options\Port [X1 P1]	\Port ontions\Pound	rios	tion	
End of detection of		icea options(Fort [X FFT]	End of topology dis-		End of the sync do-	False
accessible devices			covery		main	
PROFINET interface Hardware identifier		nced options\Port [X1 P1]	\Hardware identifier\I	Hardware identifier		
PROFINET interface		server access				
Enable Web server			The Web server			
using this interface			must also be activa- ted in the properties			
			of the PLC.			
PROFINET interface Hardware identifier		vare identifier\Hardware	identifier Hardware identifier	64		
High speed counters		C1\General\Enable	riaidware identifier	O T		
Enable this high speed counter	0					
•	(HSC)\HSC	C1\General\Project inform	nation			
Name	HSC_1	·	Comment			
High speed counters Type of counting	Count	C1\Function	Operating phase	Single phase		
Counting direction		ram (internal direction	Initial counting di-	Count up		
is specified by	control)		rection	·		
Frequency measur- ing period	-/-sec					
		C1\Reset to initial values\				
Initial counter value	0		Initial reference val- ue	0		
		C1\Reset to initial values\	Reset options			
Use external reset input	0		Reset signal level	-1-		
•	(HSC)\HSC	C1\Event configuration\				
Generate interrupt	_		RidPrefixCvEqualsPv	49152	Event name:	0
for counter value equals reference						
value event.						
Hardware interrupt:	0		Counter value equal to reference value0	Counter value equal to reference value0	ValueNull	0
ValueNull	0		EventPriority	6		
	_	C1\Event configuration\	ni in C = c :=	40.400		
Generate interrupt for external reset	U		RidPrefixExternalRe- set	49408	Event name:	0
event.						
Hardware interrupt: ValueNull	0		External reset0 EventPriority	External reset0 6	ValueNull	0
	-	C1\Event configuration\		-		
Generate interrupt				49280	Event name:	0
for change of direction event.			Change			
Hardware interrupt:	0		Change of direc-	Change of direction0	ValueNull	0
ValueNull	0		tion0 EventPriority	6		
High speed counters		C1\Hardware inputs\				
Clock generator in- put			HSCInput0_Status	1	Direction input	
Reset input			Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDis- play
Adapter name the user control should use for the Output Source	HscChann	el.OutputSource	<u>9</u>	I	J MJO GI CEDISPIRY	I

Automation Porta	nl				
igh speed counters	(HSC)\HSC1\Hardware inputs\				
irection input		HSCInput1_Status	1	Clock generator in- put	
eset input	 	Adapter name the	HscChannel.AddressString	Adapter name the	HscChannel.SpeedAndSourceDi
		user control should		II .	play
		use for the address		use for the Spee-	
	11. 61. 10. 15	string		dAndSourceDisplay	
dapter name the ser control should	HscChannel.OutputSource				
e for the Output					
urce					
gh speed counters	(HSC)\HSC1\Hardware inputs\				
set input		HSCInput2_Status	1	Clock generator in-	
		A.I t th	LL. Ch. a. LAdda as Chica	put	LL. Ch. and Co. alfander a D'
rection input		Adapter name the user control should	HscChannel.AddressString	Adapter name the user control should	HscChannel.SpeedAndSourceDi
		use for the address		use for the Spee-	Pray
		string		dAndSourceDisplay	
dapter name the	HscChannel.OutputSource				
er control should					
e for the Output urce					
	(HSC)\HSC1\I/O addresses\Input a	addresses			
art address	1000	End address	1003	Organization block	0
cess image	0			<u> </u>	
h speed counters	(HSC)\HSC1\Hardware identifier	Hardware identifier			
rdware identifier	257				
gh speed counters	(HSC)\HSC2\General\Enable				
able this high	0				
eed counter	(1100)\1100010				
	G (HSC)\HSC2\General\Project info				
me	HSC_2 (HSC)\HSC2\Function	Comment			
gn speed counters pe of counting	Count	Operating phase	Single phase		
unting direction	User program (internal direction	Initial counting di-	Count up		
specified by	control)	rection	, F		
equency measur-	-/-sec				
g period					
-	(HSC)\HSC2\Reset to initial value				
tial counter value	0	Initial reference val-	0		
		ue			
	(HSC)\HSC2\Reset to initial value		1.		
se external reset		s\Reset options Reset signal level	- -		
se external reset put	0	Reset signal level	-1-		
se external reset put gh speed counters	0 (HSC)\HSC2\Event configuration	Reset signal level			la .
se external reset put igh speed counters enerate interrupt	0 (HSC)\HSC2\Event configuration	Reset signal level		Event name:	0
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se external reset put gh speed counters enerate interrupt r counter value quals reference ilue event. ardware interrupt:	0 s (HSC)\HSC2\Event configuration\ 0	Reset signal level RidPrefixCvEqualsPv Counter value equal to reference value1	49152 Counter value equal to reference value1		
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Totally Integrated Automation Porta					
Adapter name the user control should use for the Output Source	HscChannel.OutputSource				
	(HSC)\HSC2\I/O addresses\Input ac	ldresses			
Start address	1004	End address	1007	Organization block	0
Process image	0				
	(HSC)\HSC2\Hardware identifier\H	ardware identifier			
Hardware identifier					
	(HSC)\HSC3\General\Enable				
Enable this high speed counter	O				
•	(HSC)\HSC3\General\Project inform	nation			
Name	HSC_3	Comment			
	(HSC)\HSC3\Function				
Type of counting	Count	Operating phase	Single phase		
Counting direction is specified by	User program (internal direction control)	Initial counting di- rection	Count up		
	-/-sec	rection	1		
ng period	, 555				
	(HSC)\HSC3\Reset to initial values				
nitial counter value	0	Initial reference val-	0		
ligh speed counters	(HSC)\HSC3\Reset to initial values	ue Reset ontions			
Jse external reset		Reset signal level	-/-		
nput		J			
	(HSC)\HSC3\Event configuration\		10450	-	
Generate interrupt for counter value	U	RidPrefixCvEqualsPv	49152	Event name:	0
or counter value equals reference					
value event.					
Hardware interrupt:	0		Counter value equal to reference	ValueNull	0
/alueNull	0	to reference value2	1		
	0 (HSC)\HSC3\Event configuration\	EventPriority	6		
Generate interrupt		RidPrefixExternalRe-	49408	Event name:	0
or external reset		set			
event.					
Hardware interrupt: /alueNull	0	External reset2	External reset2	ValueNull	0
	U (HSC)\HSC3\Event configuration\	EventPriority	6		
Generate interrupt		RidPrefixDirection-	49280	Event name:	0
or change of direc-		Change	13200	Lvent name.	
ion event.		_			
Hardware interrupt:	0	Change of direc- tion2	Change of direction2	ValueNull	0
/alueNull	0	EventPriority	6		
	(HSC)\HSC3\Hardware inputs\	Evena noney			
Clock generator in-		HSCInput0_Status	1	Direction input	
out					
Reset input		Adapter name the user control should	HscChannel.AddressString	Adapter name the user control should	HscChannel.SpeedAndSourceDisplay
		use for the address		use for the Spee-	pray
		string		dAndSourceDisplay	
Adapter name the user control should	HscChannel.OutputSource				
user control should use for the Output					
Source					
	(HSC)\HSC3\Hardware inputs\				
Direction input		HSCInput1_Status	1	Clock generator in-	
Reset input		Adapter name the	HscChannel.AddressString	put Adapter name the	HscChannel.SpeedAndSourceDis-
reset iliput		user control should	riscerialitiei. Address stillig	user control should	play
		use for the address		use for the Spee-	
N. I	He Chan I O to to	string		dAndSourceDisplay	
Adapter name the user control should	HscChannel.OutputSource				
use for the Output					
Source	(1155)111553111				
	(HSC)\HSC3\Hardware inputs\	HSCInput2_Status	1	Clock generater in	
Reset input		niscmputz_Status		Clock generator in- put	,
Direction input		Adapter name the	HscChannel.AddressString	Adapter name the	HscChannel.SpeedAndSourceDis-
-		user control should		user control should	play
		use for the address string		use for the Spee- dAndSourceDisplay	
Adapter name the	HscChannel.OutputSource	·	1		1
user control should	·				
use for the Output Source					
	 	ldresses			
Start address	1008	End address	1011	Organization block	0
rocess image	0		·		·
	(HSC)\HSC3\Hardware identifier\H	ardware identifier			
lardware identifier					
High speed counters Enable this high	(HSC)\HSC4\General\Enable				
enable this nigh speed counter	U				
•	: (HSC)\HSC4\General\Project inform	nation			
Name	HSC_4	Comment			
	(HSC)\HSC4\Function				
Type of counting	Count	Operating phase	Single phase		
Counting direction is specified by	User program (internal direction control)	Initial counting di- rection	Count up		
. Specifica by			1	JL	

roduonay moscur	Loos]			
g period	-/-sec				
ign speed counters litial counter value	s (HSC)\HSC4\Reset to initial values 0	Initial reference val-	0		
igh speed counters	 (HSC)\HSC4\Reset to initial values	ue Reset options			
se external reset iput	0	Reset signal level	-1-		
•	(HSC)\HSC4\Event configuration\	RidPrefixCvEqualsPv	40152	Event name:	0
or counter value		Marienacvequaisrv	77132	Event name.	
equals reference value event.					
lardware interrupt:	0	Counter value equal to reference value3	Counter value equal to reference value3	ValueNull	0
	0 (HSC)\HSC4\Event configuration\	EventPriority	6		
Generate interrupt	=	RidPrefixExternalRe-	49408	Event name:	0
or external reset event.		set			
Hardware interrupt: ValueNull	0	External reset3 EventPriority	External reset3	ValueNull	0
ligh speed counters	(HSC)\HSC4\Event configuration\				I-
Generate interrupt or change of direc-	0	RidPrefixDirection- Change	49280	Event name:	0
ion event. lardware interrupt:	0	Change of direc-	Change of direction3	ValueNull	0
	0	tion3 EventPriority	6		
ligh speed counters	(HSC)\HSC4\Hardware inputs\		-		
Clock generator in- out		HSCInput0_Status	1	Direction input	
Reset input		Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
Adapter name the user control should use for the Output Source	HscChannel.OutputSource			J	
ligh speed counters	(HSC)\HSC4\Hardware inputs\				
Pirection input		HSCInput1_Status	1	Clock generator in- put	
Reset input		Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
Adapter name the user control should use for the Output Source	HscChannel.OutputSource				
	s (HSC)\HSC4\Hardware inputs\	USCImput2 Status	1	Clark ways yets yin	
Reset input		HSCInput2_Status	1	Clock generator in- put	
Direction input		Adapter name the user control should use for the address string	HscChannel. Address String	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
	HscChannel.OutputSource				
ser control should se for the Output					
iser control should use for the Output cource ligh speed counters	s (HSC)\HSC4\I/O addresses\Input ad		1015	Ounce size 42	lo.
iser control should ise for the Output Jource ligh speed counters start address	s (HSC)\HSC4\I/O addresses\Input ad 1012		1015	Organization block	0
iser control should ise for the Output Jource High speed counters Start address Process image High speed counters	1012 0 s (HSC)\HSC4\Hardware identifier\H	End address	1015	Organization block	0
iser control should use for the Output source ligh speed counters start address Process image ligh speed counters lardware identifier ligh speed counters	1012 0 s (HSC)\HSC4\Hardware identifier\H 260 s (HSC)\HSC5\General\Enable	End address	1015	Organization block	0
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Automation Porta					
enerate interrupt	(HSC)\HSC5\Event configuration\	RidPrefixExternalRe-	49408	Event name:	0
or external reset vent.		set			
ardware interrupt: alueNull	0	External reset4 EventPriority	External reset4	ValueNull	0
	(HSC)\HSC5\Event configuration\	Evenu nonty			
enerate interrupt or change of direc-	0	RidPrefixDirection- Change	49280	Event name:	0
ion event.		_			
lardware interrupt:	0	Change of direc- tion4	Change of direction4	ValueNull	0
'alueNull	0	EventPriority	6		
ligh speed counters lock generator in-	(HSC)\HSC5\Hardware inputs\	USCInnutO Status	1	Direction input	
ut		HSCInput0_Status	I	Direction input	
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ser control should se for the Output ource					
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Reset input Adapter name the	HscChannel.OutputSource	Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
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irection input		Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the SpeedAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
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iser control should ise for the Output	HscChannel.OutputSource	Sumg			
	(HSC)\HSC5\I/O addresses\Input ad	ldresses	1010	Organization block	lo.
ser control should se for the Output ource ligh speed counters tart address rocess image	: (HSC)\HSC5\I/O addresses\Input ad 1016	ldresses End address	1019	Organization block	0
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ser control should se for the Output ource ligh speed counters tart address rocess image ligh speed counters lardware identifier	(HSC)\HSC5\I/O addresses\Input ad 1016 0 (HSC)\HSC5\Hardware identifier\H	ldresses End address	1019	Organization block	0
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ser control should se for the Output ource ligh speed counters tart address rocess image ligh speed counters lardware identifier ligh speed counters nable this high peed counters ligh speed counters	(HSC)\HSC5\I/O addresses\Input ad 1016 (HSC)\HSC5\Hardware identifier\H 261 (HSC)\HSC6\General\Enable (HSC)\HSC6\General\Project inform	Idresses End address ardware identifier	1019	Organization block	0
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Reset input		Adapter name the	HscChannel.AddressString	Adapter name the	HscChannel.SpeedAndSourceDis
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dapter name the ser control should se for the Output ource	HscChannel.OutputSource				
ligh speed counters	s (HSC)\HSC6\Hardware inputs\	HSCInput1_Status	1	Clock generator in-	
•				put	
Reset input		Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the Spee- dAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
Adapter name the user control should use for the Output Source	HscChannel.OutputSource				
High speed counters Reset input	s (HSC)\HSC6\Hardware inputs\	HSCInput2_Status	1	Clock generator in-	
·		. –		put	
Direction input		Adapter name the user control should use for the address string	HscChannel.AddressString	Adapter name the user control should use for the Spee- dAndSourceDisplay	HscChannel.SpeedAndSourceDisplay
Adapter name the user control should use for the Output Source	HscChannel.OutputSource				
High speed counters Start address	s (HSC)\HSC6\I/O addresses\Input 1020	addresses End address	1023	Organization block	0
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Hardware identifier					
Enable this pulse generator	TO/PWM)\PTO1/PWM1\General\Er				
Pulse generators (P1 Name	TO/PWM)\PTO1/PWM1\General\Pr Pulse_1	oject information Comment			
Pulse generators (PT Signal type	FO/PWM)\PTO1/PWM1\Parameter PWM	Time base:	Milliseconds	Pulse duration for- mat	Hundredths
Cycle time	100ms	Initial pulse dura- tion	50Hundredths		
Pulse generators (PT Enable direction	TO/PWM)\PTO1/PWM1\Hardware	outputs			
output					
Pulse generators (P) Pulse output	FO/PWM)\PTO1/PWM1\Hardware	PulseOutput1_Sta-	1	Adapter name the	Pulse Channel. Address String
		tus S- Adapter name the		user control should use for the address string	
Adapter name the user control should		s- ∥Adanter name the	PulseChannel.OutputSource		
use for the Spee- dAndSourceDisplay	PulseChannel.SpeedAndSourceDi play	user control should use for the Output Source			
use for the Spee- dAndSourceDisplay	The state of the s	user control should use for the Output Source		Adapter name the user control should use for the address	PulseChannel.AddressString
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use for the Spee-dAndSourceDisplay Pulse generators (PT PulseOutput2_Sta- tus Adapter name the user control should use for the Spee- dAndSourceDisplay Pulse generators (PT Start address Process image	PulseChannel.SpeedAndSourceDiplay TO/PWM)\PTO1/PWM1\I/O address 1000 0	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address	1001	user control should use for the address string	
use for the SpeedandSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address	1001	user control should use for the address string	
use for the SpeedandSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er	user control should use for the Output Source outputs\ Pulse output S Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide	1001	user control should use for the address string	
use for the SpeedandSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse generator	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er	user control should use for the Output Source outputs\ Pulse output S Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide	1001	user control should use for the address string	
use for the SpeedandSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedandSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse generator (PT Enable this pulse generator (PT Use generators (PT)	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Er Pulse_2 FO/PWM)\PTO2/PWM2\General\Pr Pulse_2 FO/PWM)\PTO2/PWM2\Parameter	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide able oject information Comment assignment\Pulse optice	1001 ntifier	user control should use for the address string Organization block	0
Adapter name the user control should use for the SpeedandSourceDisplay PulseOutput2_Status Adapter name the user control should use for the SpeedandSourceDisplay Pulse generators (PT ardware identifier id	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Er Pulse_2 FO/PWM)\PTO2/PWM2\Parameter PWM	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide oject information Comment assignment\Pulse optic Time base:	ntifier ntifier ns Milliseconds	user control should use for the address string	
use for the SpeedandSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedandSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse generator Pulse generators (PT Start address Process image Pulse generators (PT Start address Process image Pulse generators (PT Start address Pulse generators (PT Start address Pulse generators (PT Start address Pulse generators (PT Signal type Cycle time	PulseChannel.SpeedAndSourceDiplay PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Pr Pulse_2 FO/PWM)\PTO2/PWM2\Parameter PWM 100ms	user control should use for the Output Source outputs\ Pulse output S Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide oject information Comment assignment\Pulse optic Time base: Initial pulse duration	1001 ntifier	user control should use for the address string Organization block Pulse duration for-	0
Pulse generators (PT Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT P	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\Hardware 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Er Pulse_2 FO/PWM)\PTO2/PWM2\Parameter PWM	user control should use for the Output Source outputs\ Pulse output S Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide oject information Comment assignment\Pulse optic Time base: Initial pulse duration	ntifier ntifier ns Milliseconds	user control should use for the address string Organization block Pulse duration for-	0
use for the SpeedAndSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse generator Pulse generators (PT Signal type Cycle time Pulse generators (PT Cycle time Pulse generators (PT Cycle time Pulse generators (PT Cycle time	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\I/O address 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Pr Pulse_2 FO/PWM)\PTO2/PWM2\Parameter PWM 100ms FO/PWM)\PTO2/PWM2\Hardware	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide oject information Comment assignment\Pulse optic Time base: Initial pulse duration outputs	ntifier ntifier ns Milliseconds	user control should use for the address string Organization block Pulse duration format Adapter name the user control should	0
use for the SpeedAndSourceDisplay Pulse generators (PT PulseOutput2_Status Adapter name the user control should use for the SpeedAndSourceDisplay Pulse generators (PT Start address Process image Pulse generators (PT Hardware identifier Pulse generators (PT Enable this pulse generators (PT Enable this pulse generators (PT Start address Pulse generators (PT Coulse generato	PulseChannel.SpeedAndSourceDiplay FO/PWM)\PTO1/PWM1\I/O address 1000 0 FO/PWM)\PTO1/PWM1\I/O address 265 FO/PWM)\PTO2/PWM2\General\Er 0 FO/PWM)\PTO2/PWM2\General\Pr Pulse_2 FO/PWM)\PTO2/PWM2\Parameter PWM 100ms FO/PWM)\PTO2/PWM2\Hardware	user control should use for the Output Source outputs\ Pulse output S- Adapter name the user control should use for the Output Source ses\Output addresses End address identifier\Hardware ide oject information Comment assignment\Pulse optic Time base: Initial pulse duration outputs pulseOutput1_Status	ntifier ntifier ns Milliseconds	user control should use for the address string Organization block Pulse duration format Adapter name the	0 Hundredths

Totally Integrated Automation Porta					
Pulse generators (PT	O/PWM)\PTO2/PWM2\Hardware ou	itputs\			
PulseOutput2_Sta- tus	1	Pulse output		Adapter name the user control should use for the address string	PulseChannel.AddressString
use for the Spee- dAndSourceDisplay	PulseChannel.SpeedAndSourceDisplay	Adapter name the user control should use for the Output Source	PulseChannel.OutputSource		
	O/PWM)\PTO2/PWM2\I/O addresses		1000		
Start address	1002	End address	1003	Organization block	0
Process image	0	4:6 :	- A161		
	O/PWM)\PTO2/PWM2\Hardware ide	entifier\Hardware ider	ntifier		
Hardware identifier					
Enable this pulse generator	O/PWM)\PTO3/PWM3\General\Enal				
	O/PWM)\PTO3/PWM3\General\Projo Pulse_3	Comment			
	o/PWM)\PTO3/PWM3\Parameter as	-	ns		
Signal type	PWM	Time base:	Milliseconds	Pulse duration for-	Hundredths
Signal type	I VVIVI	Time base.	Millisecorius	mat	Hundredths
Cycle time	100ms	Initial pulse dura- tion	50Hundredths		
Pulse generators (PT	 O/PWM)\PTO3/PWM3\Hardware ou				
Enable direction output	0				
	 O/PWM)\PTO3/PWM3\Hardware ou	itputs\			
Pulse output	, Land Maria Court of the Court	PulseOutput1_Sta-	1	Adapter name the	PulseChannel.AddressString
		tus		user control should use for the address string	
Adapter name the user control should use for the SpeedAndSourceDisplay	PulseChannel.SpeedAndSourceDisplay	Adapter name the user control should use for the Output Source	PulseChannel.OutputSource		
	O/PWM)\PTO3/PWM3\Hardware ou				
PulseOutput2_Sta- tus		Pulse output		Adapter name the user control should use for the address string	Pulse Channel. Address String
Adapter name the user control should use for the SpeedAndSourceDisplay	PulseChannel.SpeedAndSourceDisplay	Adapter name the user control should use for the Output Source	Pulse Channel. Output Source		
Pulse generators (PT	O/PWM)\PTO3/PWM3\I/O addresses	s\Output addresses			
Start address	1004	End address	1005	Organization block	0
Process image	0				
	O/PWM)\PTO3/PWM3\Hardware ide	entifier\Hardware ider	ntifier		
Hardware identifier	267				
Pulse generators (PT	O/PWM)\PTO4/PWM4\General\Enal	ole			
Enable this pulse	0				
generator					
	O/PWM)\PTO4/PWM4\General\Proj				
	Pulse_4	Comment			
	O/PWM)\PTO4/PWM4\Parameter as				
Signal type	PWM	Time base:	Milliseconds	Pulse duration for- mat	Hundredths
Cycle time	100ms	Initial pulse dura-	50Hundredths		
Pulse generators (PT	O/PWM)\PTO4/PWM4\Hardware ou	tion stouts			
Enable direction					
output					
•	O/PWM)\PTO4/PWM4\Hardware ou	itputs\			
Pulse output		PulseOutput1_Sta- tus	1	Adapter name the user control should use for the address string	Pulse Channel. Address String
Adapter name the user control should use for the SpeedAndSourceDisplay	PulseChannel.SpeedAndSourceDisplay	Adapter name the user control should use for the Output Source	PulseChannel.OutputSource	Jumg	
	 O/PWM)\PTO4/PWM4\Hardware ou				
PulseOutput2_Sta-		Pulse output		Adapter name the	PulseChannel.AddressString
tus		·		user control should use for the address string	-
Adapter name the user control should	PulseChannel.SpeedAndSourceDisplay	Adapter name the user control should	PulseChannel.OutputSource		
use for the Spee- dAndSourceDisplay	O/PWM)\PTO4/PWM4\I/O addresses	use for the Output Source			
Start address	1006	End address	1007	Organization block	0
Process image	0				
Pulse generators (PT Hardware identifier Startup	O/PWM)\PTO4/PWM4\Hardware ide 268	entifier\Hardware ider	ntifier		
-	Warm restart - mode before POWER		Startup CPU even if mismatch	Configuration time for central and dis-	60000ms
	OFF	to actual configura- tion		tributed I/O	
OBs should be inter- ruptible	OFF				

	150ms					Enable min cle time fo OBs	nimum cy- 0 or cyclic	
linimum cycle time						000		
ommunication load ycle load due to	20%							
ommunication ystem and clock m	emorv\Svstem	memory bits						
nable the use of ystem memory	0	,	Address of system memory byte (MB			First cycle		
yte Piagnostic status			Always 1 (high)	,		Always 0 (low)	
hanged	VCI I	1	Always I (Iligii)			Aiways o (1011)	
ystem and clock monable the use of	0	emory bits	Address of clock	0		10 Hz cloc	k	
lock memory byte Hz clock			memory byte (MB 2.5 Hz clock	(x)		2 Hz clock		
.25 Hz clock			1 Hz clock			0.625 Hz c		
.5 Hz clock Veb server\General								
ctivate Web server n all modules of	False		Permit access only with HTTPS	y True				
his device	tie wedete							
Veb server\Automa nable automatic	True		Update interval	Os				
pdate ime of day\Local tir	me							
ime zone		Berlin, Bern, Brussels	5,					
ime of day\Dayligh		mii, vieiliid						
activate daylight aving time	1		Difference between standard and day- light saving time					
ime of day\Dayligh		tart of daylight sav		Sunday		of	March	
nonth:	01:00 a.m.							
ime of day\Dayligh	t saving time\S	tart of standard tin	1e	Conde		of	October	
t	02:00 a.m.			Sunday		ОТ	October	
Protection evel of protection	No protection							
Protection\Connection	•	s						
Permit access with PUT/GET communication from remote partner (PLC, HMI, DPC,)	False							
Configuration control Allow to reconfigure the device via the		n control for centra	l configuration					
ıser program								
Anchor (Addresses))verview/Manu			True		Address ga	aps False	
Anchor (AddressesOnputs	True		Outputs					
nputs lot	True True			, mac			, u.sc	
nputs Slot Anchor (AddressesO Type A	True True OverviewMenu) oddr. from	\Overview of addre	esses Module	PIP	DP	PN	Rack	Slot
nputs lot anchor (AddressesO ype A	True True OverviewMenu) ddr. from	NOverview of address Addr. to	Module DI 8/DQ 6_1	PIP None	DP -	,,	Rack 0	1 1
nputs Flot Anchor (AddressesO ype 0 6 1	True True OverviewMenu) ddr. from 4 000	Addr. to 0 67 1003	Module DI 8/DQ 6_1 AI 2_1 HSC_1	PIP None None None	DP - - -	,,	Rack 0 0	1 1 1 2 1 16
nputs Slot Anchor (AddressesO ype A 6 1	True True OverviewMenu) ddr. from 4 000 004	Addr. to 0 67 1003	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2	PIP None None None None	DP	,,	Rack 0 0 0	1 1 1 2 1 16 1 17
nputs Slot Anchor (AddressesO Type A 6 1 1	True True OverviewMenu) ddr. from 4 000	Addr. to 0 67 1003	Module DI 8/DQ 6_1 AI 2_1 HSC_1	PIP None None None	DP	,,	Rack 0 0	1 1 1 2 1 16
nputs Slot Anchor (AddressesO) Type A 0 6 1 1 1 1	True True OverviewMenu) ddr. from 4 000 004 008 012 016	Addr. to 0 67 1003 1007 1011 1015 1019	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20
nputs Slot Anchor (AddressesO ype 0 6 1 1 1 1	True True DverviewMenu) ddr. from 4 000 004 008 012 016 020	Addr. to 0 67 1003 1007 1011 1015 1019 1023	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5 HSC_6	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20 1 21
nputs ilot Inchor (AddressesO ype 0 6 1 1 1 1 1 9 0 0 0 0	True True OverviewMenu) ddr. from 4 000 004 008 012 016 020 6	Addr. to 0 67 1003 1007 1011 1015 1019 1023 103 0	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5 HSC_6 AI 4xRTD_1 DI 8/DQ 6_1	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20 1 21 2
nputs Slot Anchor (AddressesO) Type	True True OverviewMenu) ddr. from 4 000 004 008 012 016 020 6	Addr. to 0 67 1003 1007 1011 1015 1019 1023 103 0 1001	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5 HSC_6 AI 4xRTD_1 DI 8/DQ 6_1 Pulse_1	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20 1 21 2 1 1 1 32
nputs lot nchor (AddressesO ype	True True OverviewMenu) ddr. from 4 000 004 008 012 016 020 6	Addr. to 0 67 1003 1007 1011 1015 1019 1023 103 0 1001 1003	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5 HSC_6 AI 4xRTD_1 DI 8/DQ 6_1 Pulse_1 Pulse_2	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20 1 21 2 1 1 1 32 1 33
nputs lot Inchor (AddressesOrype A	True True OverviewMenu) ddr. from 4 000 004 008 012 016 020 6	Addr. to 0 67 1003 1007 1011 1015 1019 1023 103 0 1001	Module DI 8/DQ 6_1 AI 2_1 HSC_1 HSC_2 HSC_3 HSC_4 HSC_5 HSC_6 AI 4xRTD_1 DI 8/DQ 6_1 Pulse_1	PIP None None None None None None None None	DP	,,	Rack 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 1 2 1 16 1 17 1 18 1 19 1 20 1 21 2 1 1 1 32

Main Mumber 1 Type OB Language LAD Ibering automatic Imation Main Program Sweep (Cy-cle)" OB Mumber Main Program Sweep (Cy-cle)" Ion O.1 User-defined ID OB Mumber OB Mumber Mumber Initial_Cal	ain [OB									
bering automatic mation "Main Program Sweep (Cy-cle)" O.1 User-defined ID Bool Remanence emp onstant work 1: **M0.0 **M0.2 **M0.0 **M0.2 **M0.0	neral me	Main	Number	1		Туре	ОВ		Language	LAD
Main Program Sweep (Cy Author cle)*	ımbering	automatic								
Data type e pout Initial_Call Bool Remanence Bool emp onstant work 1: MAD 2	ormation le	"Main Program Swee	ep (Cy- Author			Comment			Family	
Per Data type Default value Initia_Call Bool Remanence Bool work 1: MANO MANO MANO MANO MANO MANO MANO MANO	rsion	cle)"	Llcor dofin	and						
Initial_Call		0.1		ieu						
Initial_Call	me				Data ty	/pe		Default value		
Remanence	Input					•				
emp onstant work 1: MO.0										
work 1:		ence			Bool					
work 1: MM0.0	Constant									
*M0.0					· · · · · · · · · · · · · · · · · · ·			:		
work 2:	WOIN I.									
work 2:										
work 2:				%M0.0 %M0 "start" "awa				%Q0.0 "naped ON"		
%M0.3 "stop" "naped ON" (R) %M0.2 "awaria"								(s)		
%M0.3 "stop" "naped ON" (R) %M0.2 "awaria"										
%M0.3 "stop" "naped ON" (R) %M0.2 "awaria"	work 2.									
	work Z:									
				%M0.3 "stop"				%Q0.0 "naned ON"		
"awaria"								—(R)——		
"awaria"				%M0.2						
				"awaria"						
				1 1						
			l							

Totally Integrated Automation Portal		
PLC_1 [CPU 121	2C AC/DC/Rly]	
Technology objec	ts	
This folder is empty.		
This folder is empty.		

Totally Integrated Automation Portal

PLC_1 [CPU 1212C AC/DC/Rly] / PLC tags / Default tag table [36]

PLC tags

PLC tags				
	Name	Data type	Address	Retain
1	start	Bool	%M0.0	False
-11	gotow	Bool	%M0.1	False
10	awaria	Bool	%M0.2	False
्वा	stop	Bool	%M0.3	False
ा	naped ON	Bool	%Q0.0	False
-111	praca	Bool	%Q0.1	False

Totally Integrated Automation Portal				
PLC_1 [CPU 121 User constants	2C AC/DC/Rly] / PLC tags / Defa	ult tag table [36]		
User constants				
Name		Data type	Value	

Totally Integrated Automation Portal		
PLC_1 [CPU 12120	C AC/DC/Rly]	
PLC data types		
This folder is empty.		

Totally Integrated Automation Portal				
PLC_1 [CPU 121 Force table	2C AC/DC/Rly] / Watch and force tables			
Name Name	Address	Display format	Force value	
Itunic	radiess	Display format	i orec value	

Totally Integrated Automation Portal		
PLC_1 [CPU 121	2C AC/DC/Rly] / Traces	
Measurements		
This folder is empty.		

Totally Integrated Automation Portal	
PLC_1 [CPU 1212C AC/DC/Rly]	•
Text lists	
This folder is empty.	
This folder is empty.	

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PLC_1 [CPU 1212C AC/DC/Rly] / Local modules

AI 4xRTD_1

General\Project info	rmation				
Name	AI 4xRTD_1	Author	AA_LAB7	Comment	
Slot	2	Autiloi	AA_LAB/	Comment	
General\Catalog info	<u> </u>				
_	SM 1231 Al4 x RTD	Description	Analog input module AI4 x RTD	Article number	6ES7 231-5PD32-0XB
Firmware version	V2.0	Description	Arialog Input module AI4 x KTD	Article Humber	0E37 231-3PD32-0AB
	. =				
AI 4xRTD\Project info		C = m = m = m t			
Name	AI 4xRTD_1	Comment			
AI 4xRTD\Module dia	ignostics	A 1 1'' 1 1'			
Enable power sup- ply diagnostics	1	Additional diagnos- tics may be selected for each input/ output.			
AI 4xRTD\Analog inp	uts\Noise reduction				
Integration time	50 Hz (20 ms)				
AI 4xRTD\Analog inp	uts\Channel0				
Channel address	IW96	Measurement type	Thermal resistor (4-wire)	Thermal resistor	Pt 100 standard range
Temperature coeffi-	Pt 0.00385055 ohms/ohms/°C (DIN	Temperature scale	Celsius	Smoothing	Weak (4 cycles)
cient	EN 60751)				
		Enable broken wire diagnostics	0	Enable overflow diagnostics	1
Enable underflow diagnostics	1				
AI 4xRTD\Analog inp	uts\Channel1				
Channel address	IW98	Measurement type	Thermal resistor (4-wire)	Thermal resistor	Pt 100 standard range
Temperature coeffi- cient	Pt 0.00385055 ohms/ohms/°C (DIN EN 60751)	Temperature scale	Celsius	Smoothing	Weak (4 cycles)
	·	Enable broken wire diagnostics	0	Enable overflow diagnostics	1
Enable underflow diagnostics	1				
AI 4xRTD\Analog inp	uts\Channel2				
Channel address	IW100	Measurement type	Thermal resistor (4-wire)	Thermal resistor	Pt 100 standard range
Temperature coeffi- cient	Pt 0.00385055 ohms/ohms/°C (DIN EN 60751)	Temperature scale	Celsius	Smoothing	Weak (4 cycles)
		Enable broken wire diagnostics	0	Enable overflow diagnostics	1
Enable underflow diagnostics	1				
AI 4xRTD\Analog inp					
Channel address	IW102	Measurement type	Thermal resistor (4-wire)	Thermal resistor	Pt 100 standard range
Temperature coeffi- cient	Pt 0.00385055 ohms/ohms/°C (DIN EN 60751)	Temperature scale	Celsius	Smoothing	Weak (4 cycles)
		Enable broken wire diagnostics	0	Enable overflow diagnostics	1
Enable underflow diagnostics	1				
ulagilostics	ses\Input addresses				
AI 4xRTD\I/O address	The state of the s			0 ' ' ' ' '	0
	96	End address	103	Organization block	U
AI 4xRTD\I/O address		End address	103	Organization block	U

Totally Integrated Automation Portal	
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PLC_1 [CPU 1212C AC/DC/Rly] / Local modules

AQ 2x14BIT_1

AQ 2x14BIT_1					
General\Project info	rmation				
Name	AQ 2x14BIT_1	Author	AA_LAB7	Comment	
Slot	3				
General\Catalog info	ormation				
Short designation	SM 1232 AQ2	Description	Analog output module AQ2 x 14 bits; plug-in terminal blocks; output: +/-10V and 0 to 20 mA; selectable diagnostics; selectable substitute value for output	Article number	6ES7 232-4HB32-0XB0
Firmware version	V2.0				
AQ 2\Project information	ation				
Name	AQ 2x14BIT_1	Comment			
AQ 2\Module diagno	ostics				
Enable power sup- ply diagnostics	1	Additional diagnos- tics may be selected for each input/ output.			
AQ 2\Analog output	s				
Reaction to CPU STOP	Use substitute value				
AQ 2\Analog output	s\Channel0				
Channel address	QW112	Analog output type	Voltage	Voltage range	+/- 10 V
Substitute value for channel on a change from RUN to STOP				Enable short circuit diagnostics	1
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AQ 2\Analog output	s\Channel1				
Channel address	QW114	Analog output type	Voltage	Voltage range	+/- 10 V
Substitute value for channel on a change from RUN to STOP				Enable short circuit diagnostics	1
Enable overflow diagnostics	1	Enable underflow diagnostics	1		
AQ 2\I/O addresses\0	Output addresses				
Start address	112	End address	115	Organization block	0
Process image	0		•		
	tifier\Hardware identifier				
AQ 2\Hardware iden	unei maruware idenunei				