OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_42.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

Legend:
- ✕ V [IC10.A2 (at pin) / IC10.A1 (at pin)]
- ◯ V [IC10.D5 (at pin) / IC10.C4 (at pin)]
- △ V [IC10.A2 (at pin) / IC10.A1 (at pin)]
- ☐ V [IC10.D5 (at pin) / IC10.C4 (at pin)]

Y-axis: Voltage -mV-

X-axis: Time (ns)
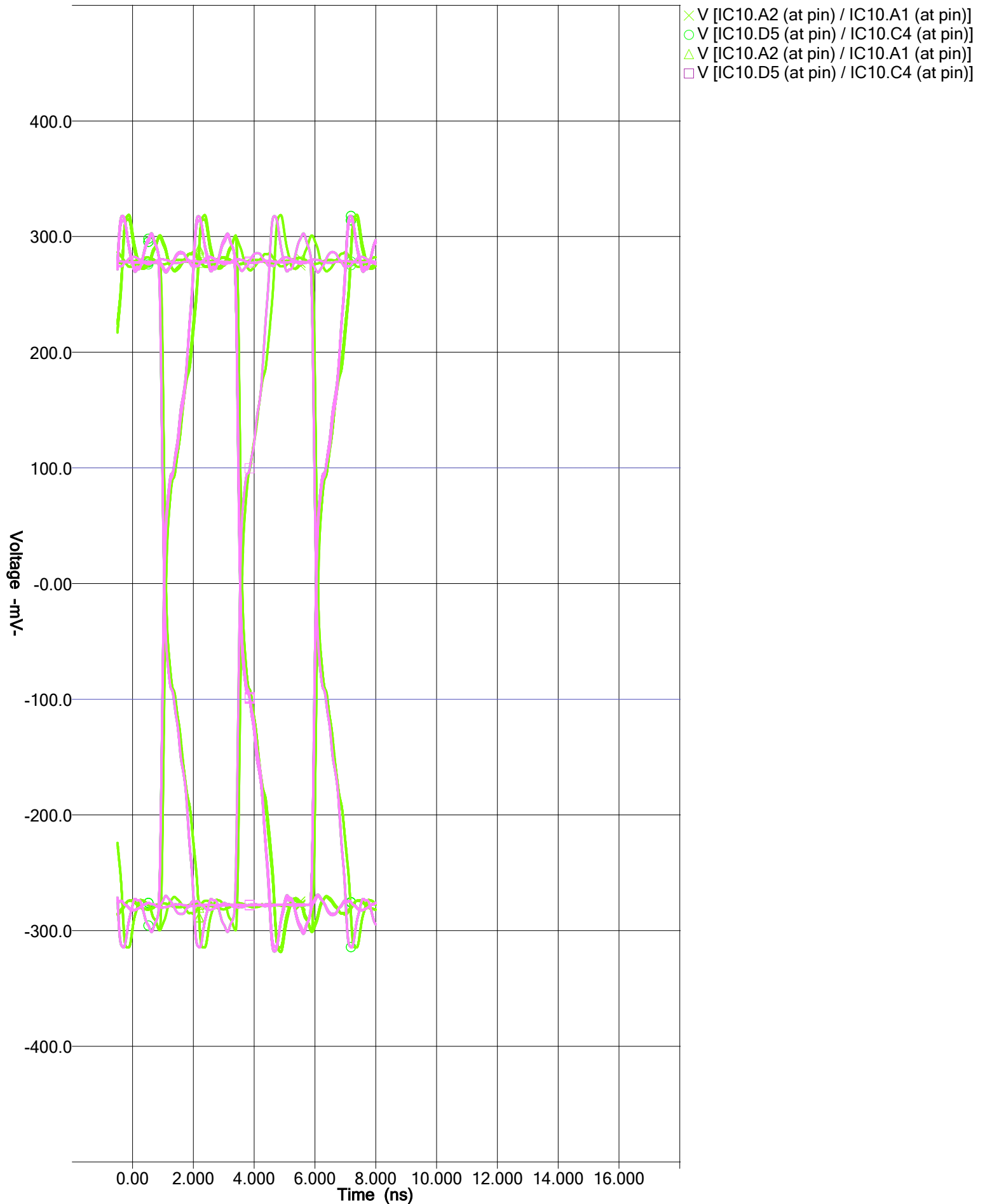
Date: Sunday Jan. 21, 2018   Time: 12:44:09
Net name: FPGA_BANK35_DIFF15_P