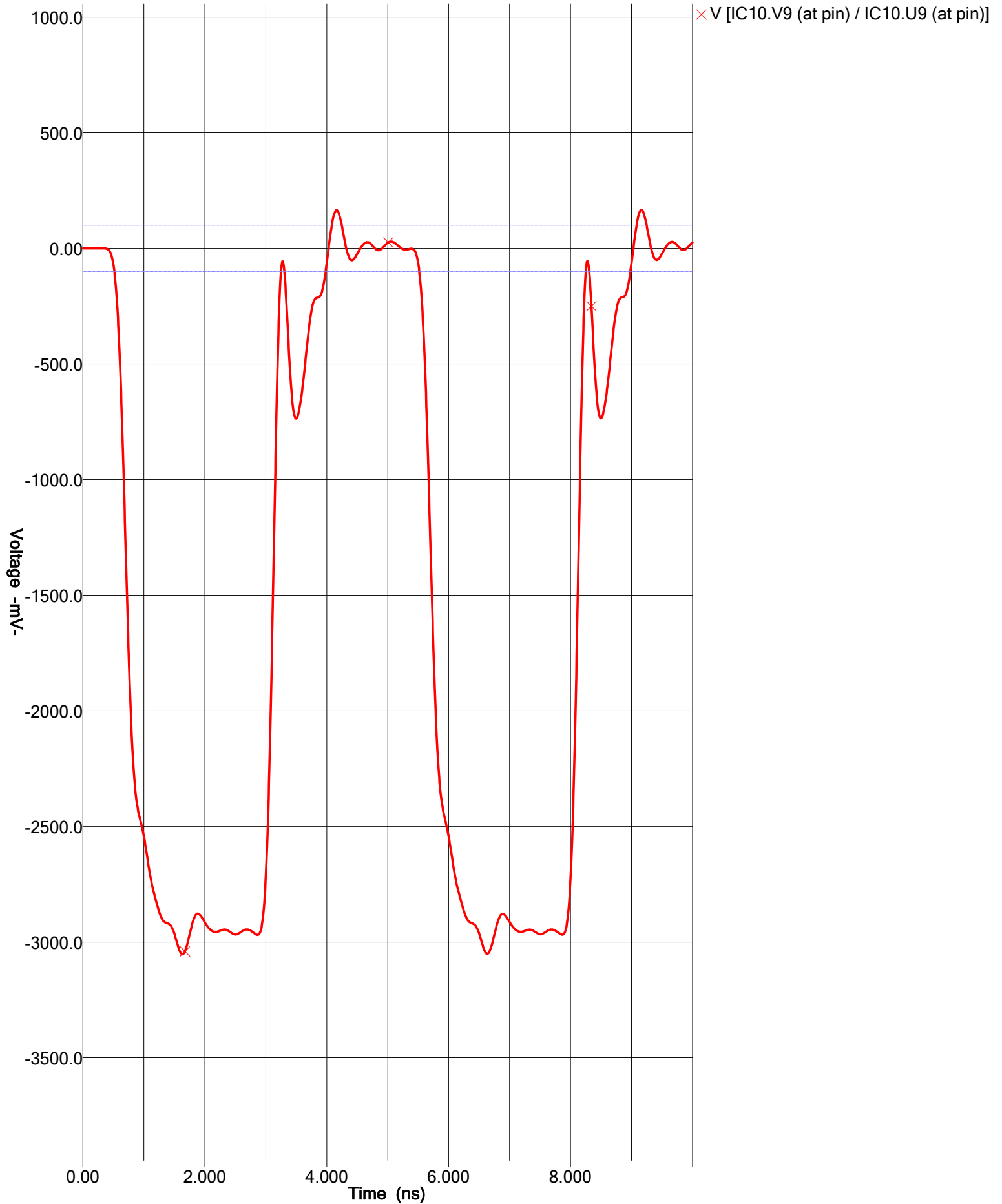


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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_37.HYP

Designer: Piotr Zdunek

HyperLynx v9.4.2



Date: Saturday Jan. 20, 2018 Time: 16:02:59

Net name: CLK_MGT0_P

Show Latest Waveform = YES, Show Previous Waveform = YES