OSCILLOSCOPE

Design file: SPEKTROP2_CMV4000_V1.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

Legend:
× V [IC10_B00.C4 (at pin) / IC10_B00.D5 (at pin)]
○ V [IC10_B00.C4 (at pin) / IC10_B00.D5 (at pin)]
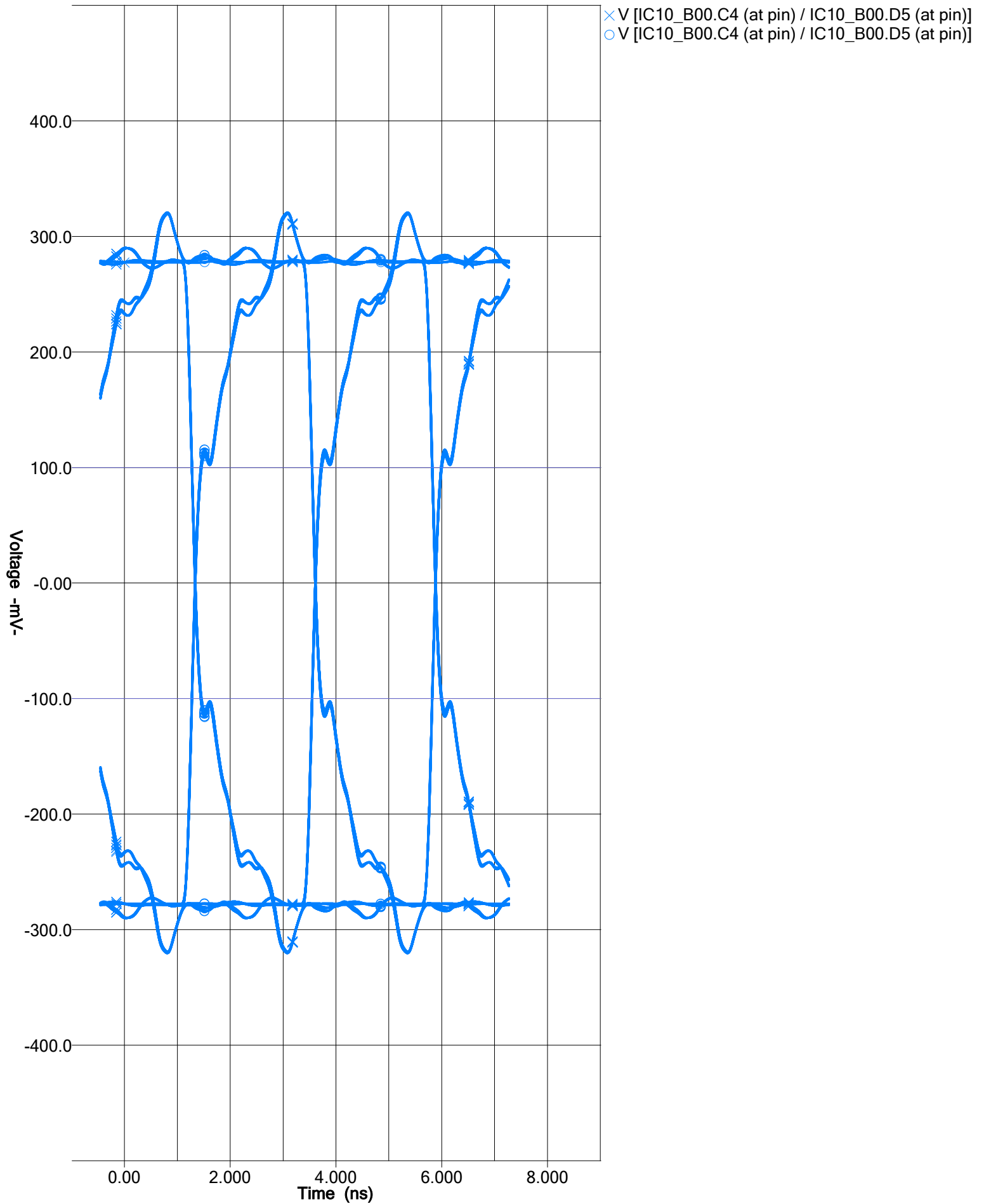
Date: Wednesday Jan. 24, 2018   Time: 0:34:26
Net name: FPGA_BANK35_DIFF12_N_B00