OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_39.HYP

HyperLynx v9.4.2

Designer: Piotr Zdunek

V [IC10.R2 (at pin)] V [IC10.M8 (at pin)] V [IC10.U2 (at pin)] V [IC10.U1 (at pin)] ∇V [IC10.T2 (at pin)] ∨ [IC10.T1 (at pin)] 6.000 ∨ [IC10.R7 (at pin)] V [IC10.R5 (at pin)] V [IC10.N6 (at pin)] △ V [IC10.N5 (at pin)] V [IC10.M2 (at pin)] ∇V [IC10.M1 (at pin)] 5.000 ∨ [IC10.L7 (at pin)] ∨ [IC10.L5 (at pin)] \times V [IC10.L4 (at pin)] V [IC10.L2 (at pin)] ∆ V [IC10.L1 (at pin)] □ V [IC10.K7 (at pin)] 4.000 ∇ V [IC10.K5 (at pin)] ∨ [IC10.K4 (at pin)] ∨ [IC10.K3 (at pin)] imesV [IC10.K2 (at pin)] V [IC10.J5 (at pin)] 3.000 △ V [IC10.J3 (at pin)] □ V [IC10.J1 (at pin)] ∇V [IC10.J6 (at pin)] ∨ [IC10.M8 (at pin)] Voltage - V-∨ [IC10.U2 (at pin)] \times V [IC10.U1 (at pin)] 2.000 V [IC10.T2 (at pin)] △ V [IC10.T1 (at pin)] U [IC10.R7 (at pin)] ∇ V [IC10.R5 (at pin)] ∇ [IC10.N6 (at pin)] ∨ [IC10.N5 (at pin)] 1.000 V [IC10.M2 (at pin)] V [IC10.M1 (at pin)] △ V [IC10.L7 (at pin)] V [IC10.L5 (at pin)] ∇V [IC10.L4 (at pin)] V [IC10.L2 (at pin)] 0.00 ∨ [IC10.L1 (at pin)] \times V [IC10.K7 (at pin)] V [IC10.K5 (at pin)] △ V [IC10.K4 (at pin)] V [IC10.K3 (at pin)] ∇ V [IC10.K2 (at pin)] -1.000∨ [IC10.J5 (at pin)] ∨ [IC10.J3 (at pin)] \times V [IC10.J1 (at pin)] ○ V [IC10.J6 (at pin)] -2.000-3.000 0.00 5.000 10.000 15.000 20.000 25.000 30.000 35.000 40.000 45.000 Time (ns)

Date: Sunday Jan. 21, 2018 Time: 1:19:42
Net name: FPGA_BANK34_HSTL_IN_IO5
Show Latest Waveform = YES, Show Previous Waveform = YES