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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_37.HYP

HyperLynx v9.4.2

Designer: Piotr Zdunek

×V [IC3.24 (at pin) / IC3.25 (at pin)] 4.000 3.000 2.000 1.000 Voltage - V-0.00 -1.000 -2.000 -3.000 -4.000 -5.000 0.00 5.000 **Time (ns)** 6.000 7.000 8.000 9.000 1.000 2.000 3.000 4.000

Date: Saturday Jan. 20, 2018 Time: 16:18:10

Net name: CLK_MGT1_N

Show Latest Waveform = YES, Show Previous Waveform = YES