OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_31.HYP

HyperLynx v9.4.2

Designer: Piotr Zdunek

×V [IC10.U5 (at pin) / IC10.V5 (at pin)]

OV [IC10.U5 (at pin) / IC10.V5 (at pin)] 500.0-0.00 -500.0 -1000.0-Voltage -1500.0⁻ -mV--2000.0--2500.0--3000.0--3500.0 -4000.0 4.000 Time (ns) 0.00 2.000 6.000 8.000

Date: Thursday Jan. 18, 2018 Time: 23:19:38 Net name: CLK_MGT1_N