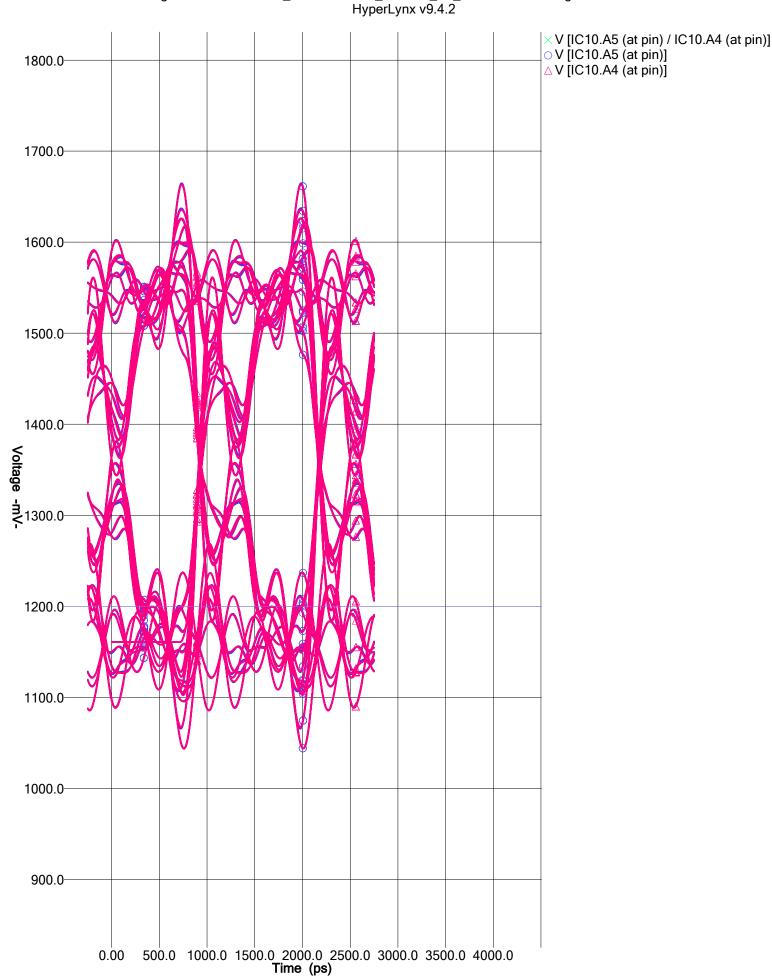
OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP Designer: Piotr Zdunek



Date: Saturday Jan. 20, 2018 Time: 0:16:20 Net name: FPGA_BANK35_DIFF10_N