OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_34.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

× V [IC10.AA5 (at pin) / IC10.AB5 (at pin)]
○ V [IC10.AA5 (at pin) / IC10.AB5 (at pin)]
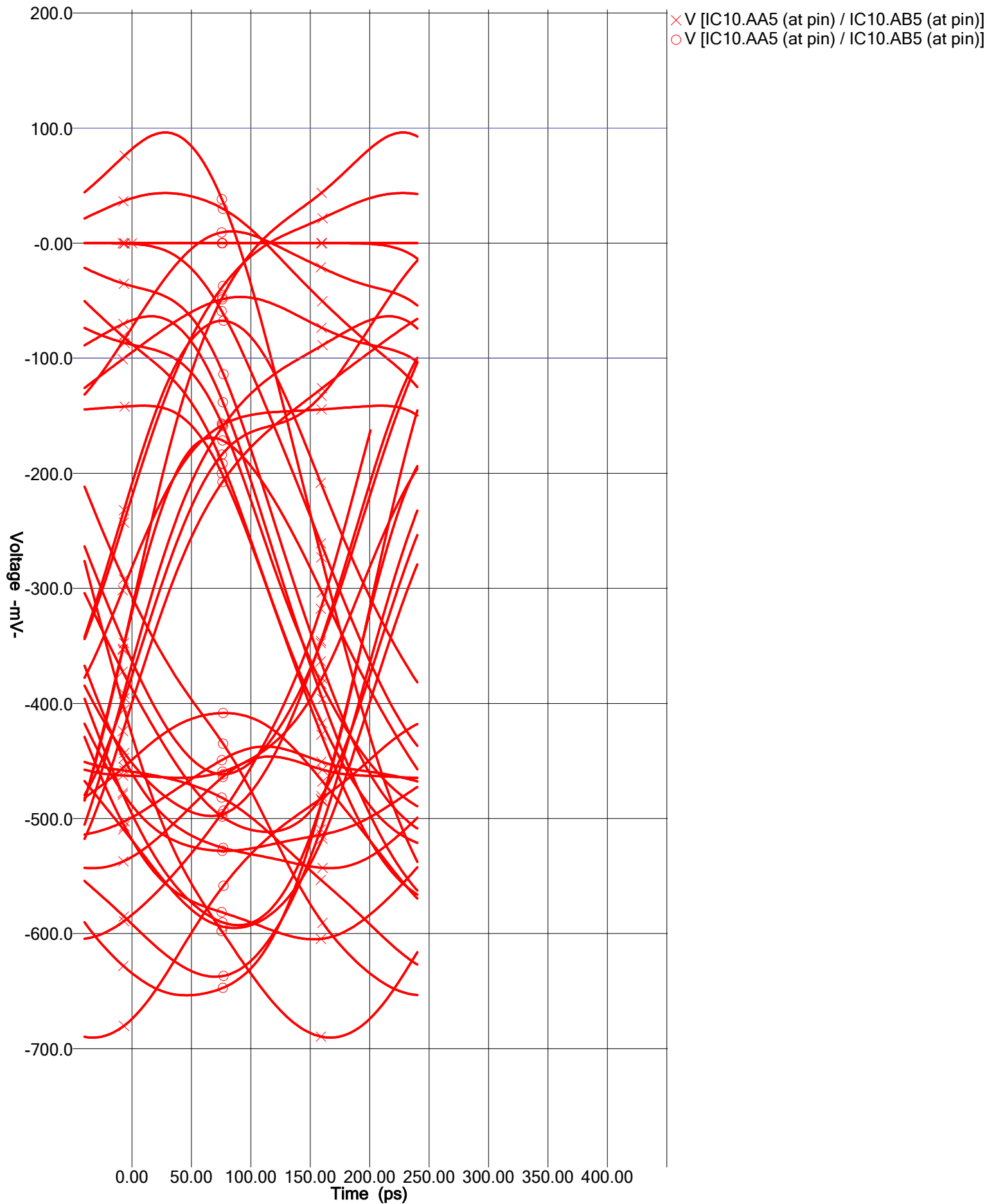
Date: Friday Jan. 19, 2018   Time: 0:37:16
Net name: MGT_TX2_P
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