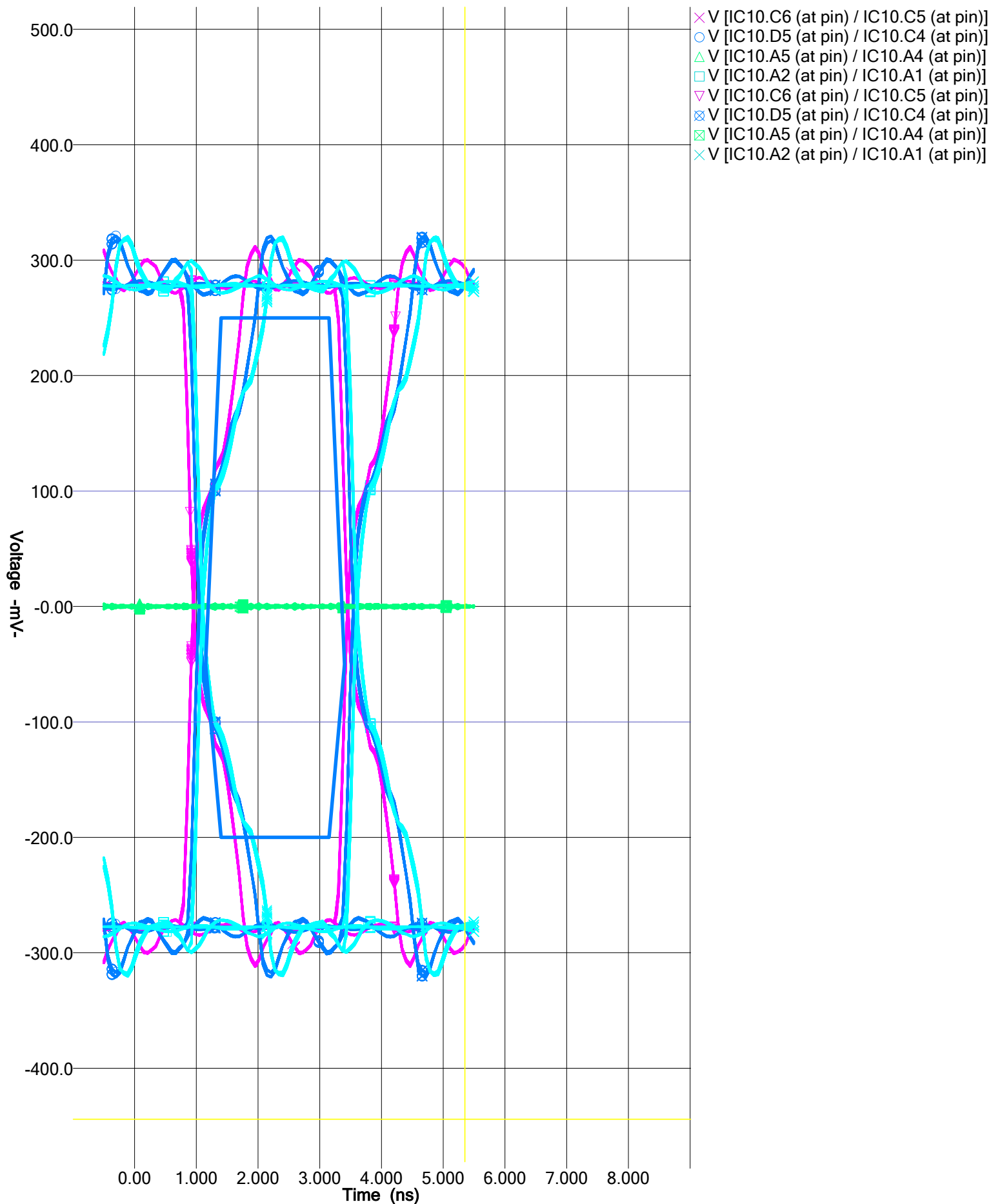


OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP

Designer: Piotr Zdunek

HyperLynx v9.4.2



Date: Saturday Jan. 20, 2018 Time: 0:43:22

Net name: FPGA_BANK35_DIFF10_N

Cursor 1, Voltage = -444.3mV, Time = 5.349ns