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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_27.HYP   Designer: Piotr Zdunek
HyperLynx v9.4.2

× V [IC10.C5 (at pin) / IC10.C6 (at pin)]
○ V [IC10.C5 (at pin) / IC10.C6 (at pin)]
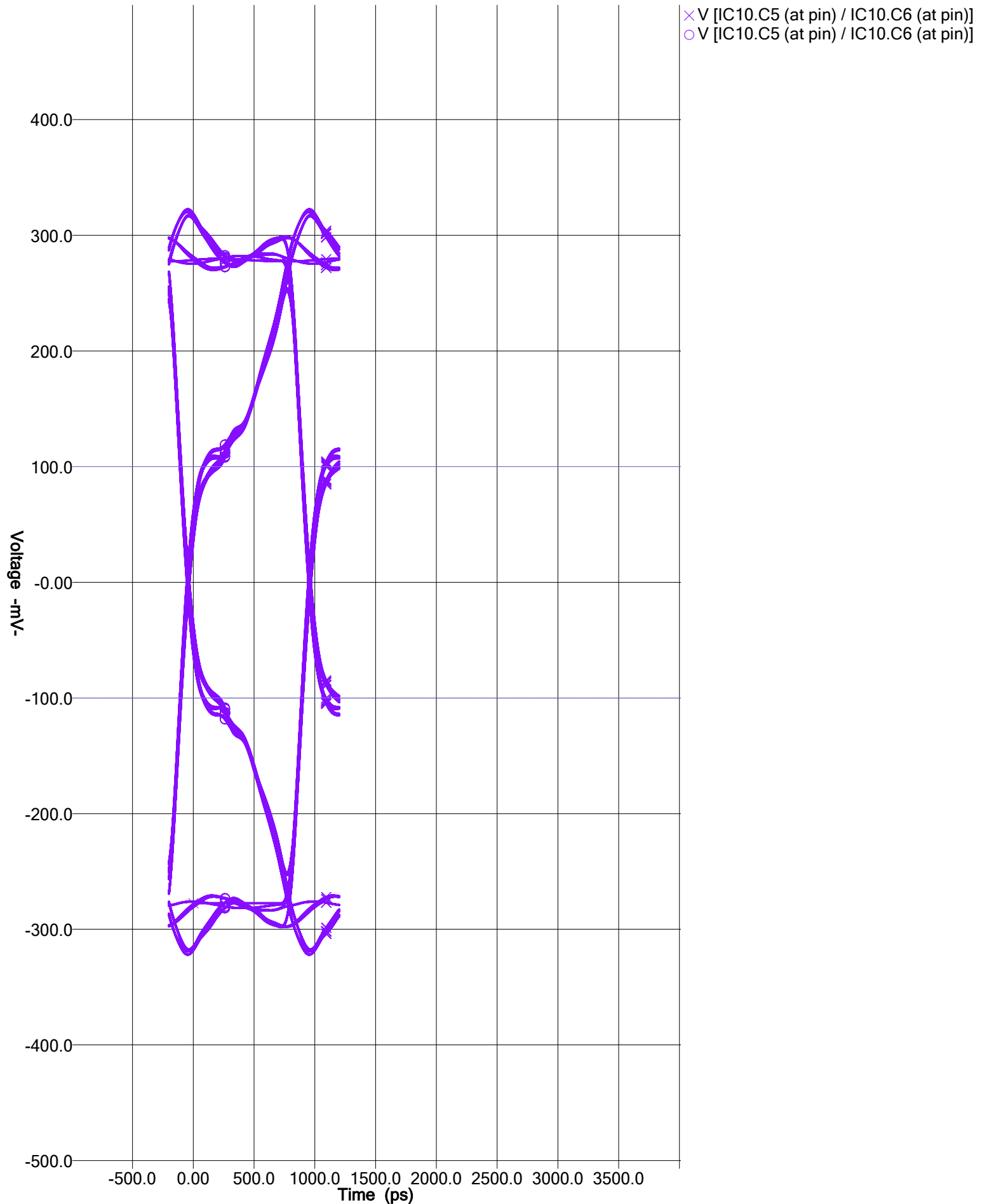
Date: Thursday Jan. 18, 2018   Time: 1:21:45
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