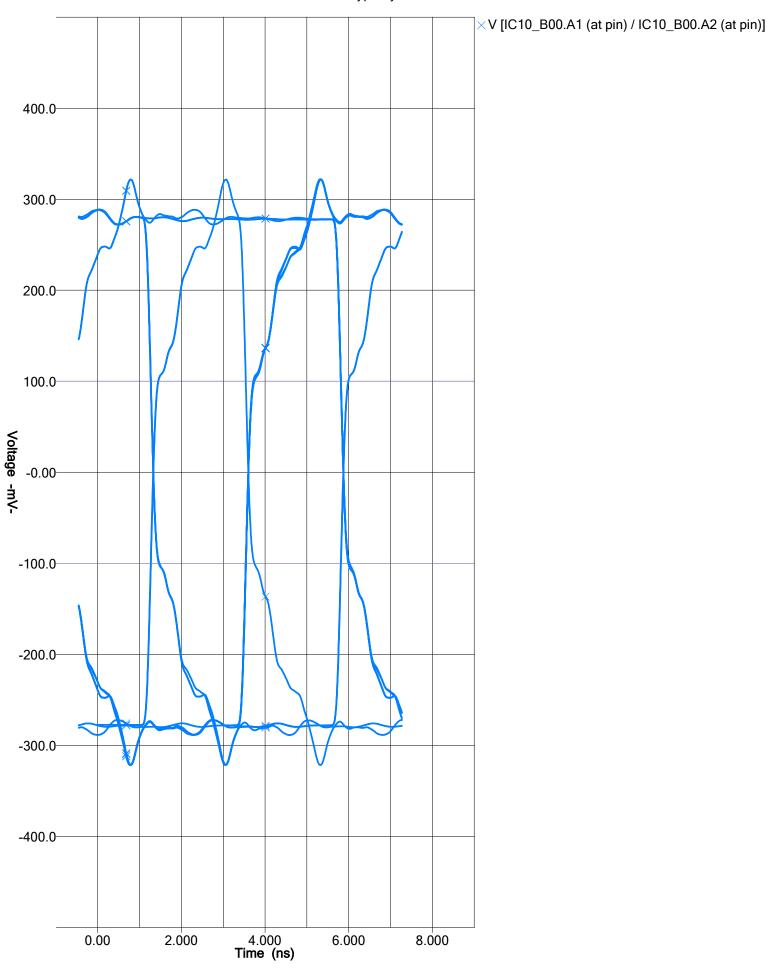
OSCILLOSCOPE

Design file: SPEKTROP2\_CMV4000\_V1.HYP

HyperLynx v9.4.2

Designer: Piotr Zdunek



Date: Wednesday Jan. 24, 2018 Time: 0:11:50 Net name: FPGA\_BANK35\_DIFF15\_P\_B00