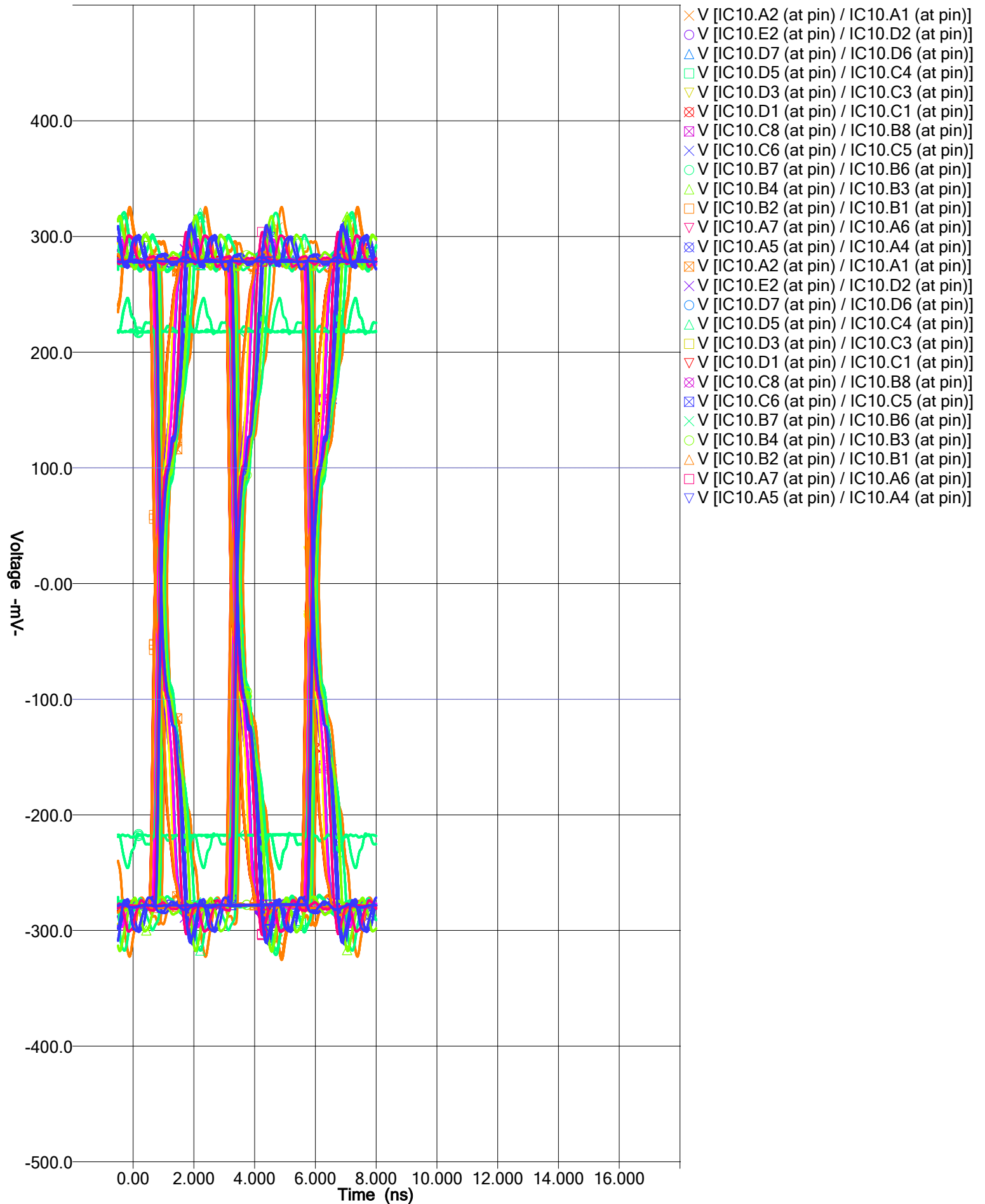


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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP

Designer: Piotr Zdunek

HyperLynx v9.4.2



Date: Saturday Jan. 20, 2018 Time: 1:57:23

Net name: FPGA_BANK35_DIFF15_P