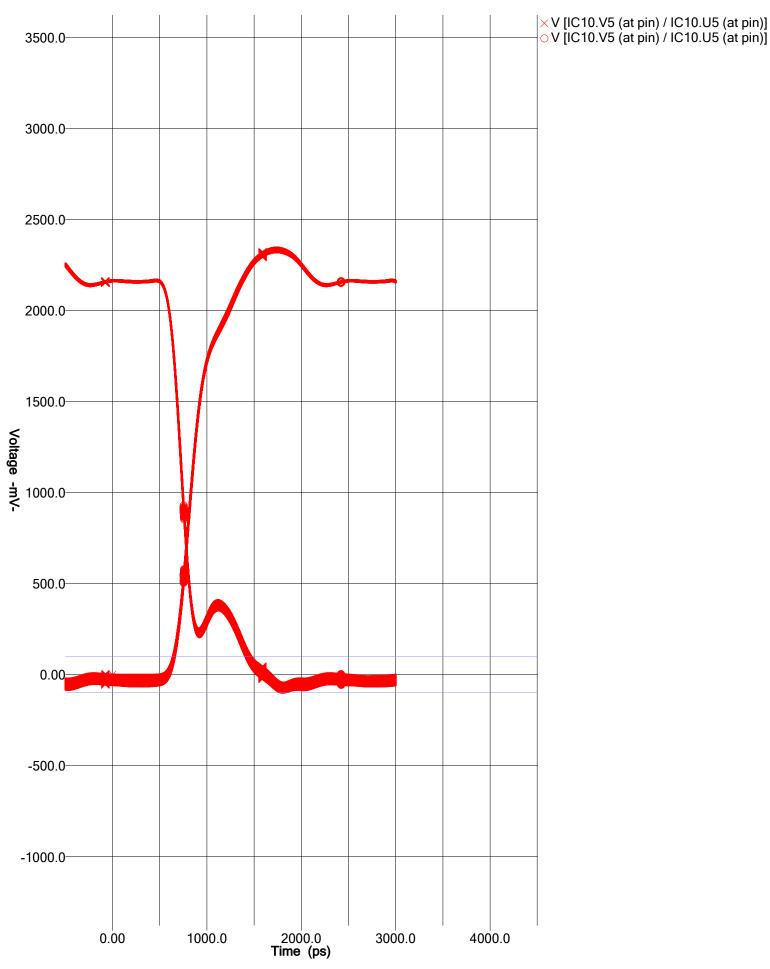
**OSCILLOSCOPE** 

Design file: SPEKTROP2\_PROCESSOR\_BOARD\_V1\_39.HYP

HyperLynx v9.4.2

Designer: Piotr Zdunek



Date: Saturday Jan. 20, 2018 Time: 16:34:31 Net name: CLK\_MGT1\_N