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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_39.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

✕ V [IC10.V9 (at pin) / IC10.U9 (at pin)]

Voltage -mV-

Time (ps)
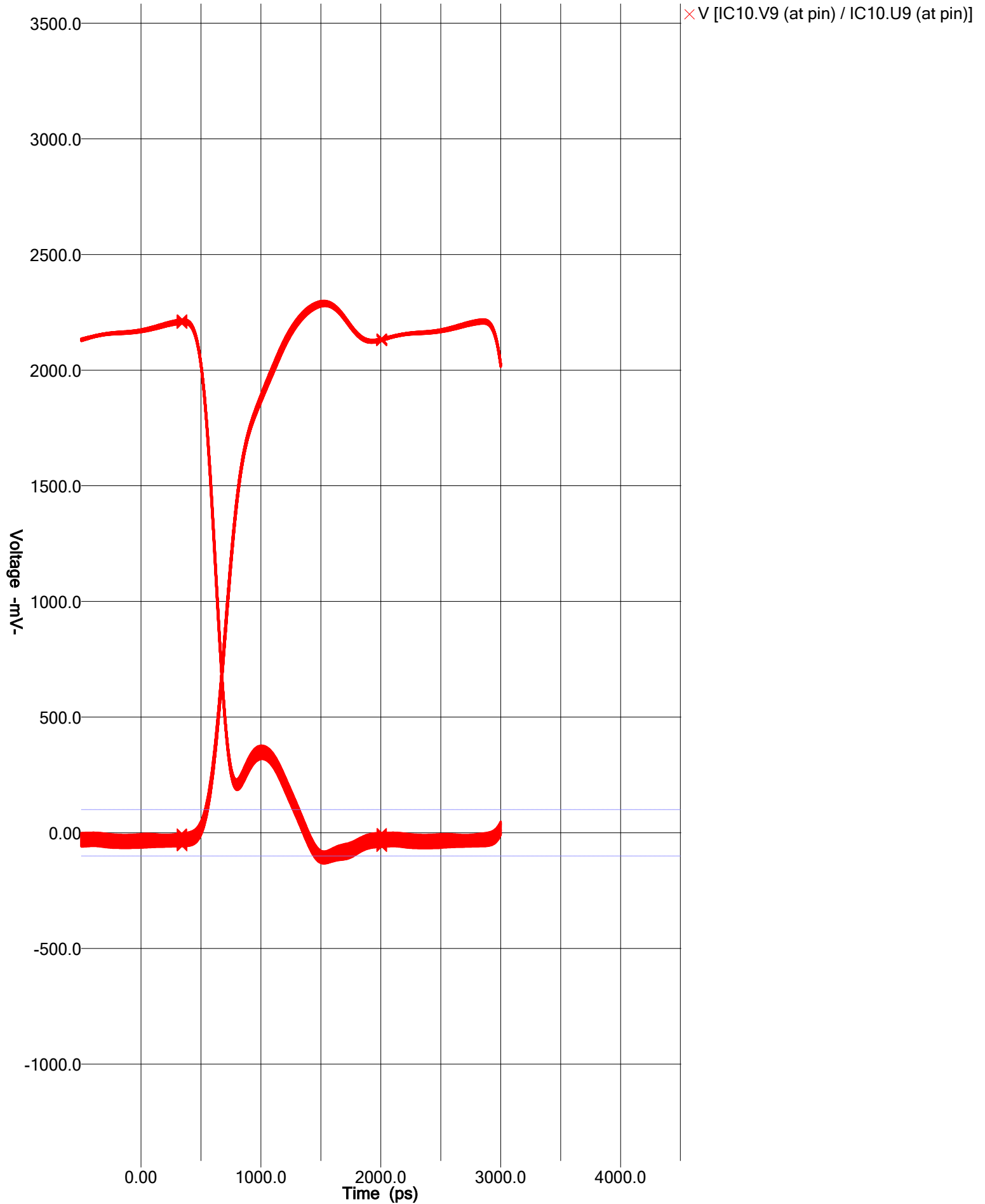
Date: Saturday Jan. 20, 2018   Time: 16:37:52

Net name: CLK_MGT0_P