OSCILLOSCOPE

Design file: SPEKTROP2_PROCESSOR_BOARD_V1_39.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

Legend:
- ✕ V [J9.37 (at pin)]
- ◯ V [IC10.K8 (at pin)]
- △ V [J9.27 (at pin)]
- ▢ V [IC10.J8 (at pin)]
- ▽ V [J9.31 (at pin)]
- ⊠ V [IC10.J7 (at pin)]
- ⊠ V [J9.44 (at pin)]
- ✕ V [IC10.J2 (at pin)]
- ◯ V [J9.37 (at pin)]
- △ V [IC10.K8 (at pin)]
- ▢ V [J9.27 (at pin)]
- ▽ V [IC10.J8 (at pin)]
- ⊠ V [J9.31 (at pin)]
- ⊠ V [IC10.J7 (at pin)]
- ✕ V [J9.44 (at pin)]
- ◯ V [IC10.J2 (at pin)]

Voltage - V-

Time (ns)

Date: Sunday Jan. 21, 2018   Time: 1:27:31
Net name: FPGA_BANK34_HSTL_IO1
Show Latest Waveform = YES,   Show Previous Waveform = YES