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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP   Designer: Piotr Zdunek
HyperLynx v9.4.2

× V [IC10.A2 (at pin) / IC10.A1 (at pin)]
○ V [IC10.A2 (at pin) / IC10.A1 (at pin)]
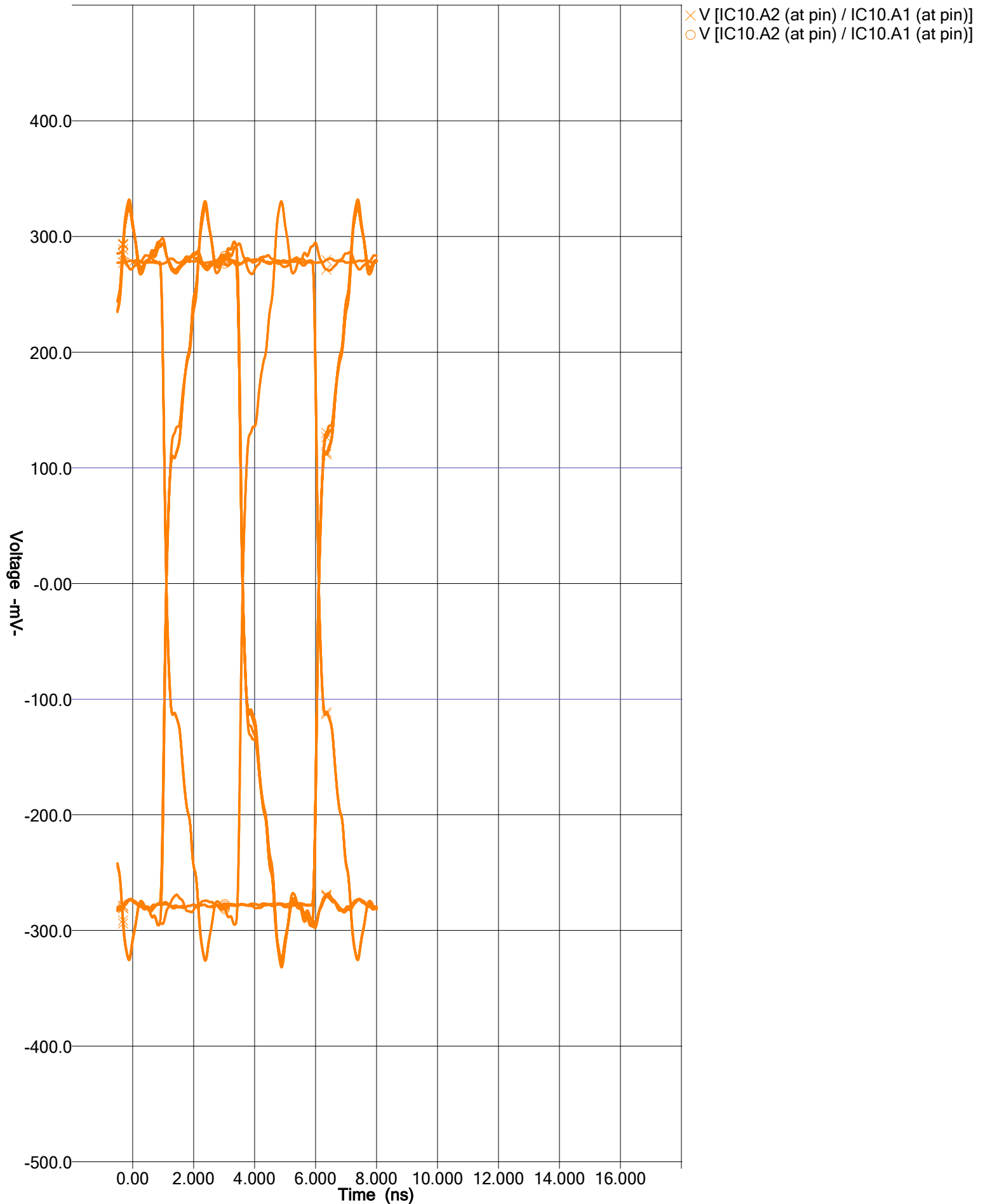
Date: Sunday Jan. 21, 2018   Time: 1:55:21
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