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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP   Designer: Piotr Zdunek

HyperLynx v9.4.2

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○ V [IC10.AA7 (at pin) / IC10.AB7 (at pin)]
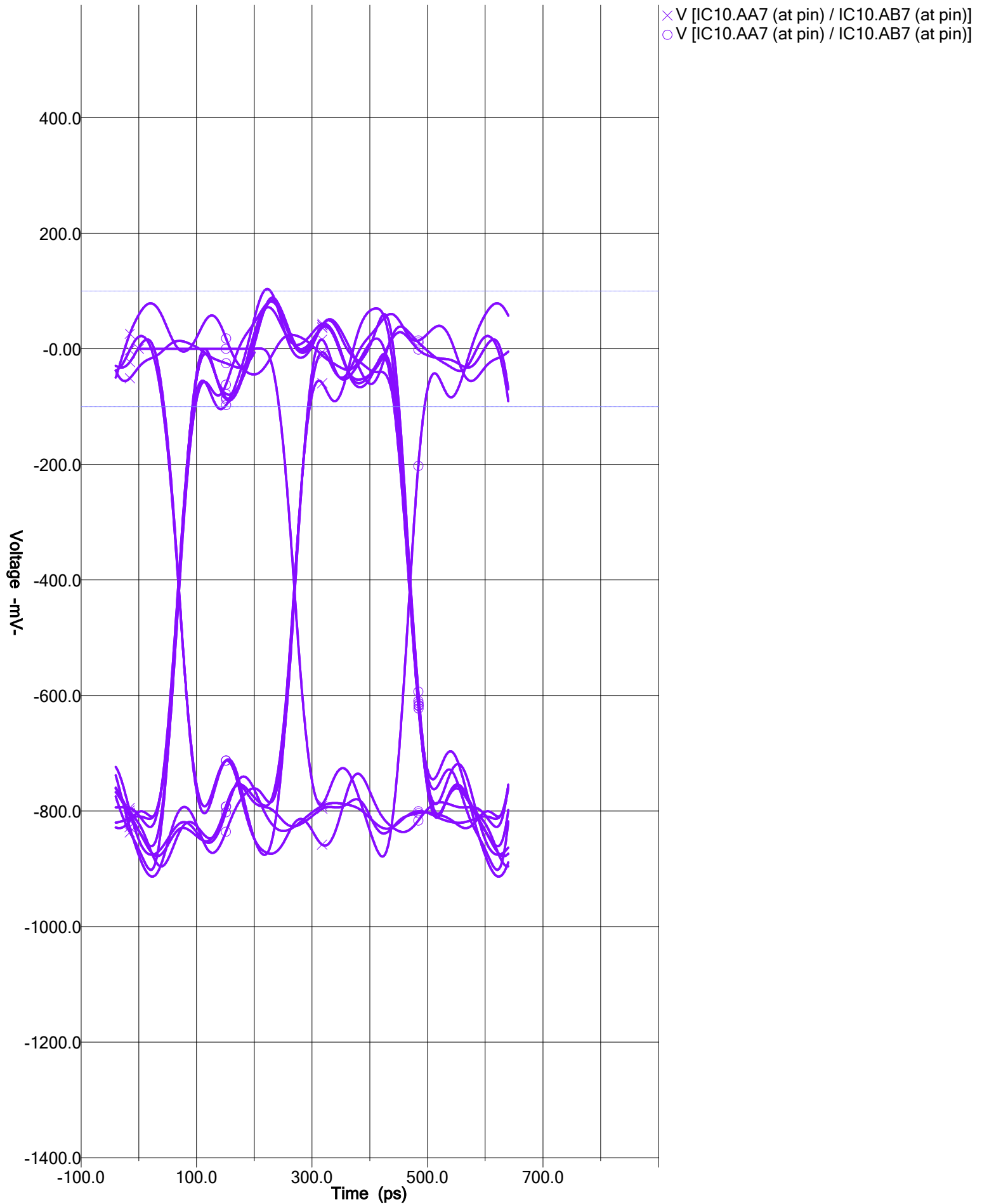
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