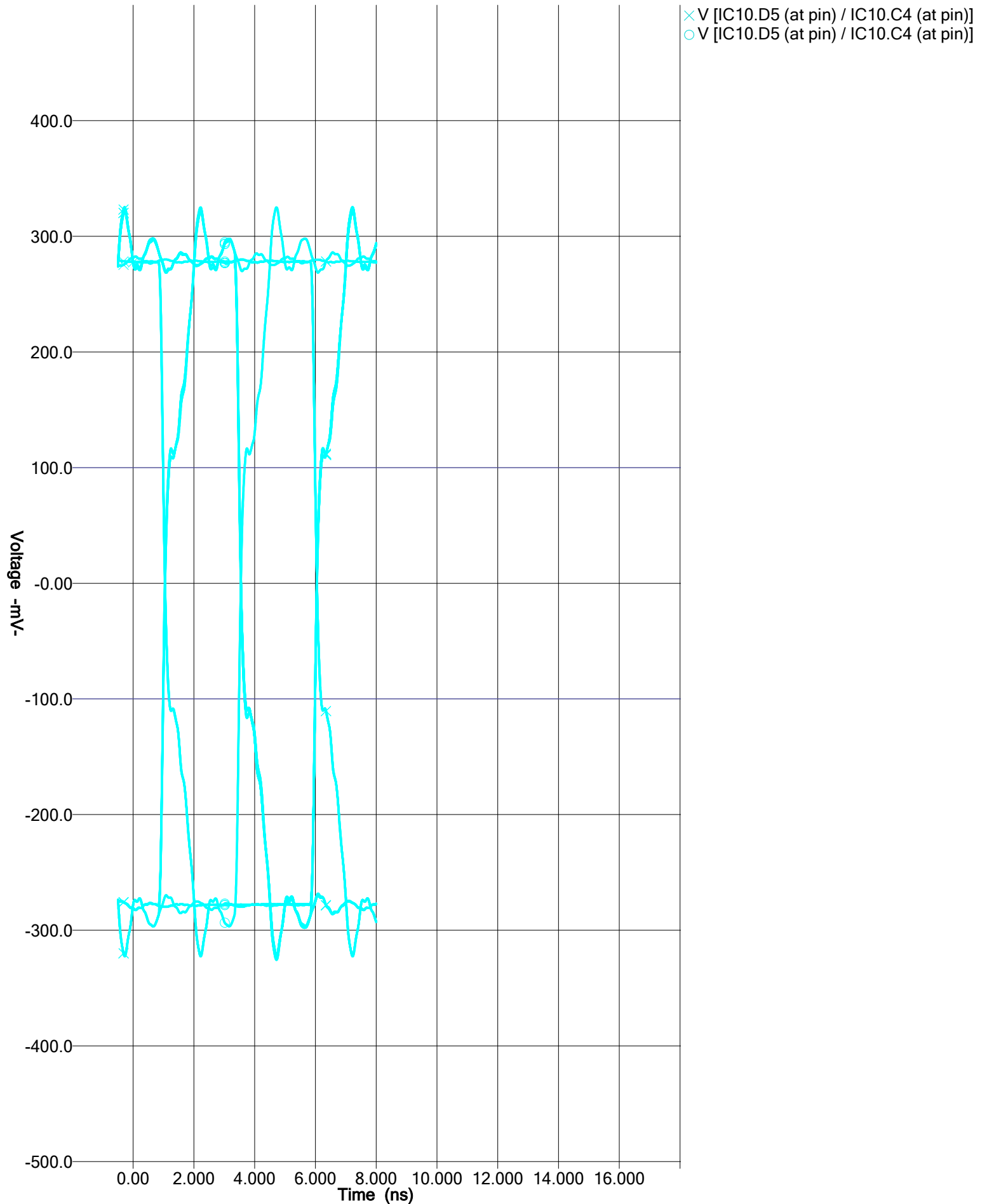


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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP
HyperLynx v9.4.2

Designer: Piotr Zdunek



Date: Sunday Jan. 21, 2018 Time: 1:50:11

Net name: FPGA_BANK35_DIFF12_N