OSCILLOSCOPE Design file: SPEKTROP2_PROCESSOR_BOARD_V1_39.HYP Designer: Piotr Zdunek HyperLynx v9.4.2 ×V [J9.9 (at pin)] ○V [IC10.L4 (at pin)] △ V [J9.9 (at pin)] V [IC10.L4 (at pin)] 1000.0 900.0 800.0 700.0 Voltage -mV-600.0 500.0 400.0 300.0 200.0

Date: Saturday Jan. 20, 2018 Time: 16:43:14
Net name: FPGA_BANK34_HSTL_IN_IO18
Show Latest Waveform = YES, Show Previous Waveform = YES

6.000

7.000

8.000

9.000

5.000 **Time (ns)**

100.0

0.00

1.000

2.000

3.000

4.000