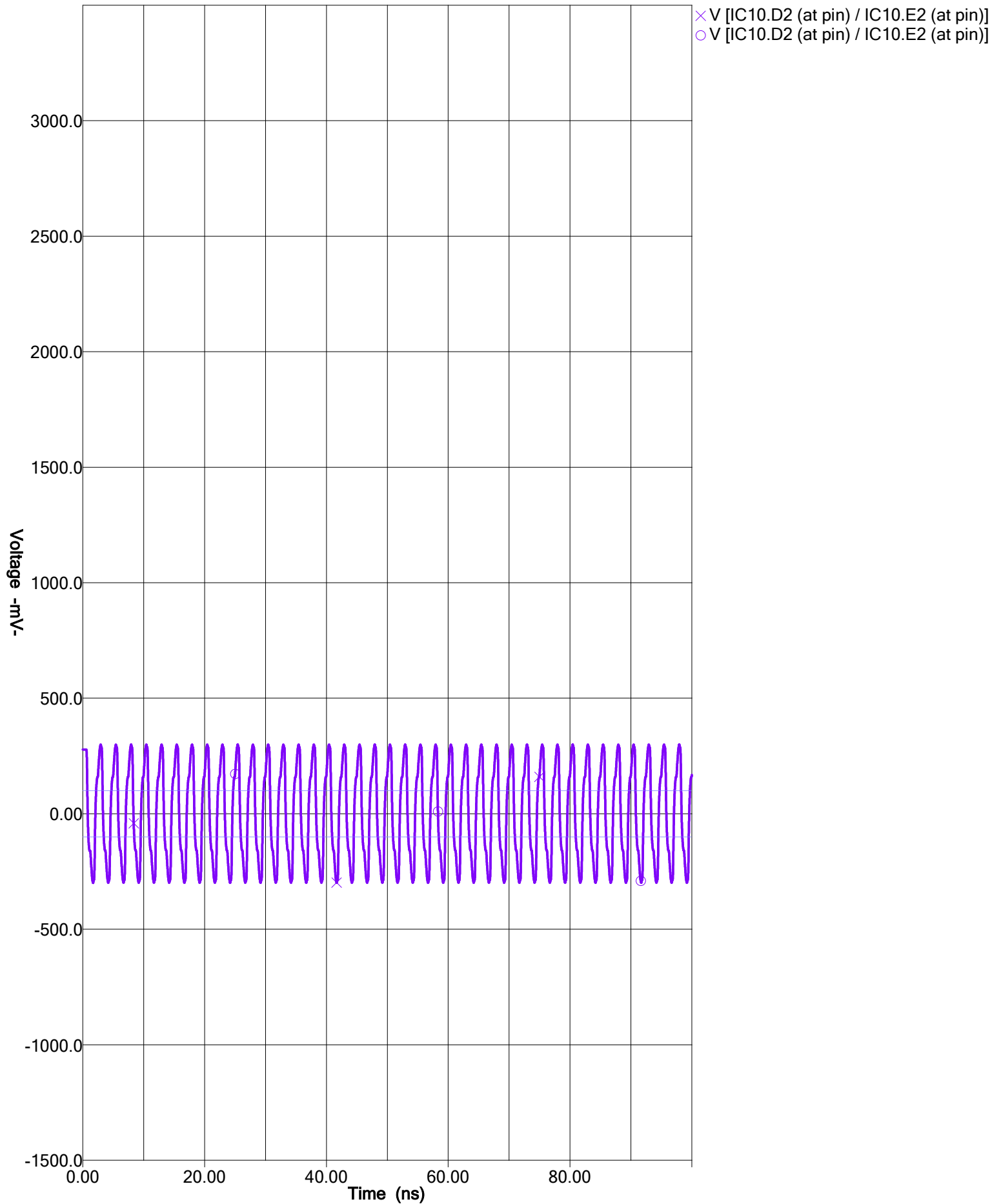


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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_24.HYP

Designer: Piotr Zdunek

HyperLynx v9.4.2



Date: Wednesday Jan. 17, 2018 Time: 0:15:55

Net name: FPGA_BANK35_DIFF17_N

Show Latest Waveform = YES, Show Previous Waveform = YES