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Design file: SPEKTROP2_PROCESSOR_BOARD_V1_36.HYP

Designer: Piotr Zdunek HyperLynx v9.4.2

imes V [IC10.D2 (at pin) / IC10.E2 (at pin)] OV [IC10.D2 (at pin) / IC10.E2 (at pin)] 400.0 300.0 200.0 100.0 Voltage -mV--0.00 -100.0 -200.0 -300.0 -400.0 -500.0 600.0 **Time (ps)** 800.0 1000.0 1200.0 1400.0 -400.0 -200.0 0.00 200.0 400.0

Date: Friday Jan. 19, 2018 Time: 22:21:04 Net name: FPGA_BANK35_DIFF17_N Cursor 1, Voltage = $-\overline{199.0}$ mV, Time = $9\overline{42.0}$ ps