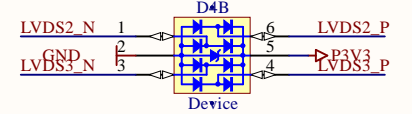
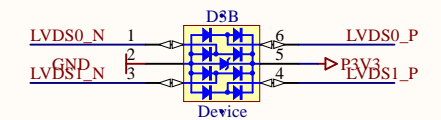
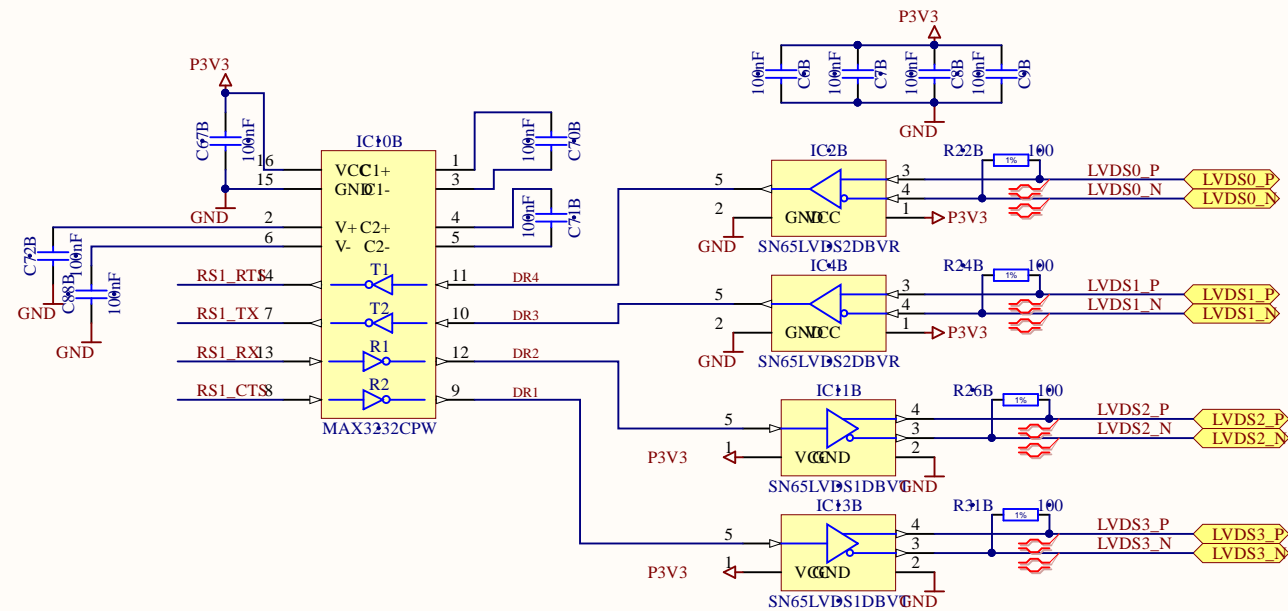
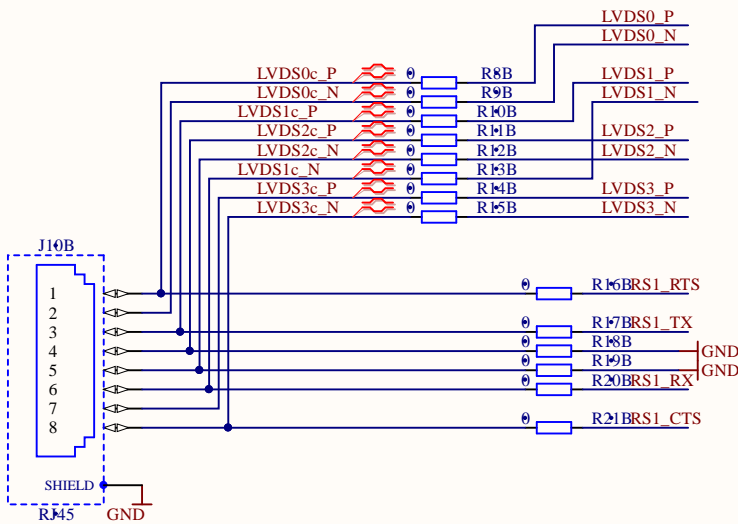
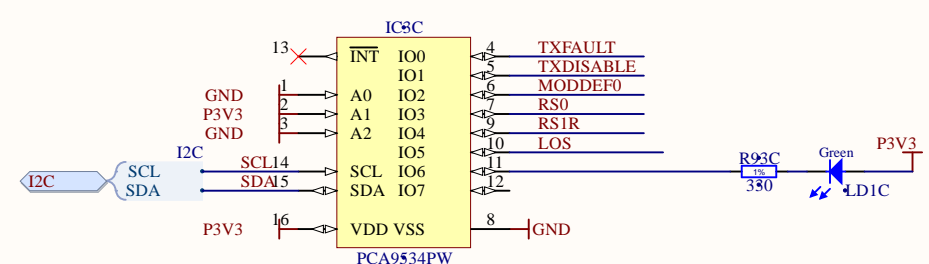
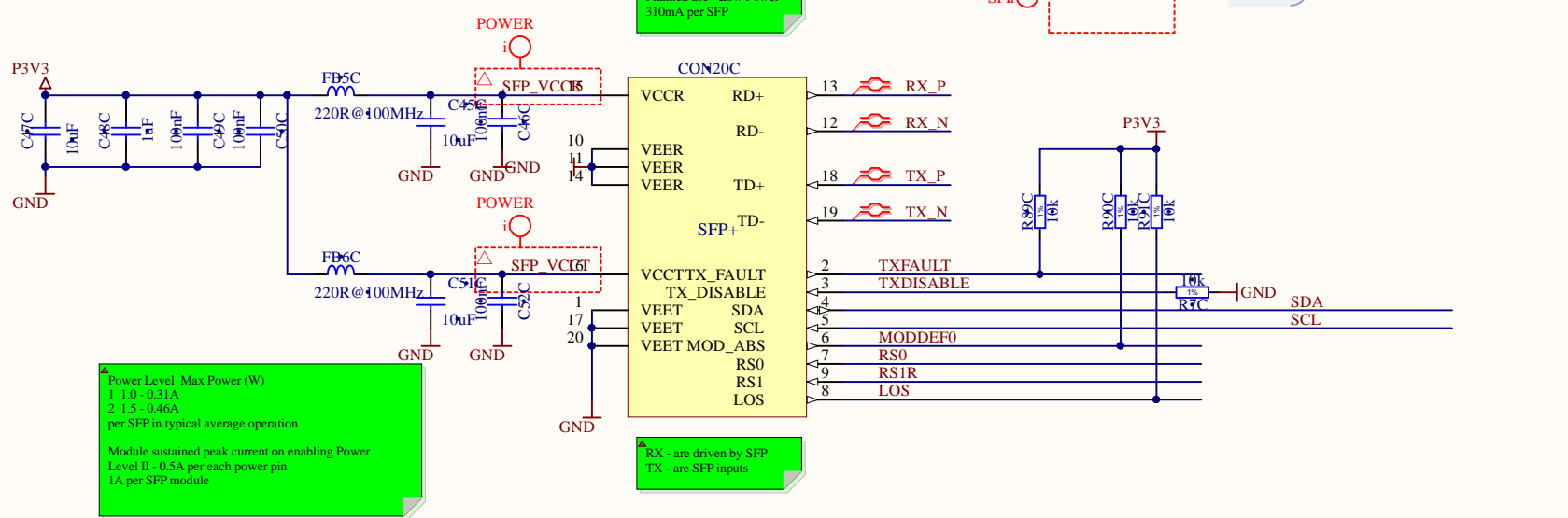


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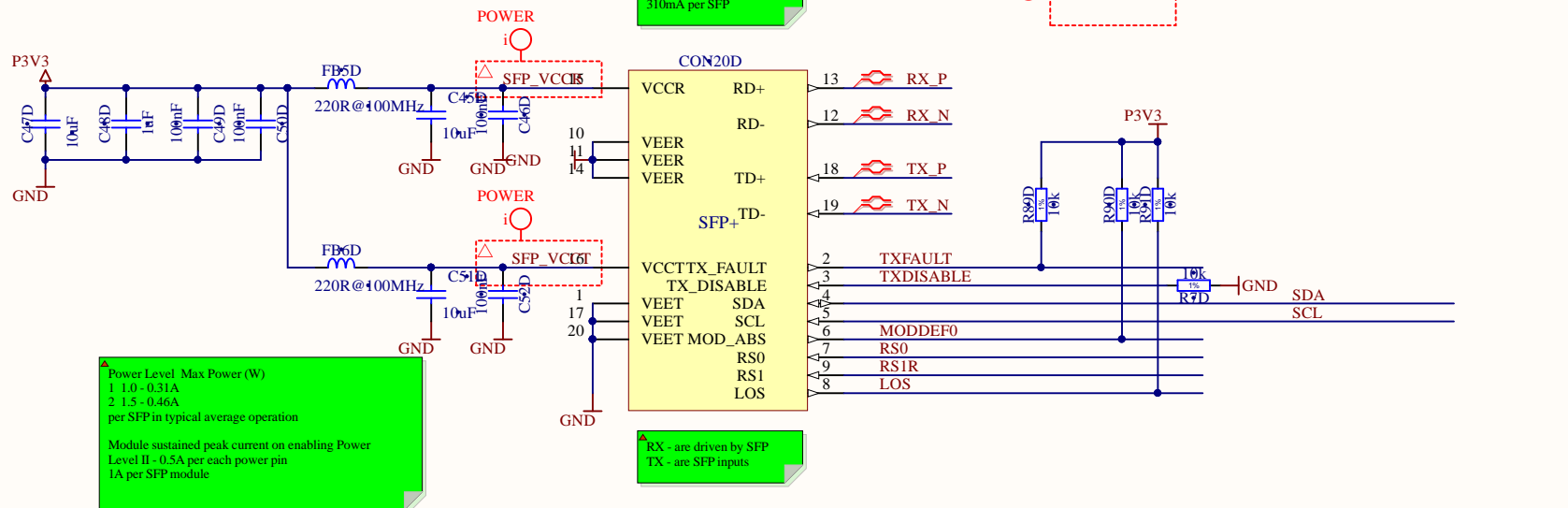


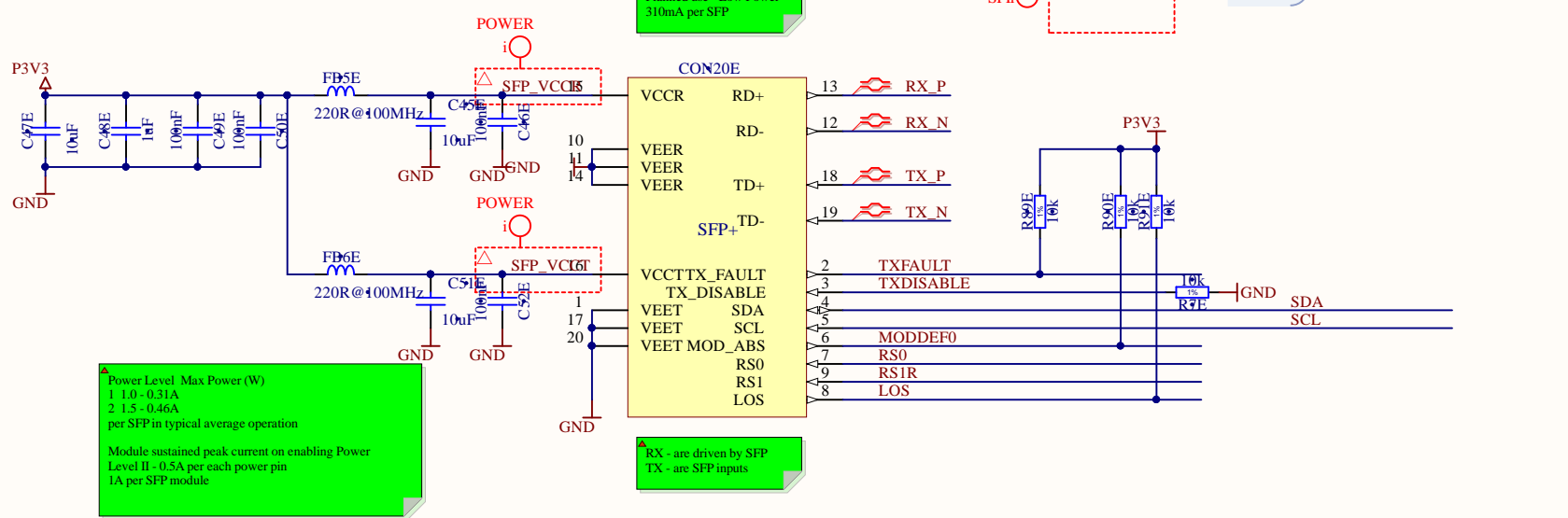


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LLH	33 (decimal), 21 (hexadecimal)		
LHL	34 (decimal), 22 (hexadecimal)		
LHH	35 (decimal), 23 (hexadecimal)		
HLL	36 (decimal), 24 (hexadecimal)		
HLH	37 (decimal), 25 (hexadecimal)		
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HHH	39 (decimal), 27 (hexadecimal)		





IC3E

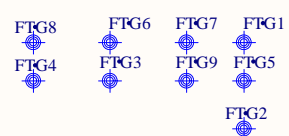
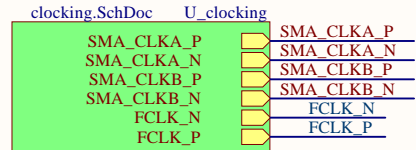
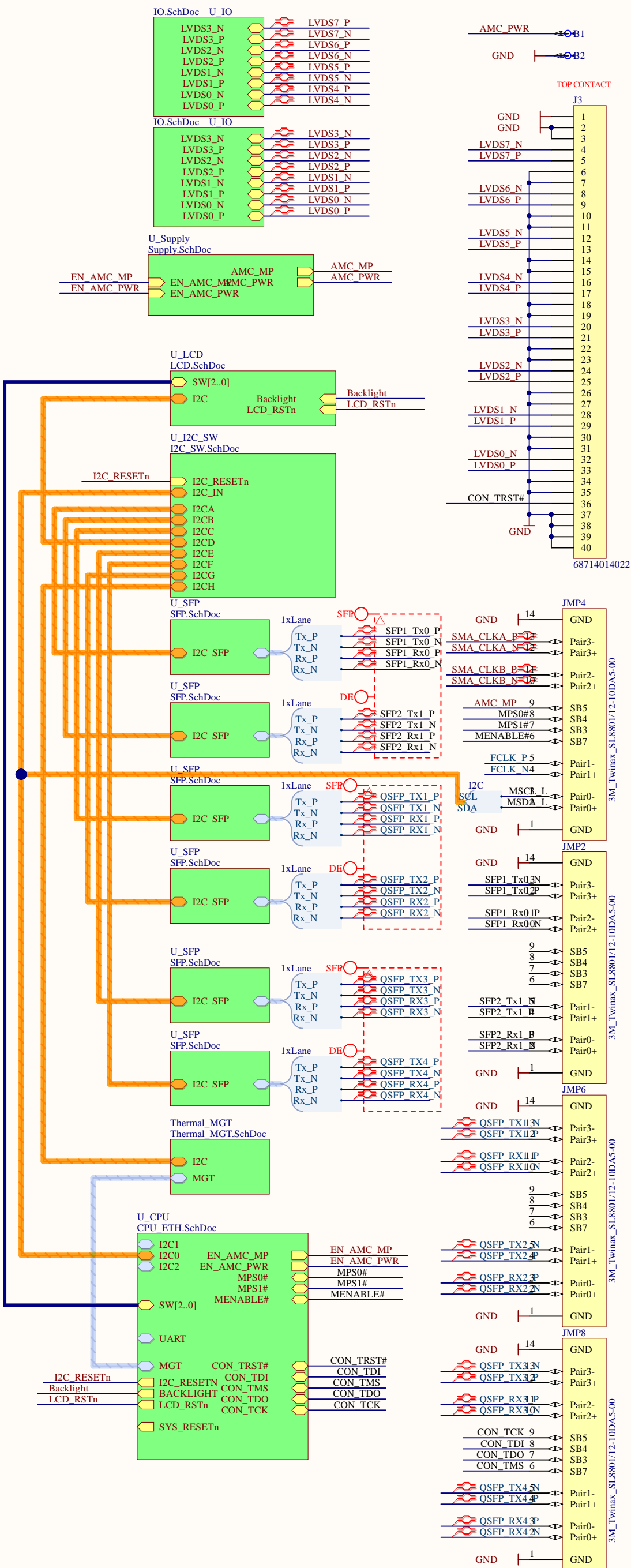
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1	A0	IO1	5	TXDISABLE
2	A1	IO2	6	MODDEF0
3	A2	IO3	7	RS0
10	IO4	IO4	8	RS1R
11	IO5	IO5	9	LOS
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13	IO7	IO7	11	
14	VDD	VSS	12	
15	VSS		13	

PCA9534PW

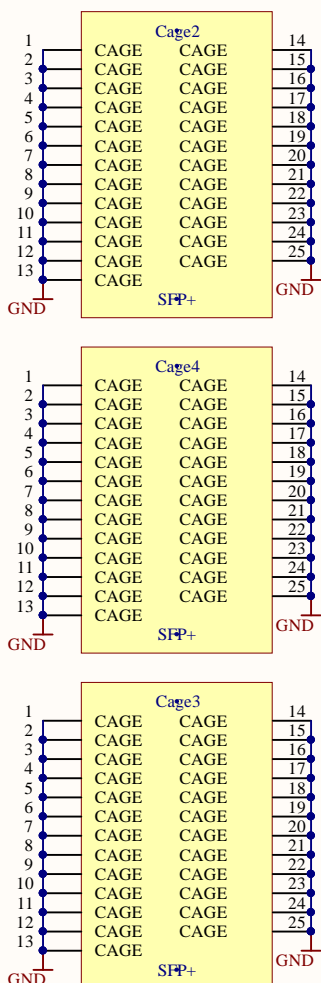
A2 A1 A0	A2 A1 A0
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LHL	34 (decimal), 22 (hexadecimal)
LHH	35 (decimal), 23 (hexadecimal)
HLL	36 (decimal), 24 (hexadecimal)
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HHH	39 (decimal), 27 (hexadecimal)



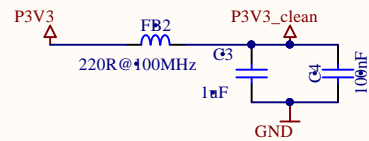
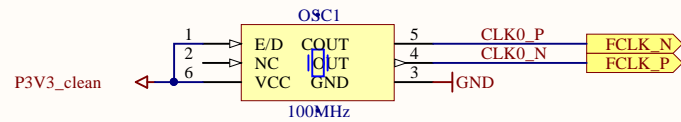
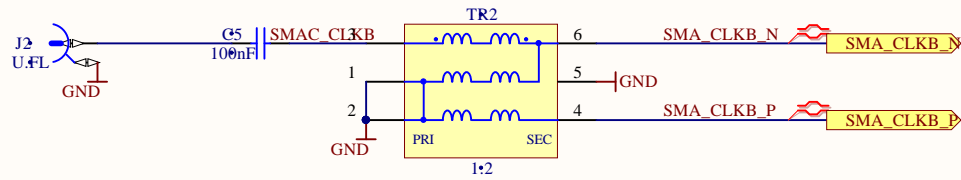
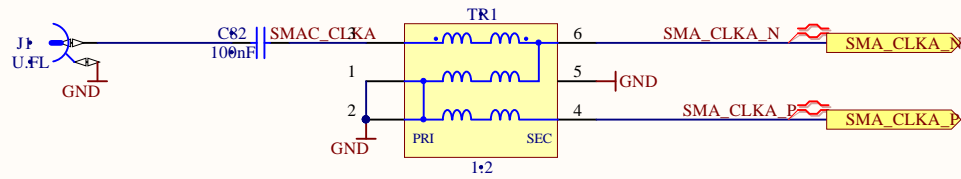




PANEL-SIDE MOUNTING HOLES

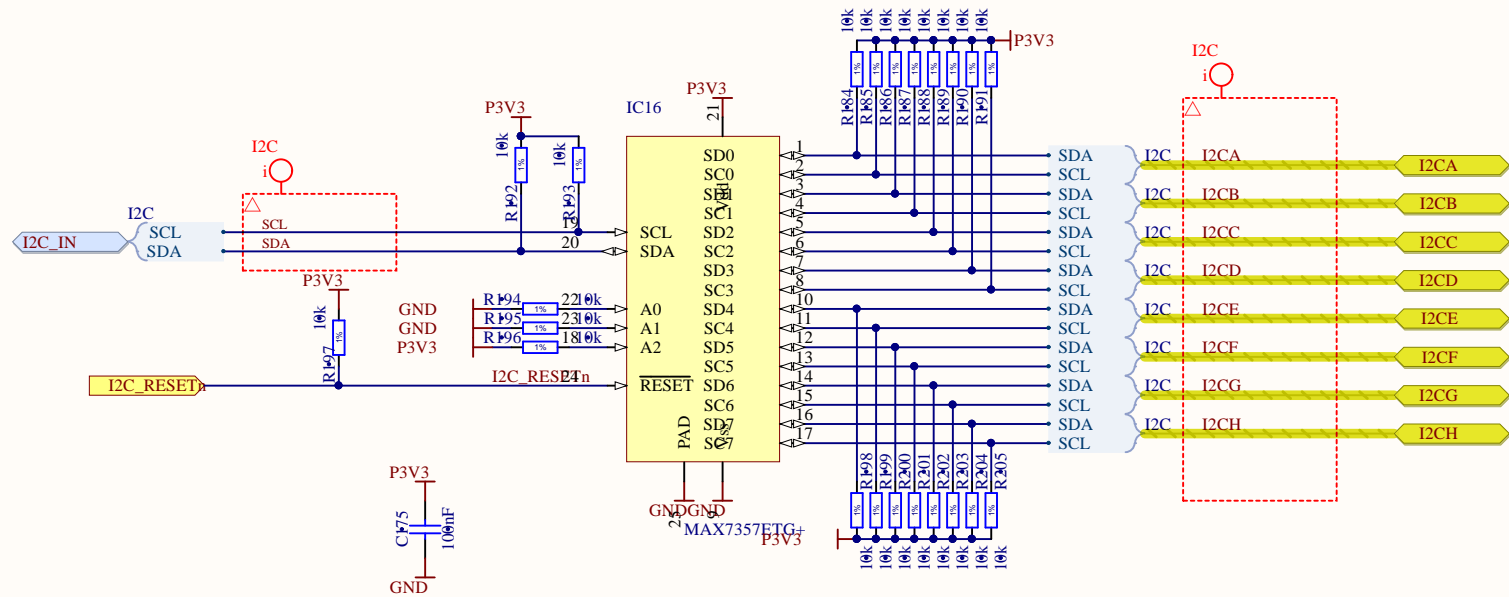


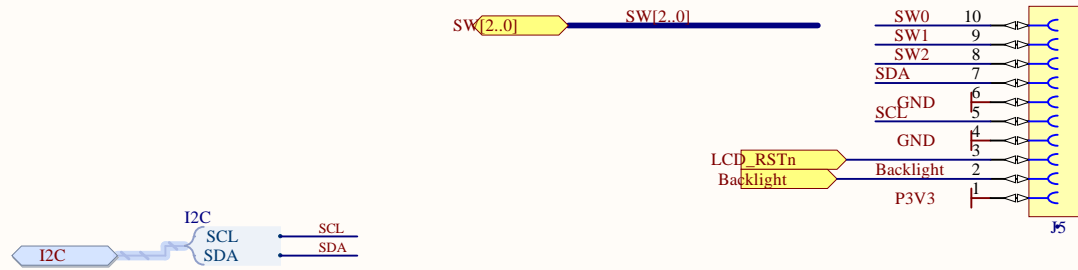
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Date:	26.10.2017	Sheet of
File:	D:\Dropbox\...\clocking.SchDoc	Drawn By:



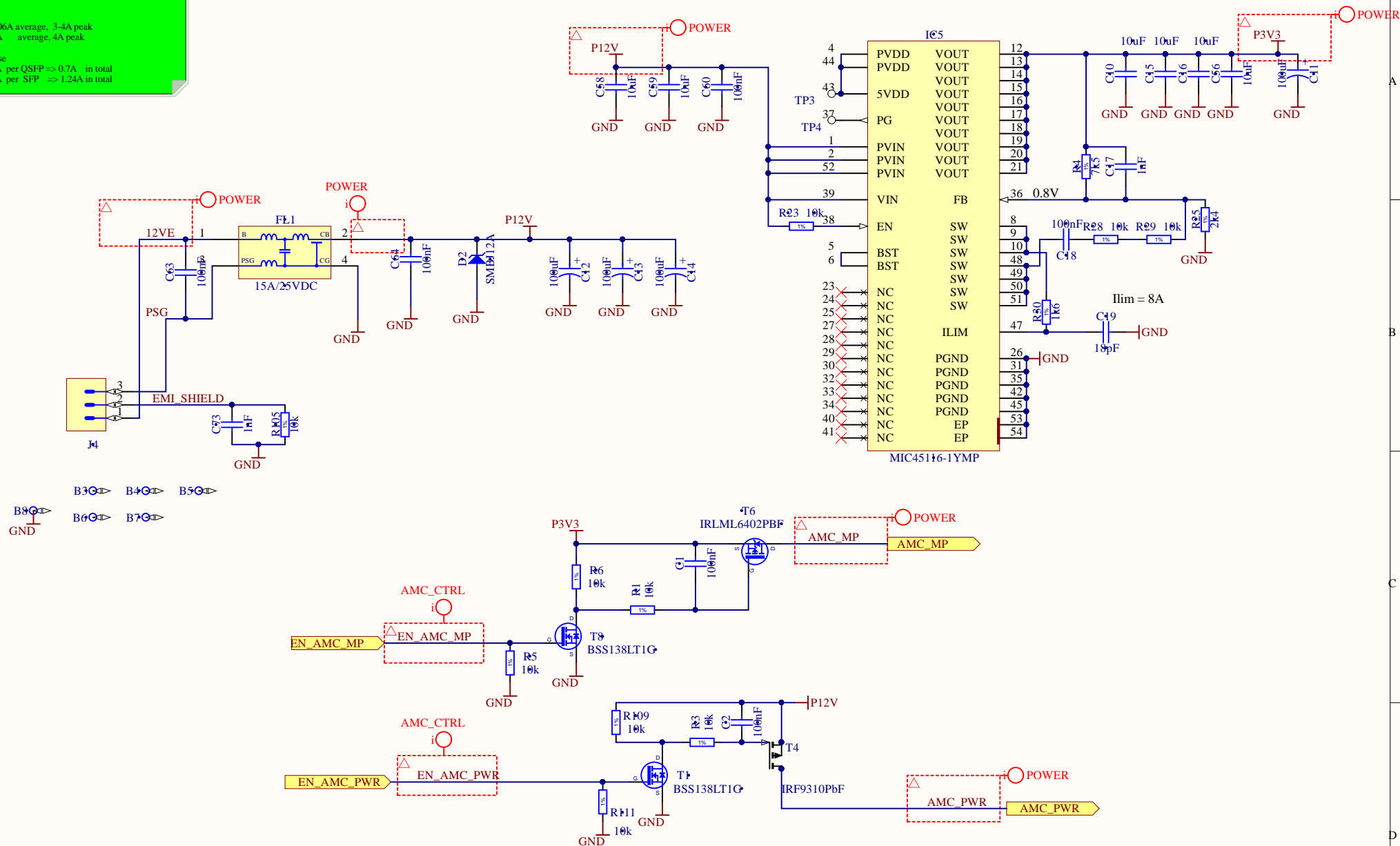




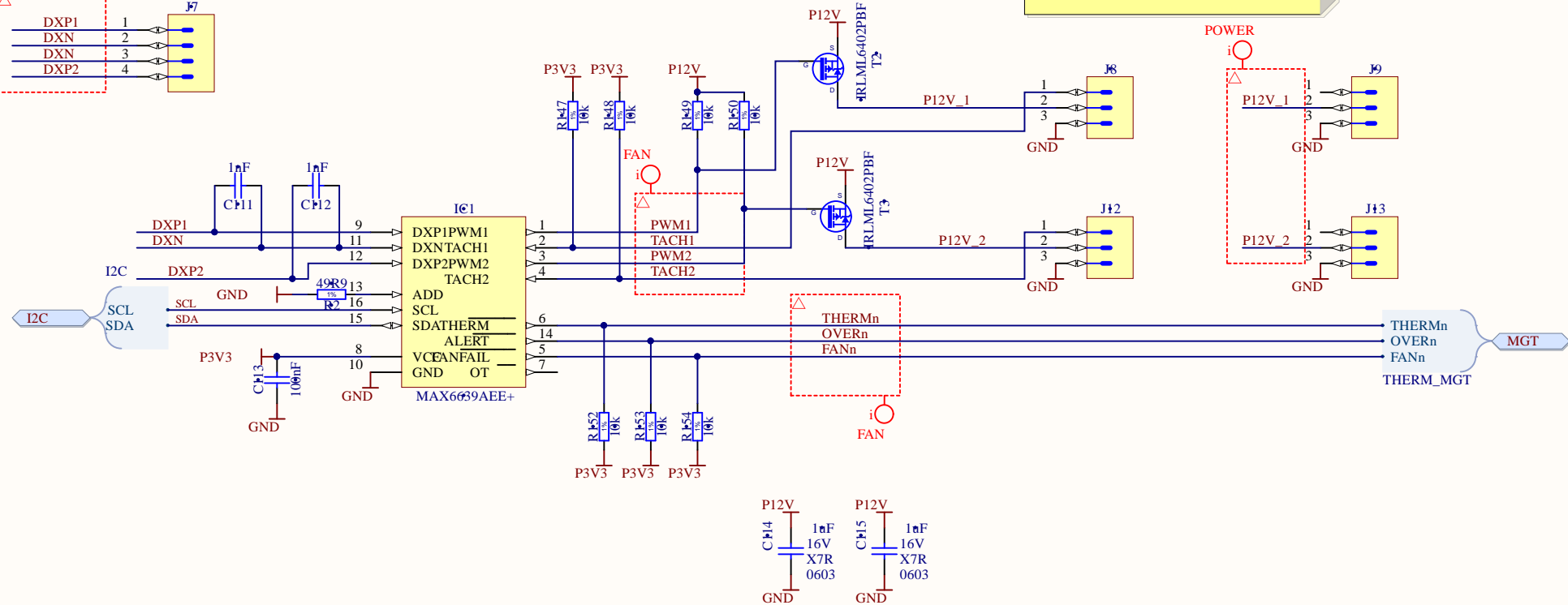
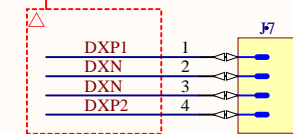
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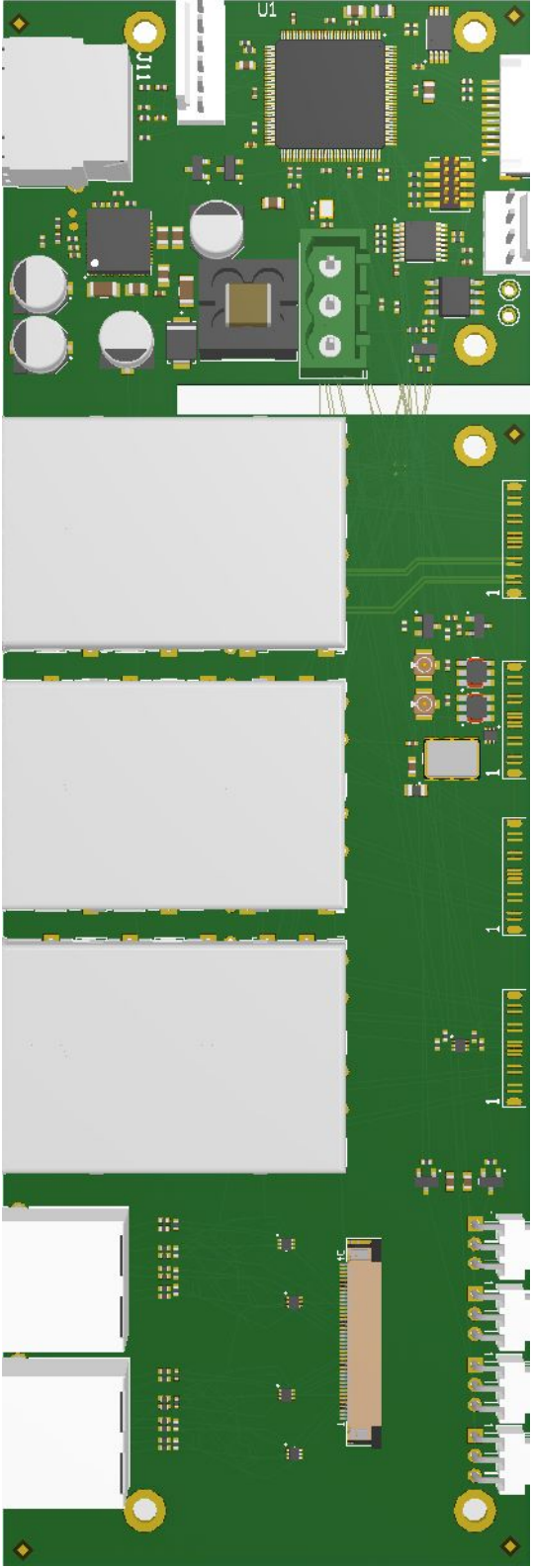
Worst case  
2x QSFP => 2.06A average, 3-4A peak  
4x SFP = 2A average, 4A peak

Planned use case  
average 350mA per QSFP => 0.7A in total  
average 310mA per SFP => 1.24A in total

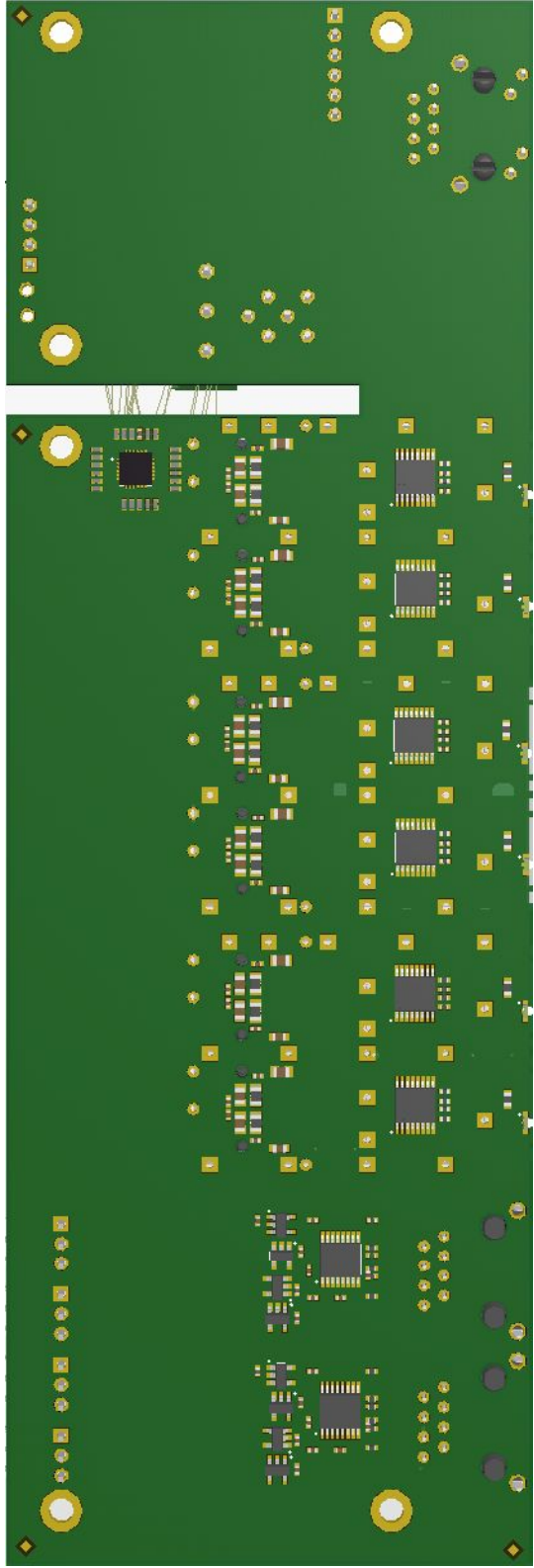


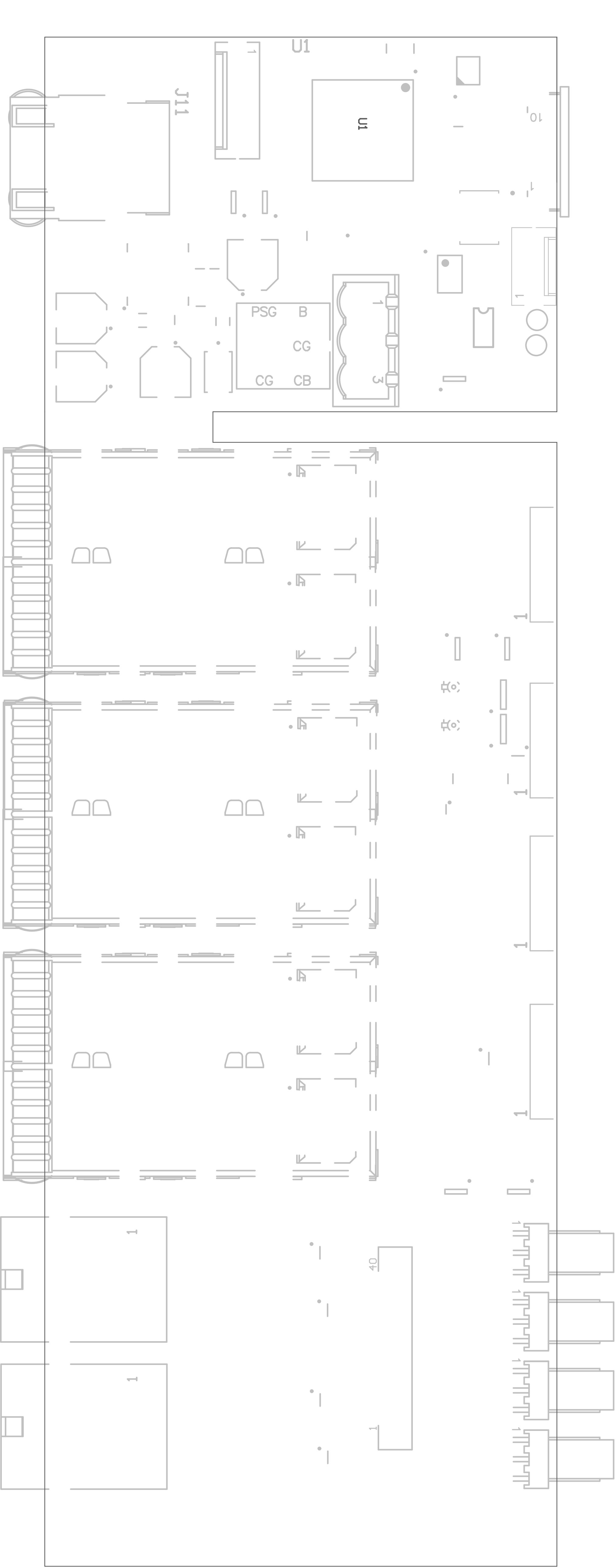
FAN

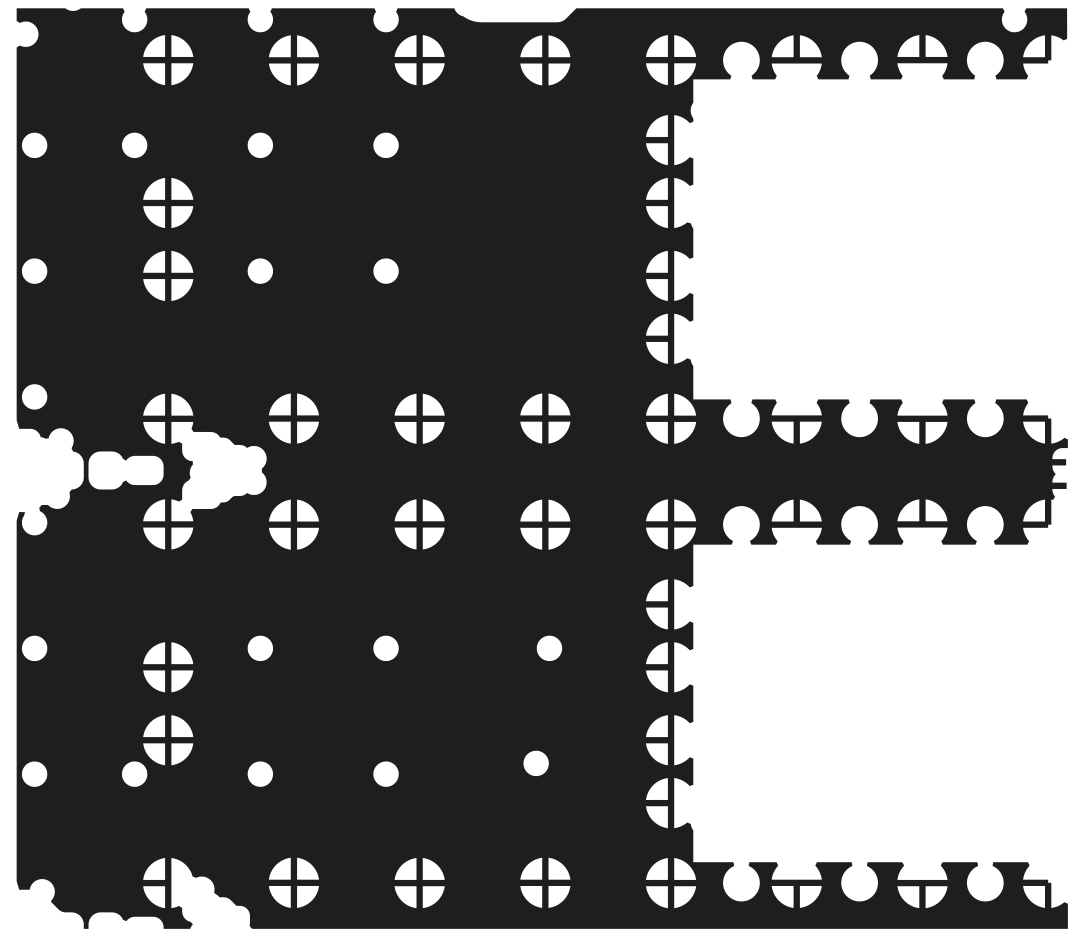
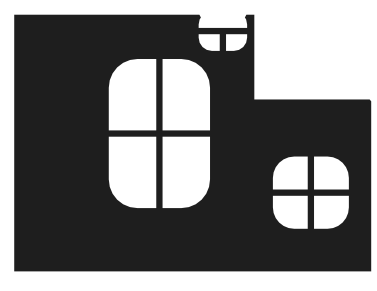
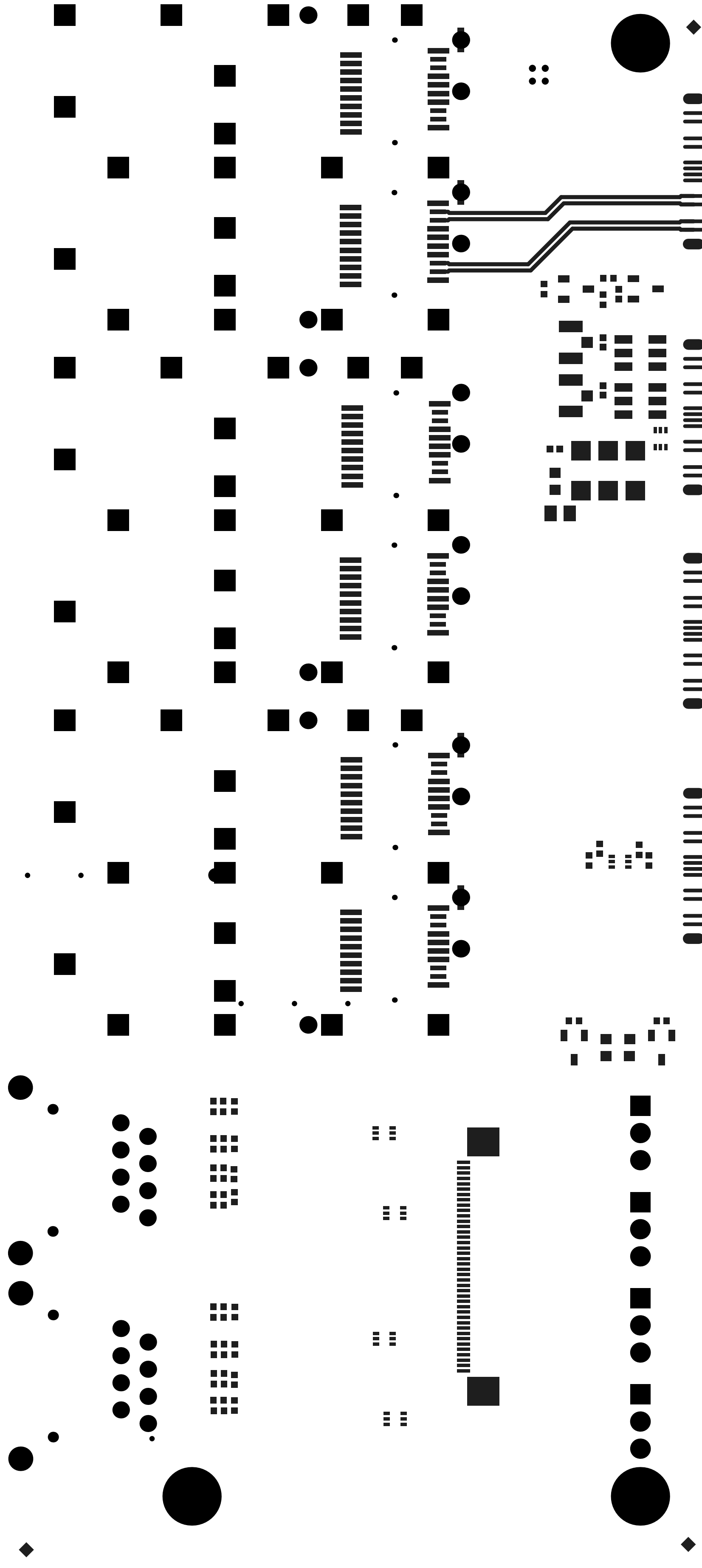
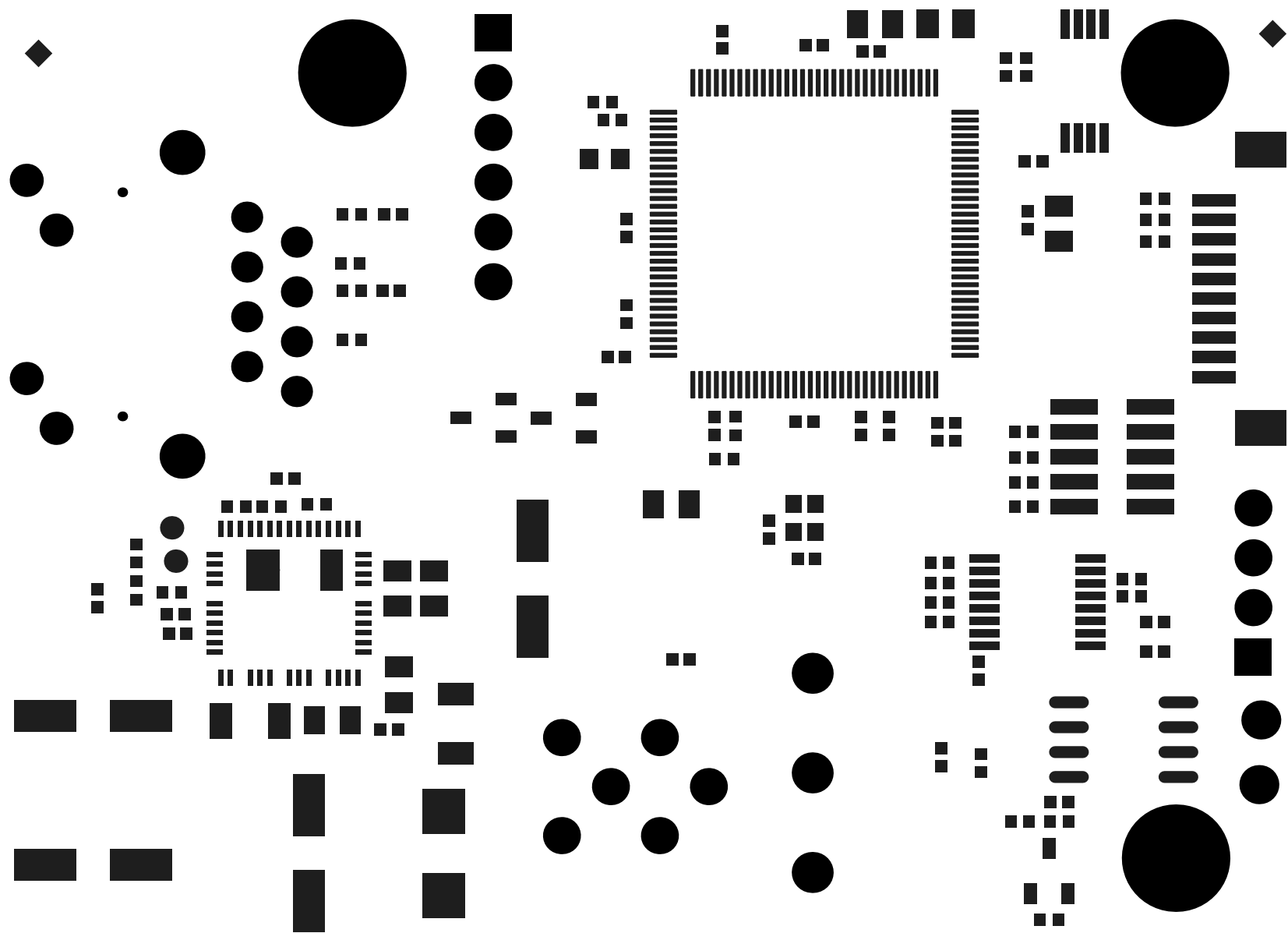


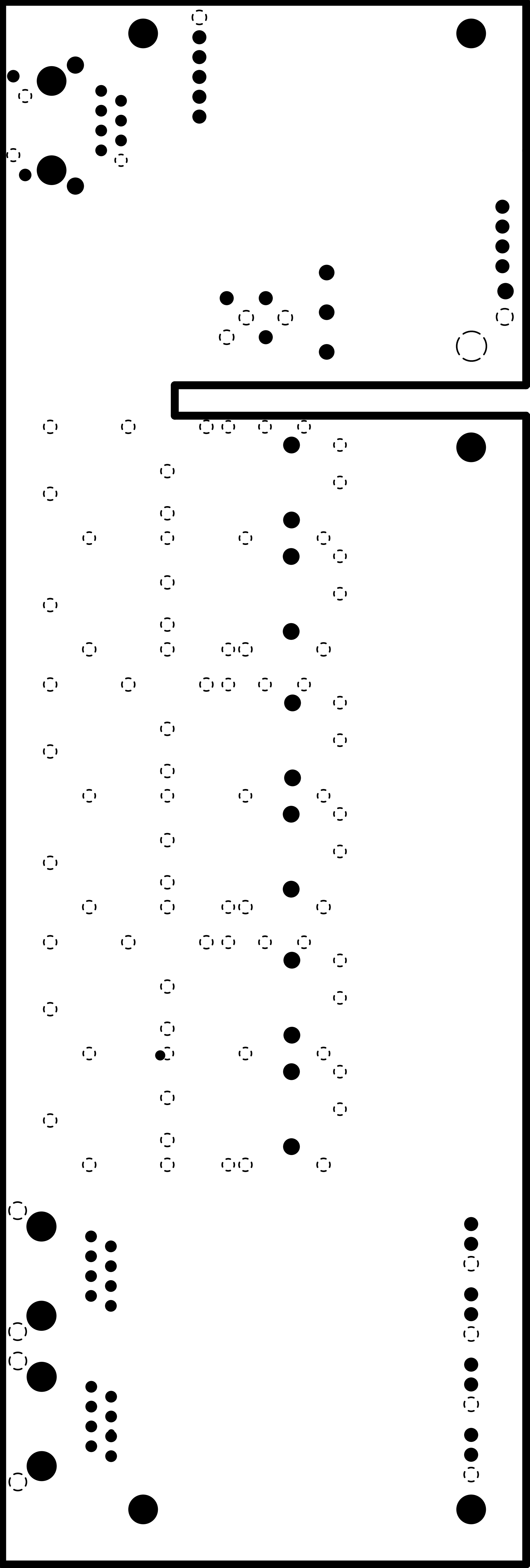






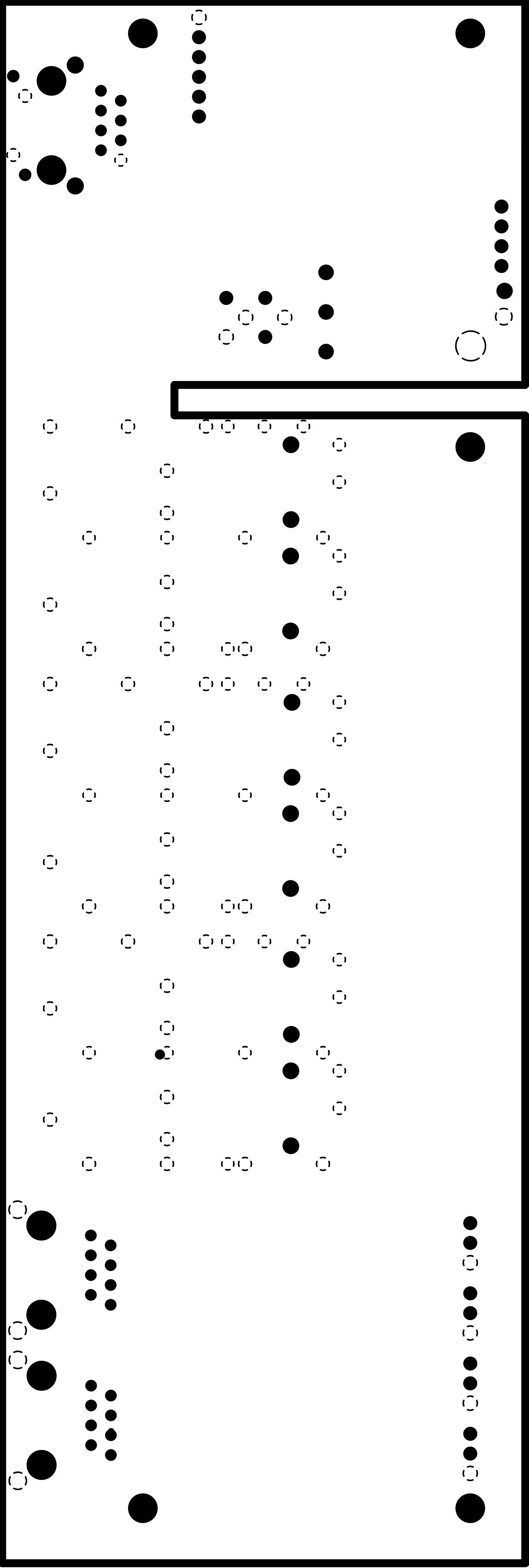


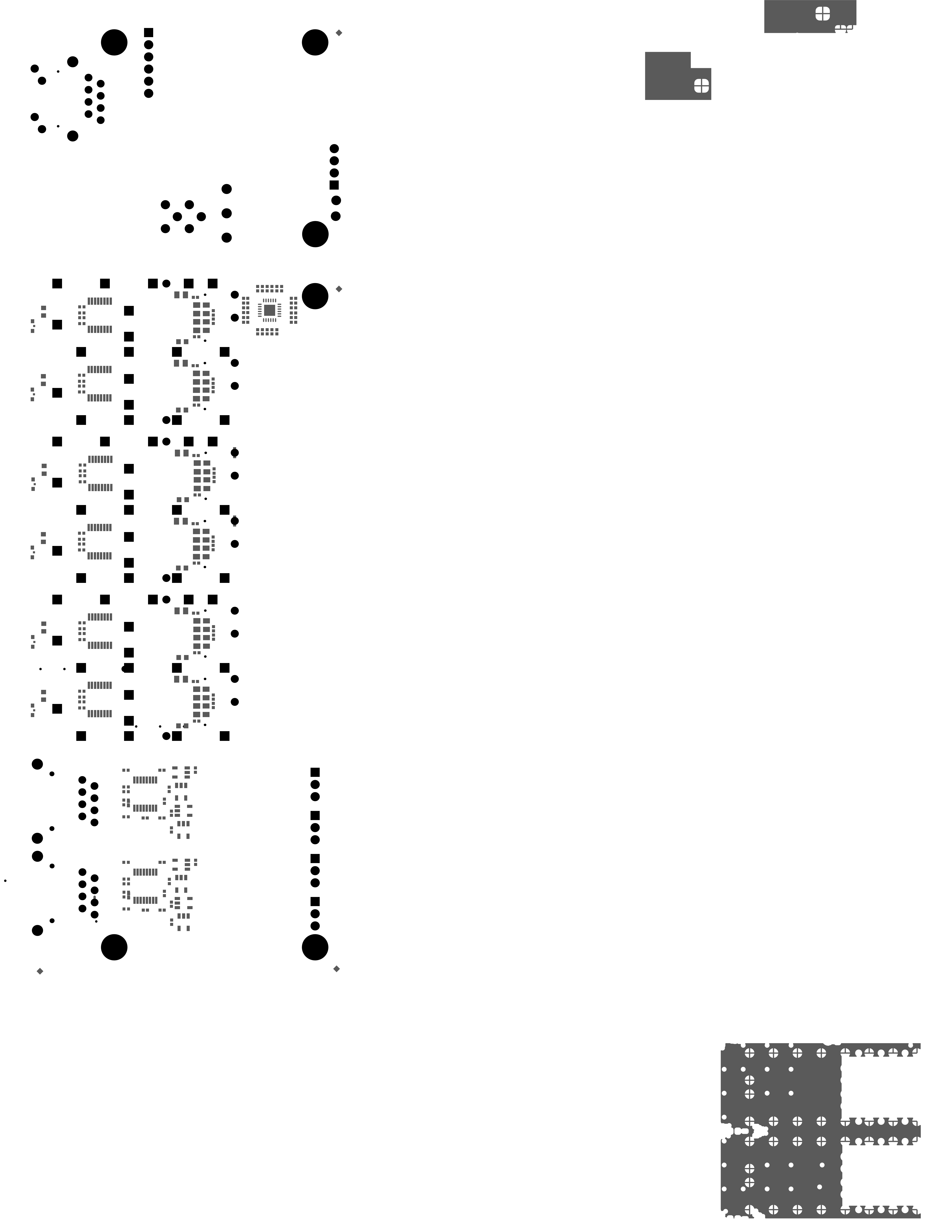














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