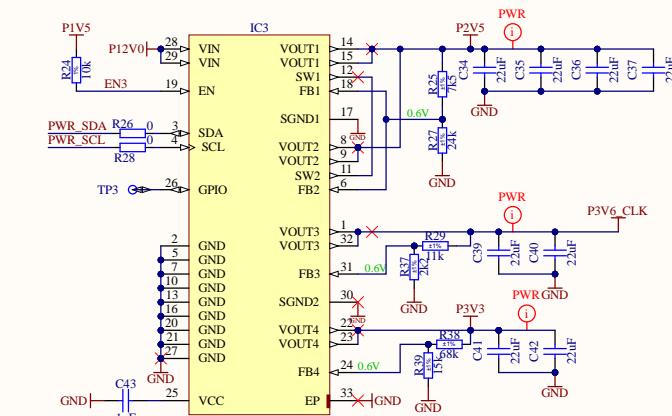
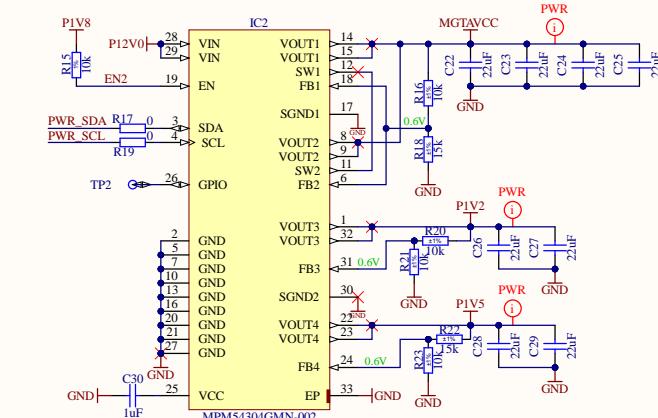
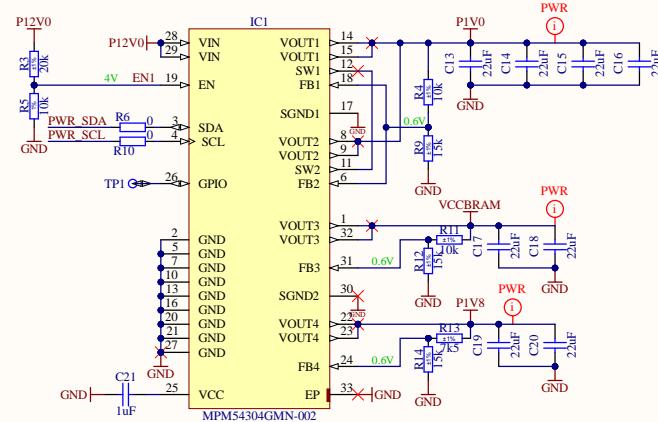
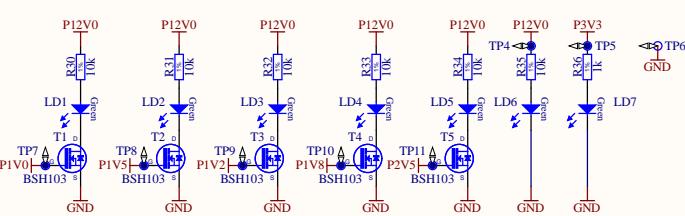
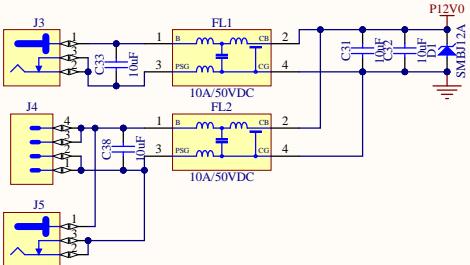
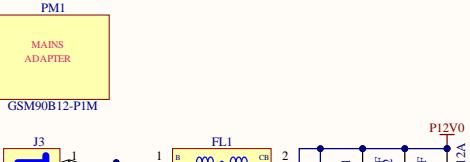
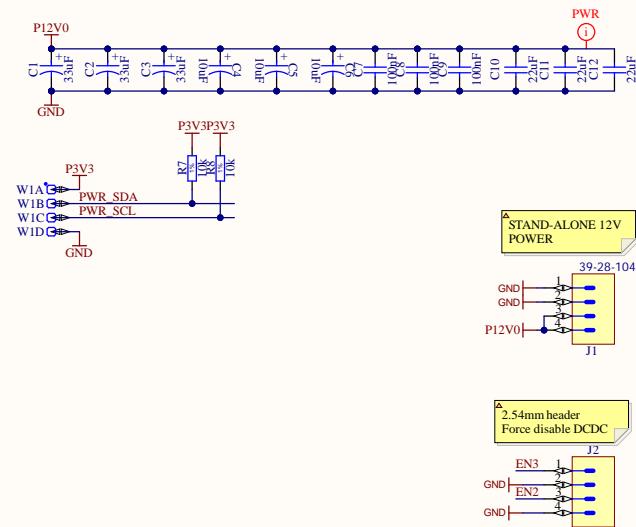


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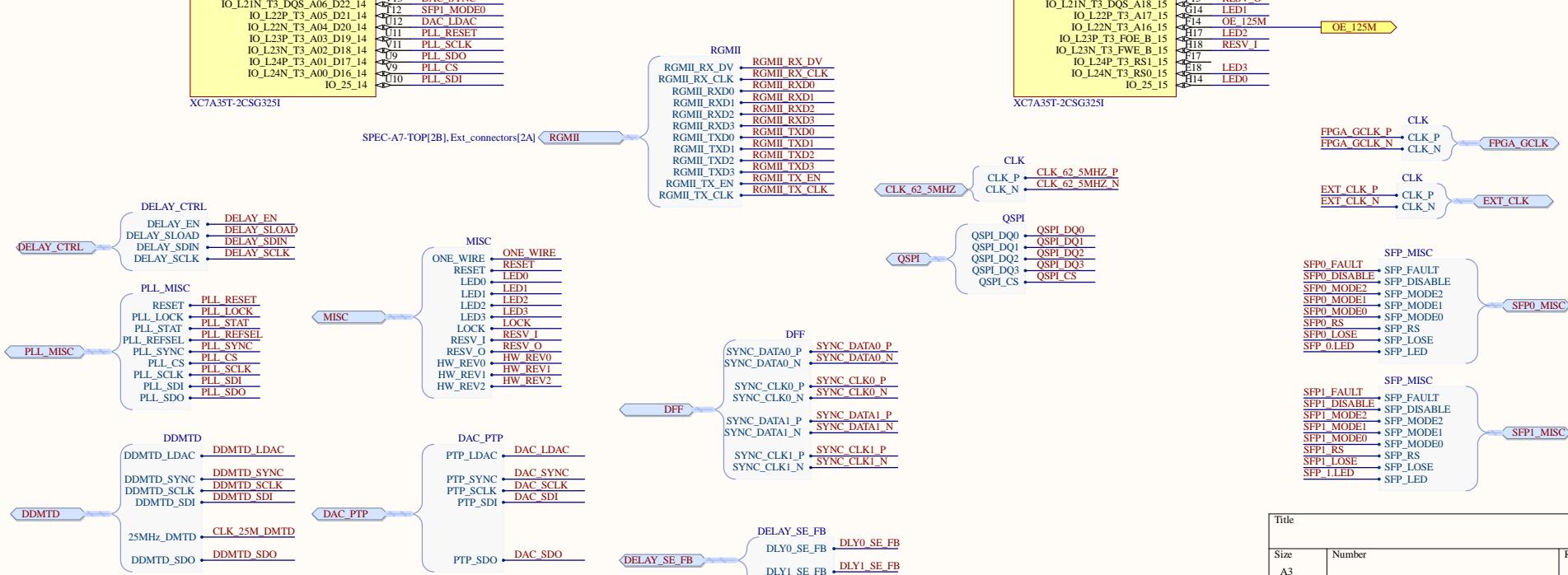
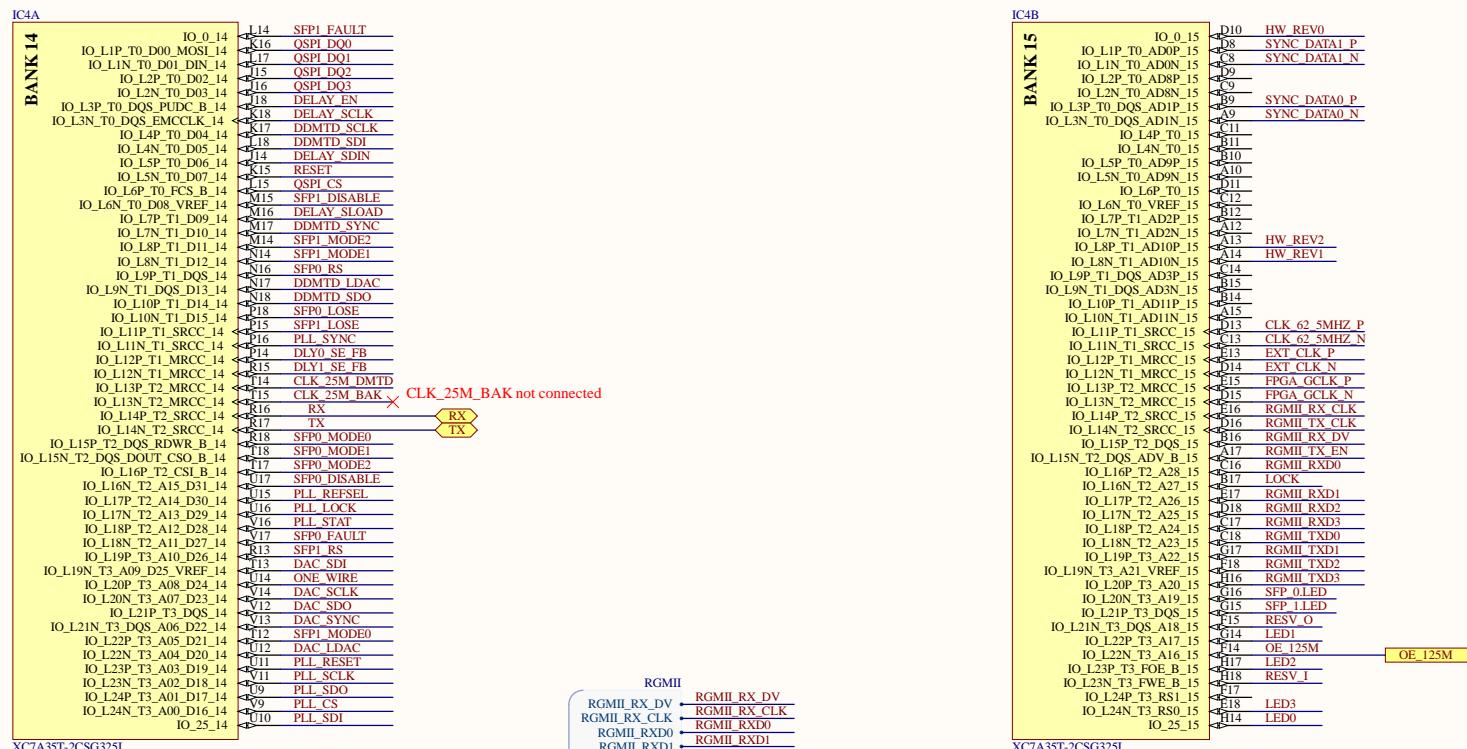
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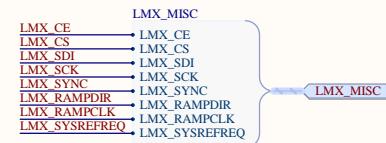
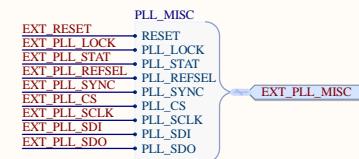
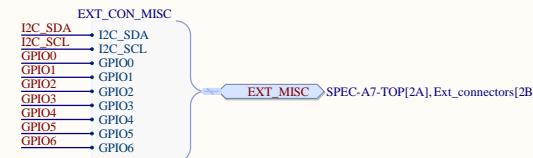
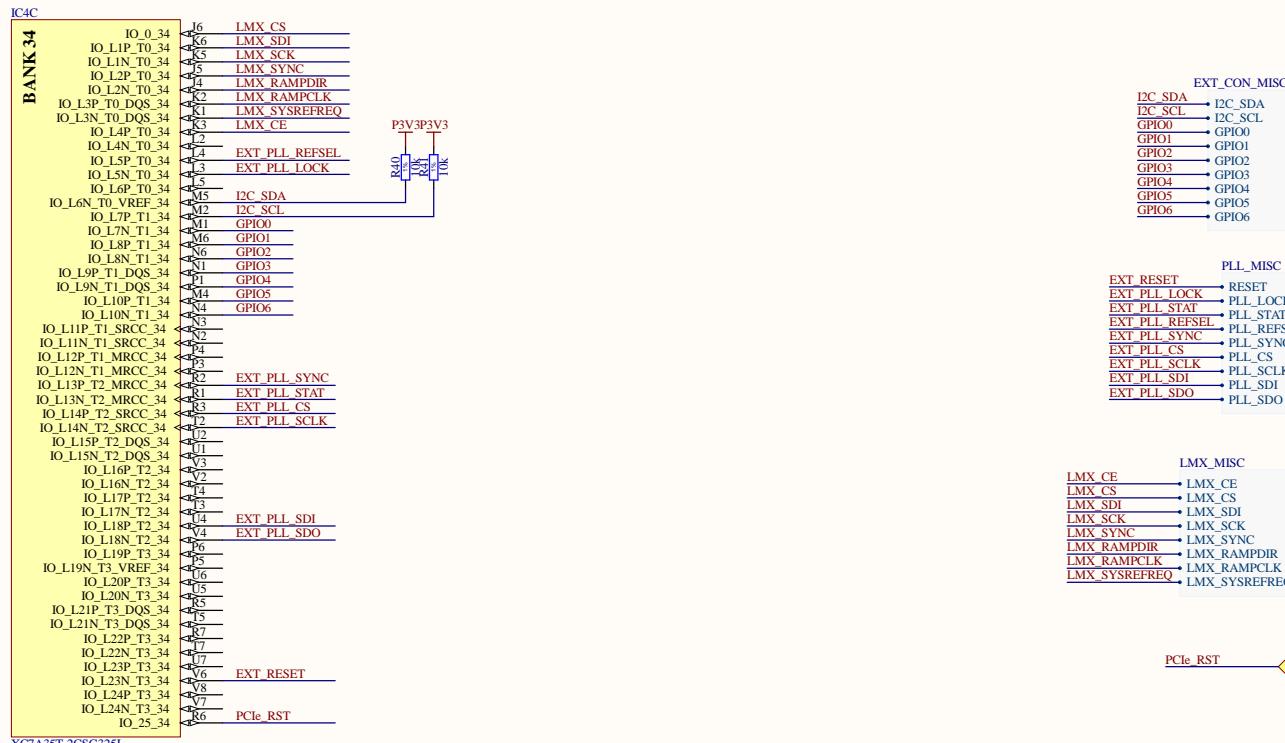
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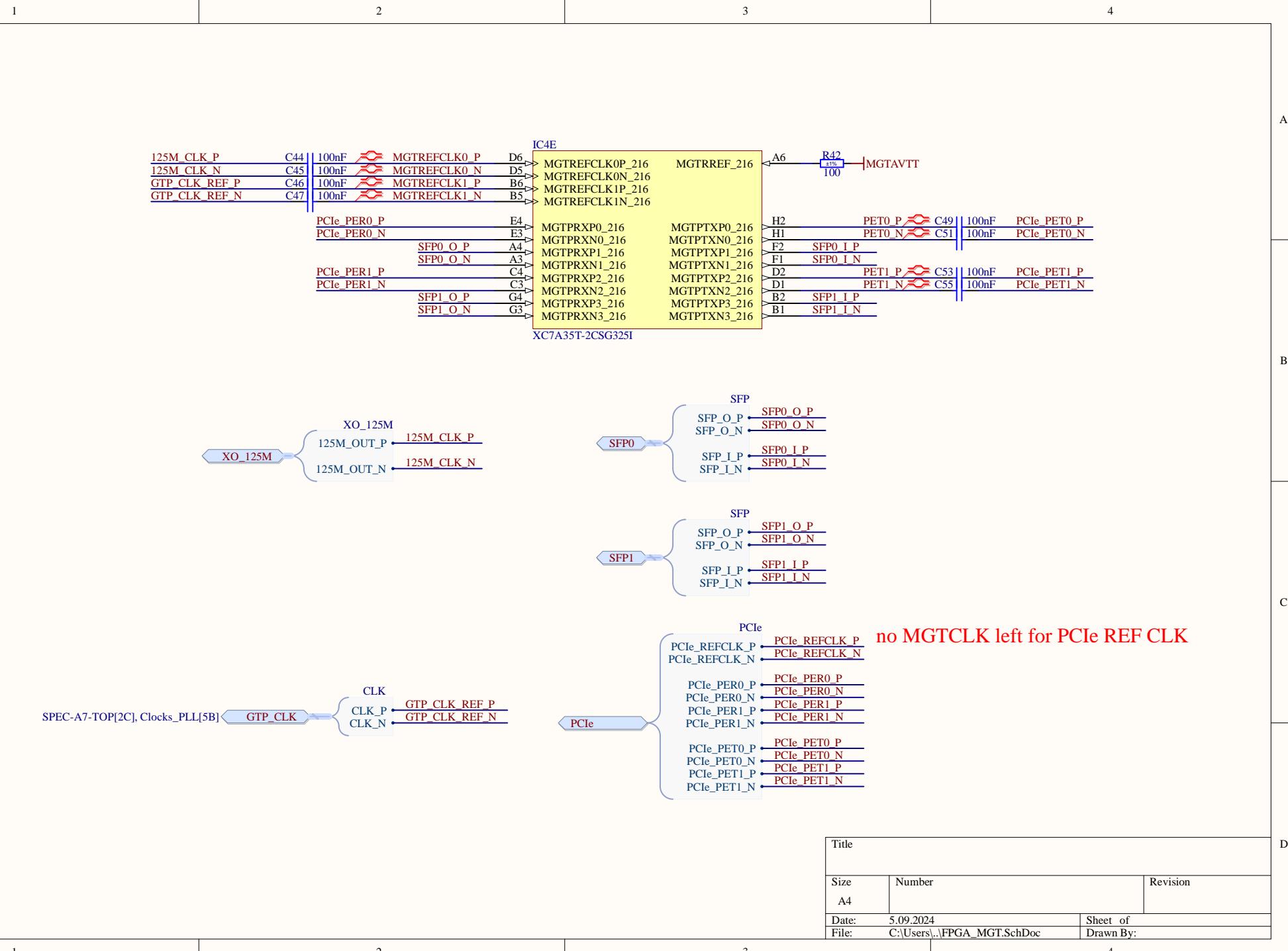
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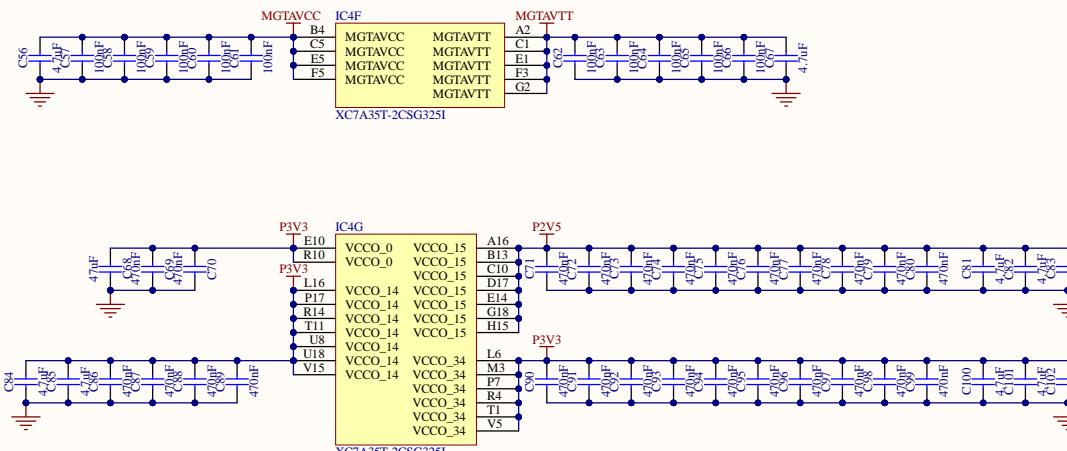
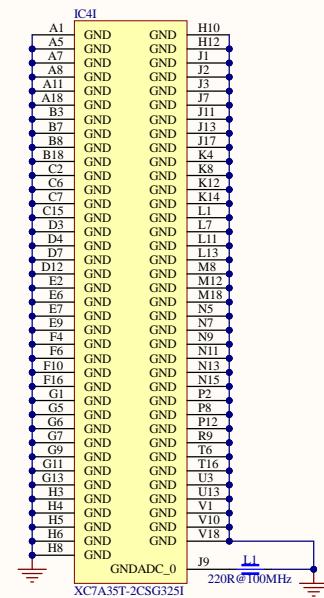
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PCIe\_RST → PCIe\_RST

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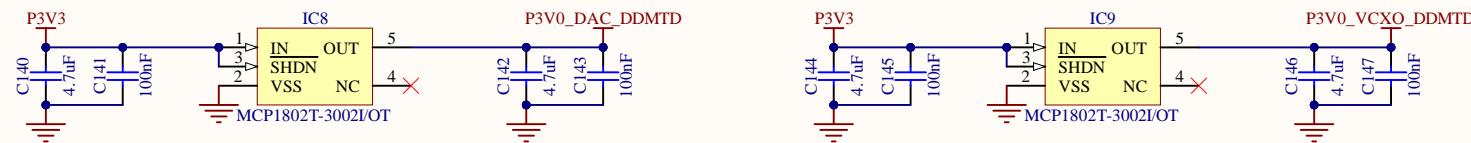
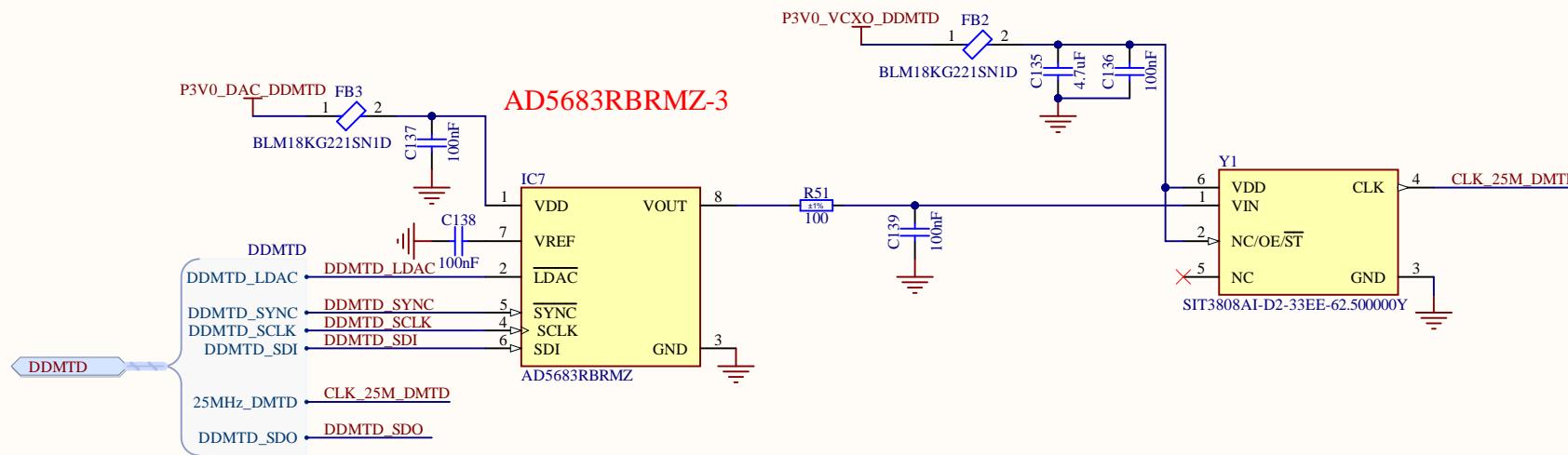
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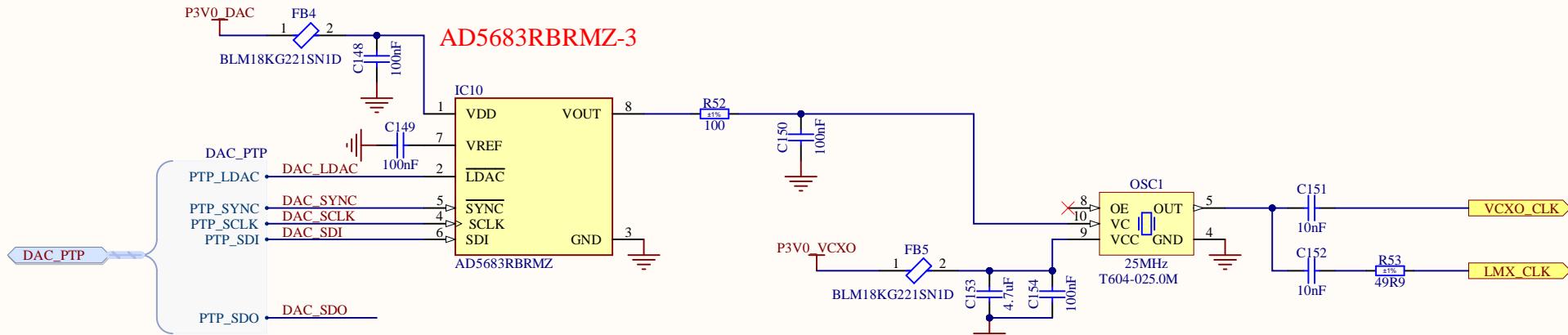


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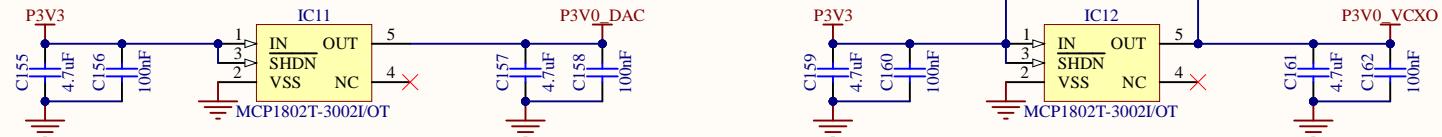
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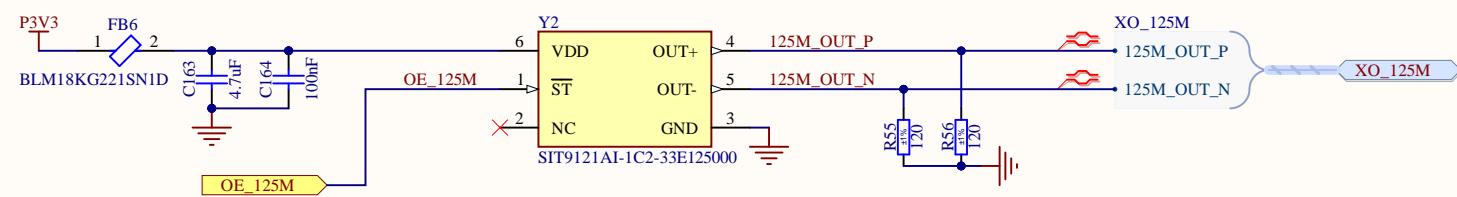
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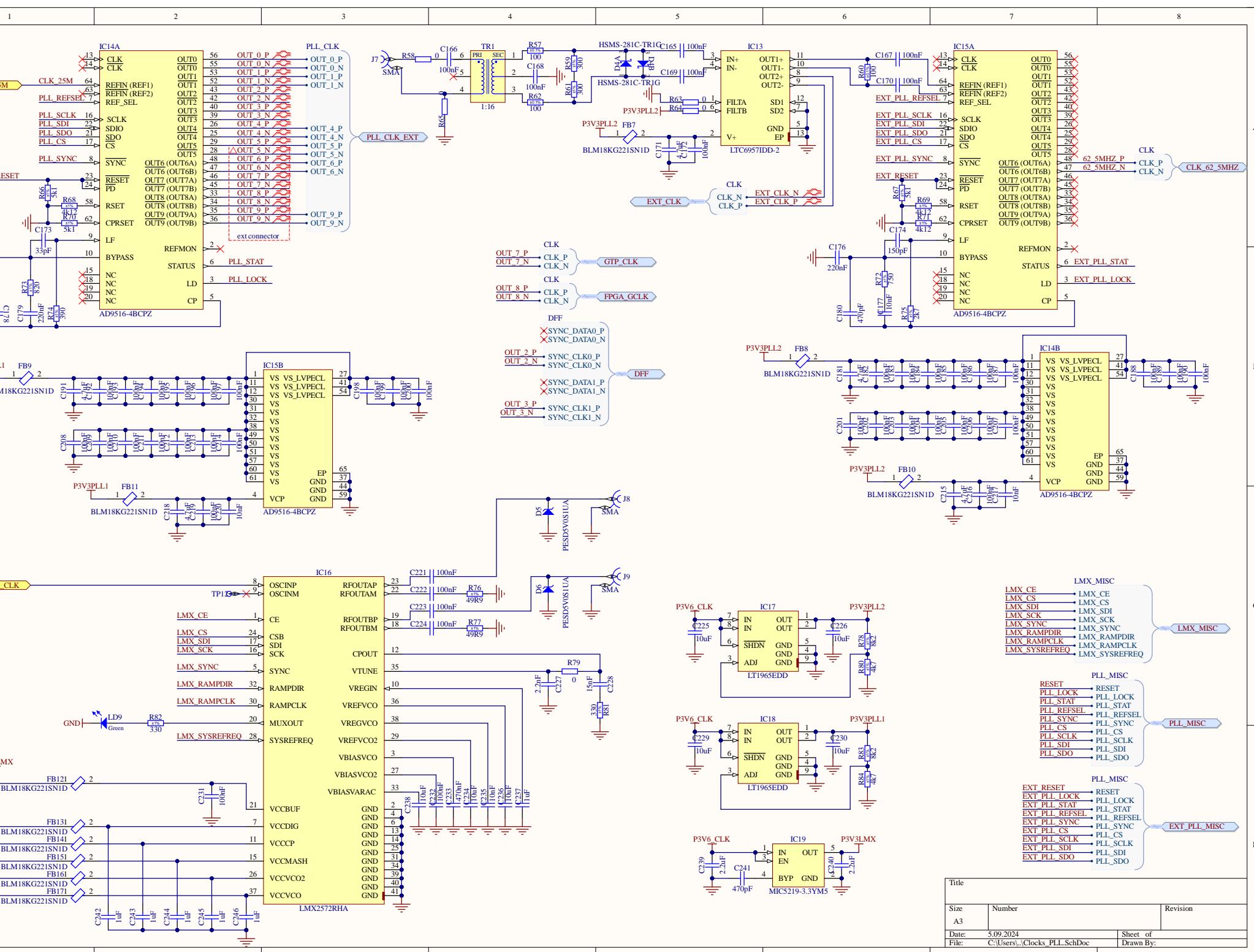
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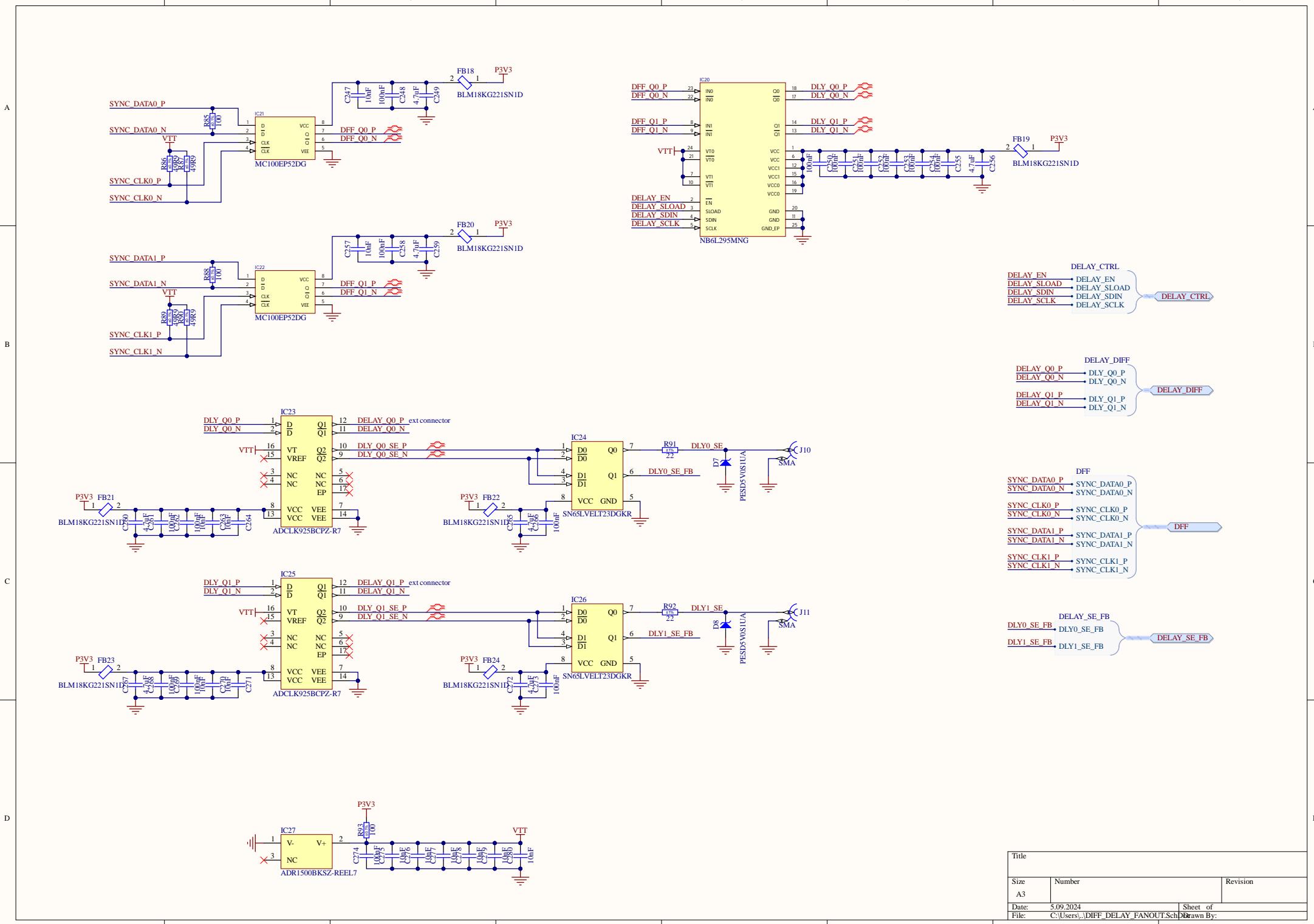
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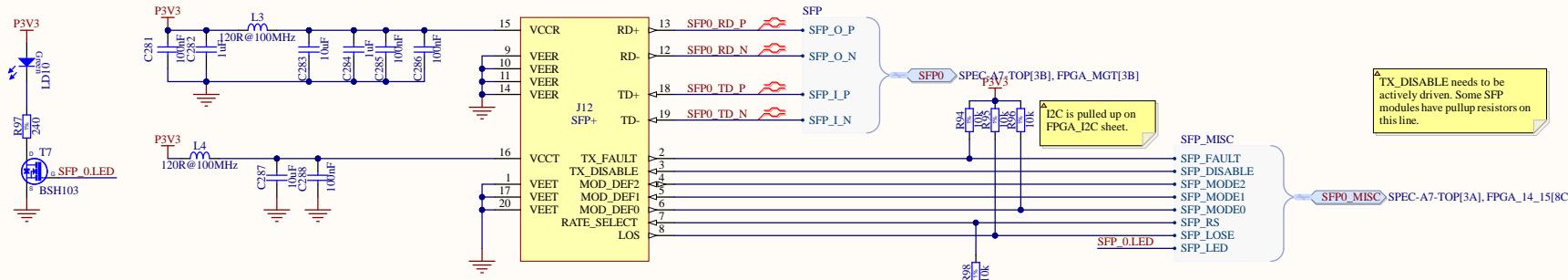


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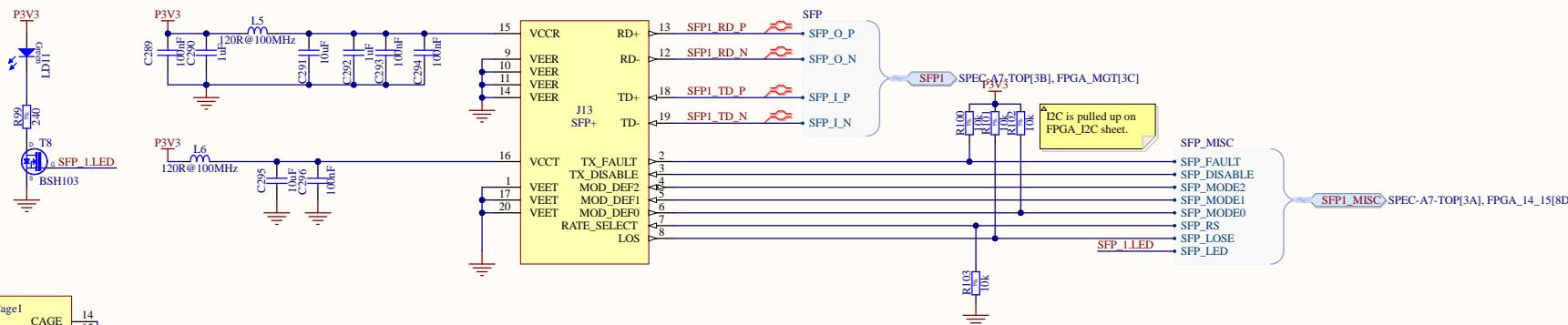
A



TX\_DISABLE needs to be actively driven. Some SFP modules have pullup resistors on this line.

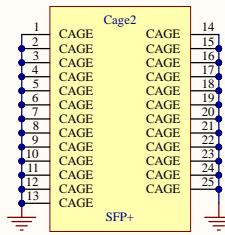
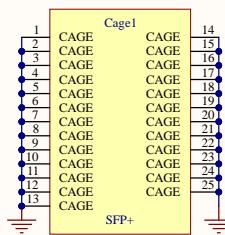
SFP0\_MISC > SPEC-A7-TOP[3A], FPGA\_14\_15[8C]

B



SFP1\_MISC > SPEC-A7-TOP[3A], FPGA\_14\_15[8D]

C



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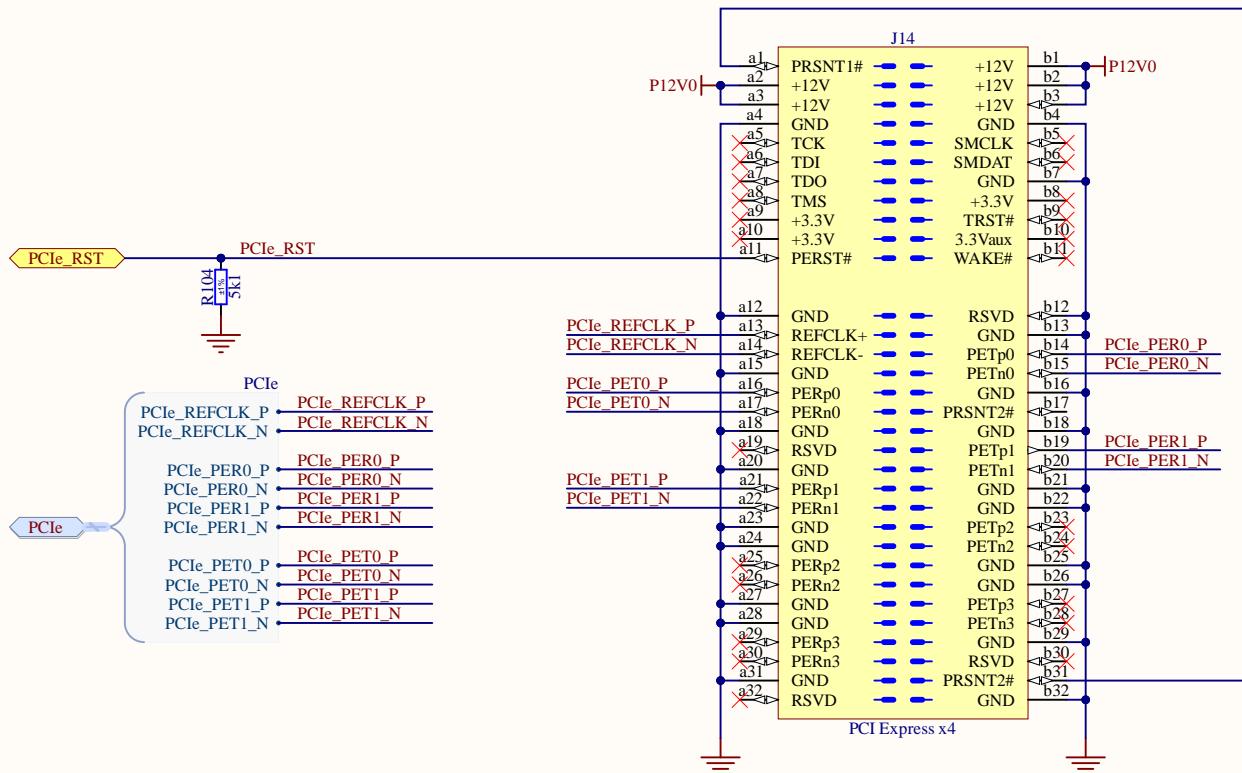
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Title

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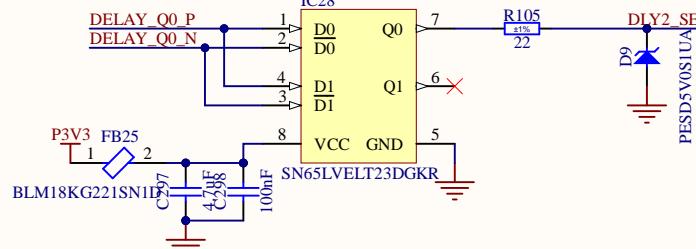
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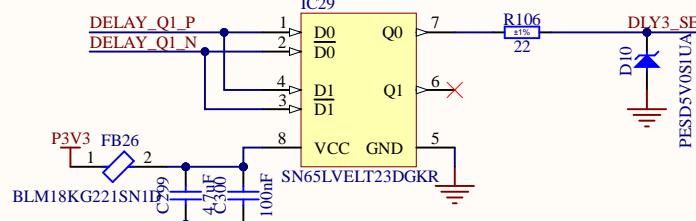
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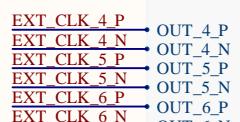
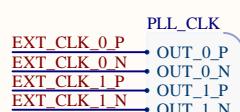
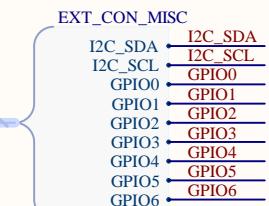
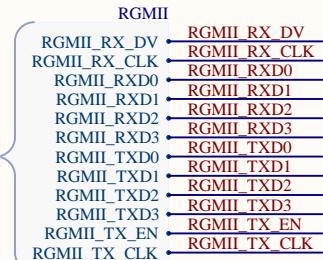
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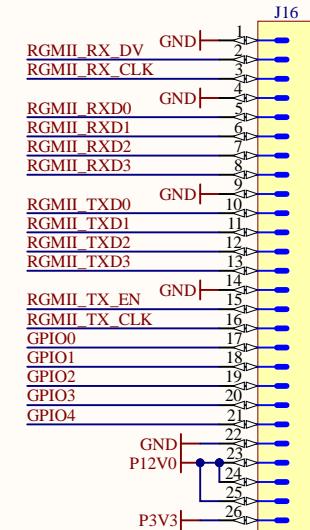
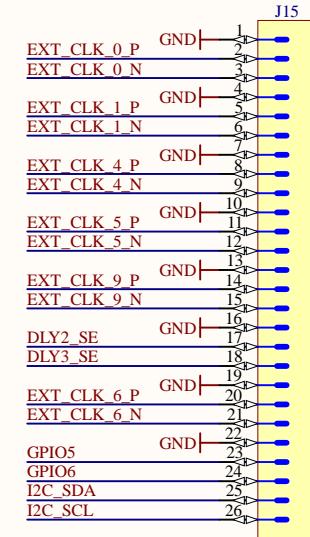
C



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PLL\_CLK\_EXT → SPEC-A7-TOP[1C], Clocks\_PLL[3A]



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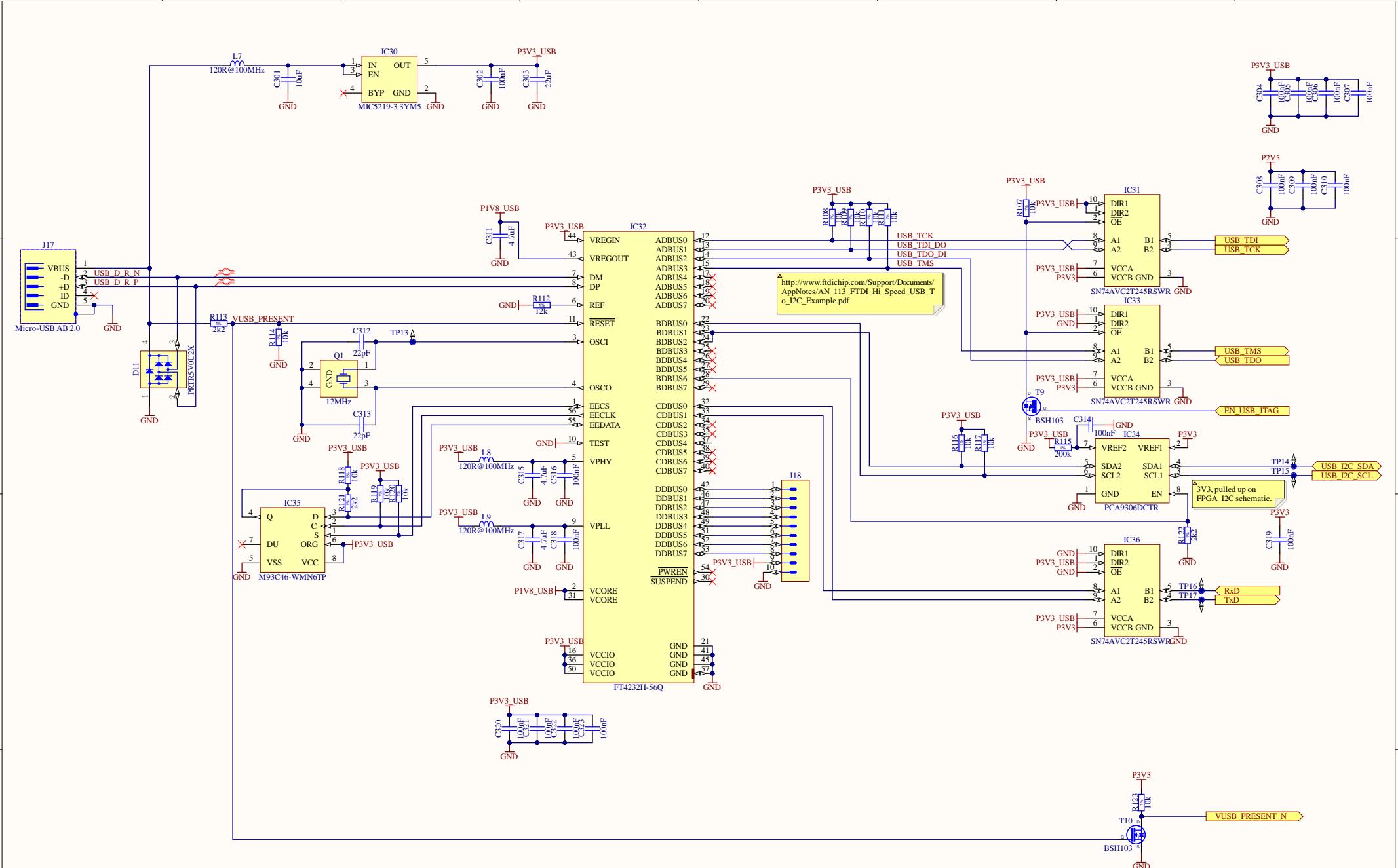
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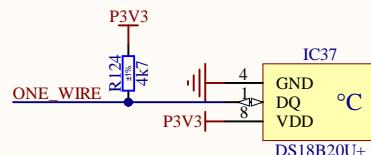
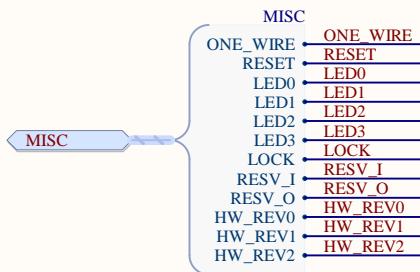
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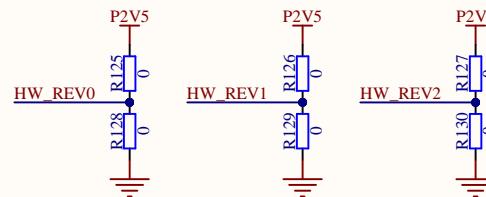


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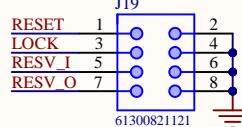
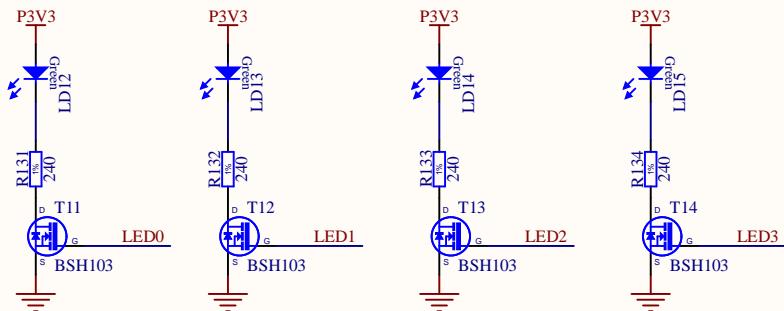
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Title		
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A4		
Date: 5.09.2024	Sheet of	
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