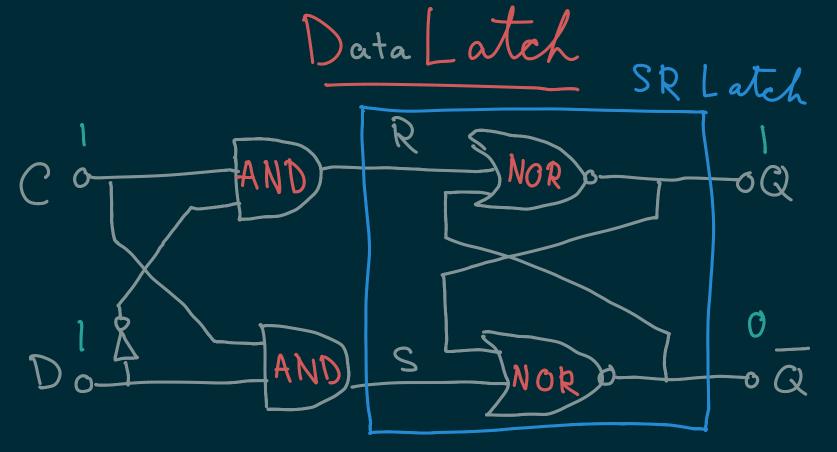


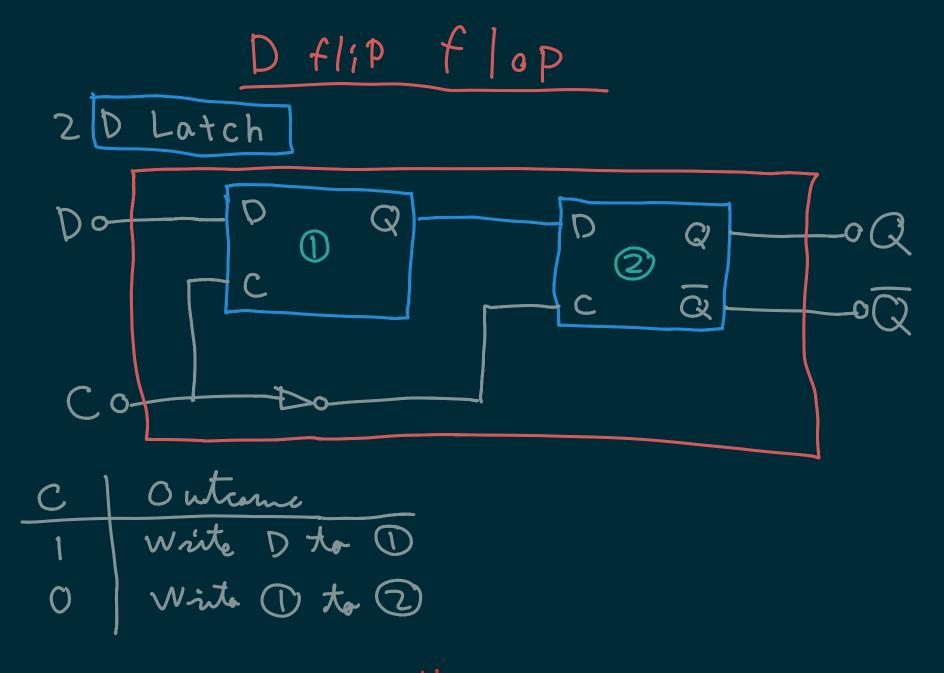
S	R	Owlant
0	0	No change
١	0	Q = 1
0	١	Q=0
1	1	Not allowed
		00000



Impossible for R and S to both be 1

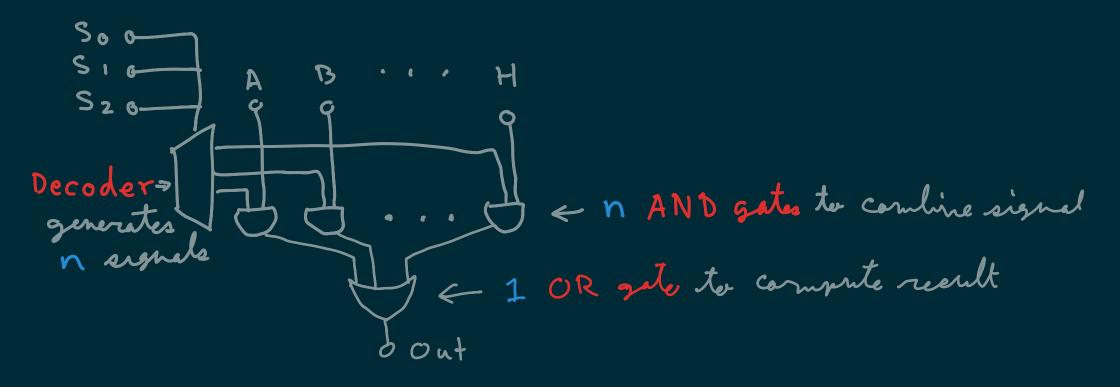
<u>C</u>	D	Outcome
0	Any	Hold
1 /	1	Q = 1
1 (0	Q = Q

When C is 1, Q has same value as D



M ultiplexor

Given n signals, need log₂n selector input 3 - 8 Decader:



1-bit Half Adder